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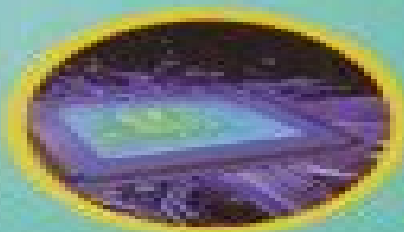
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verification Case studies illustrating real world applications detailed explanations of complex algorithms and self explaining illustrations add depth to the presentation Comprehensively covering all techniques related to the hardware software design and verification of reconfigurable systems this book provides a single source for information that otherwise would have been dispersed among the literature making it very difficult to search compare and select the technique most suitable The authors do it all for you making it easy to find the techniques that fit your system requirements without having to surf the net or digital libraries to find the candidate techniques and compare them yourself

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real time processing are given Coverage of standard and emerging DSP architectures enable the reader to make an informed selection when undertaking their own designs Presents readers with the elementary building blocks for the design of digital hardware systems and processor networks Provides a unique evaluation of standard DSP architectures whilst providing up to date information on the latest architectures including the TI 55x and TigerSharc chip families and the Virtex FPGA field programmable gate array Introduces the concepts and methodologies for describing and designing hardware VHDL is presented and used to illustrate the design of a simple processor A practical overview of hardware software codesign with design techniques and considerations illustrated with examples of real world designs Fundamental reading for graduate and senior undergraduate students of computer and electronic engineering and Practicing engineers developing DSP applications

Leading Edge Computer Science Research Susan Shannon,2006 The books in this series present leading edge research in the field of computer research technology and applications Each contribution has been carefully selected for inclusion based on the significance of the research to the field Summaries of all chapters are gathered at the beginning of the book and an in depth index is presented to facilitate access Computer Science - Theory and Applications Anna Frid,Andrei S.

Morozov,Andrey Rybalchenko,Klaus W. Wagner,2009-07-28 This book constitutes the refereed proceedings of the Fourth International Computer Science Symposium in Russia CSR 2009 held in Novosibirsk Russia August 18 23 2009 The 29 revised papers presented together with 4 invited papers were carefully reviewed and selected from 66 submissions All major areas in computer science are addressed The theory track deals with algorithms protocols and data structures complexity and cryptography formal languages automata and their applications to computer science computational models and concepts proof theory and applications of logic to computer science **Hands on Data Structures & Algorithms 1500+ MCQ**

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Computer System Architecture P. V. S. RAO, 2008-12-30 Intended as a text for undergraduate and postgraduate students of engineering in Computer Science and Engineering Information Technology and students pursuing courses in computer applications BCA MCA and computer science B Sc M Sc this state of the art study acquaints the students with concepts and implementations in computer architectures Though a new title it is a completely reorganized thoroughly revised and fully updated version of the author s earlier book Perspectives in Computer Architecture The text begins with a brief account of the very early history of computers and describes the von Neumann IAS type of computers then it goes on to give a brief introduction to the subsequent advances in computer systems covering device technologies operational aspects system organization and applications This is followed by an analysis of the advances and innovations that have taken place in these areas Advanced concepts such as look ahead pipelining RISC architectures and multi programming are fully analyzed The text concludes with a discussion on such topical subjects as computer networks microprocessors and microcomputers microprocessor families Intel Pentium series and newer high power processors

HALLMARKS OF THE BOOK The text fully reflects Professor P V S Rao s long experience as an eminent academic and his professional experience as an adviser to leading telecommunications software companies Gives a systematic account of the evolution of computers Provides a large number of exercises to drill the students in self study The five Appendices at the end of the text cover the basic concepts to enable the students to have a better understanding of the subject Besides students practising engineers should also find this book to be of immense value to them

COMBINATORICS AND GRAPH THEORY SARKAR, BIKASH KANTI, CHAKRABORTY, SWAPAN KUMAR, 2016-06-17 Combinatorics and Graph Theory is designed as a textbook for undergraduate students of computer science and engineering and postgraduate students of computer applications The book seeks to introduce students to the mathematical concepts needed to develop abstract thinking and problem solving important prerequisites for the study of computer science The book provides an exhaustive coverage of various concepts and remarkable introduction of several topics of combinatorics and graph theory The book presents an informative exposure for beginners and acts as a reference for advanced students It highlights comprehensive and rigorous views of combinatorics and graphs The text shows simplicity and step by step concepts throughout and is profusely illustrated with diagrams The real world applications corresponding to the topics are appropriately highlighted The chapters have also been interspersed throughout with numerous interesting and instructional notes Written in a lucid style the book helps students apply the mathematical tools to computer related concepts and consists of around 600 worked out examples which motivate students as a self learning mode

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