

GATE 2011 - Answer Key - Paper : XL

Paper	Section	Question no.	Key
XL	GA	1	D
XL	GA	2	D
XL	GA	3	B
XL	GA	4	C
XL	GA	5	A
XL	GA	6	A
XL	GA	7	C
XL	GA	8	D
XL	GA	9	C
XL	GA	10	B
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XL	H	2	C
XL	H	3	C
XL	H	4	A
XL	H	5	C
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XL	H	7	A
XL	H	8	D
XL	H	9	A
XL	H	10	D
XL	H	11	A
XL	H	12	D
XL	H	13	B
XL	H	14	C
XL	H	15	B
XL	I	1	D
XL	I	2	A
XL	I	3	B
XL	I	4	A
XL	I	5	C
XL	I	6	B
XL	I	7	A
XL	I	8	D
XL	I	9	C
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XL	I	11	A
XL	I	12	B
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XL	I	14	B
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XL	I	16	C
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XL	I	19	D
XL	I	20	C

Paper	Section	Question no.	Key
XL	J	1	C
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XL	J	3	C
XL	J	4	C
XL	J	5	A
XL	J	6	D
XL	J	7	A
XL	J	8	C
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XL	K	15	B
XL	K	16	B
XL	K	17	D
XL	K	18	A
XL	K	19	D
XL	K	20	A

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to learn and understand the need for and impact of advanced cybersecurity and forensics and its implementation with multiple smart computational technologies This objective answers why and how cybersecurity and forensics have evolved as one of the most promising and widely accepted technologies globally and has widely accepted applications The second objective is to learn how to use advanced cybersecurity and forensics practices to answer computational problems where confidentiality integrity and availability are essential aspects to handle and answer This book is structured in such a way so that the field of study is relevant to each reader's major or interests It aims to help each reader see the relevance of cybersecurity and forensics to their career or interests This book intends to encourage researchers to develop novel theories to enrich their scholarly knowledge to achieve sustainable development and foster sustainability Readers will gain valuable knowledge and insights about smart computing technologies using this exciting book This book Includes detailed applications of cybersecurity and forensics for real life problems Addresses the challenges and solutions related to implementing cybersecurity in multiple domains of smart computational technologies Includes the latest trends and areas of research in cybersecurity and forensics Offers both quantitative and qualitative assessments of the topics Includes case studies that will be helpful for the researchers Prof Keshav Kaushik is Assistant Professor in the Department of Systemics School of Computer Science at the University of Petroleum and Energy Studies Dehradun India Dr Shubham Tayal is Assistant Professor at SR University Warangal India Dr Akashdeep Bhardwaj is Professor Cyber Security Digital Forensics at the University of Petroleum Energy Studies UPES Dehradun India Dr Manoj Kumar is Assistant Professor SG SoCS at the University of Petroleum and Energy Studies Dehradun India

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[Enabling the Internet of Things](#) Massimo Alioto, 2017-01-23 This book offers the first comprehensive view on integrated circuit and system design for the Internet of Things IoT and in particular for the tiny nodes at its edge The authors provide a fresh perspective on how the IoT will evolve based on recent and foreseeable trends in the semiconductor industry highlighting the key challenges as well as the opportunities for circuit and system innovation to address them This book describes what the IoT really means from the design point of view and how the constraints imposed by applications translate into integrated circuit requirements and design guidelines Chapter contributions equally come from industry and academia After providing a system perspective on IoT nodes this book focuses on state of the art design techniques for IoT applications encompassing the fundamental sub systems encountered in Systems on Chip for IoT ultra low power digital architectures and circuits low and zero leakage memories including emerging

technologies circuits for hardware security and authentication System on Chip design methodologies on chip power management and energy harvesting ultra low power analog interfaces and analog digital conversion short range radios miniaturized battery technologies packaging and assembly of IoT integrated systems on silicon and non silicon substrates As a common thread all chapters conclude with a prospective view on the foreseeable evolution of the related technologies for IoT The concepts developed throughout the book are exemplified by two IoT node system demonstrations from industry The unique balance between breadth and depth of this book enables expert readers quickly to develop an understanding of the specific challenges and state of the art solutions for IoT as well as their evolution in the foreseeable future provides non experts with a comprehensive introduction to integrated circuit design for IoT and serves as an excellent starting point for further learning thanks to the broad coverage of topics and selected references makes it very well suited for practicing engineers and scientists working in the hardware and chip design for IoT and as textbook for senior undergraduate graduate and postgraduate students familiar with analog and digital circuits

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Dharma Delight Rodney Alan Greenblat,2016-05-10 In Dharma Delight abstract artist and Zen practitioner Rodney Greenblat uses lighthearted narrative and vivid pop art paintings to celebrate the joys of living life from the inside out Part graphic guide part personal testimony part art book Dharma Delight illustrates how seeking the path of compassion and acceptance can be as zany and exuberant as it is profound It is a happy exploration of Buddhist Enlightenment what it is where to seek it and how to recognize the perfection in ourselves A great option for Zen beginners and experienced practitioners alike sutras teachings Bodhisattvas enlightened beings and jataka tales parables are presented in a way that s simple upbeat and fun to read The original paintings some new some already known on the New York art scene and elsewhere are an imaginative and affirming mind s eye view of Buddhist teaching Together the words and illustrations are a warm and cheerful invitation to newcomers and a cool splash of refreshment to any traveler on the road to enlightenment

Toward Quantum FinFET Weihua Han,Zhiming M. Wang,2013-11-23 This book reviews a range of quantum phenomena in novel nanoscale transistors called FinFETs including quantized conductance of 1D transport single electron effect tunneling transport etc The goal is to create a fundamental bridge between quantum FinFET and nanotechnology to stimulate readers interest in developing new types of semiconductor technology Although the rapid development of micro nano fabrication is driving the MOSFET downscaling trend that is evolving from planar channel to nonplanar FinFET silicon based CMOS technology is expected to face fundamental limits in the near future Therefore new types of nanoscale devices are being investigated aggressively to take

advantage of the quantum effect in carrier transport The quantum confinement effect of FinFET at room temperatures was reported following the breakthrough to sub 10nm scale technology in silicon nanowires With chapters written by leading scientists throughout the world Toward Quantum FinFET provides a comprehensive introduction to the field as well as a platform for knowledge sharing and dissemination of the latest advances As a roadmap to guide further research in an area of increasing importance for the future development of materials science nanofabrication technology and nano electronic devices the book can be recommended for Physics Electrical Engineering and Materials Science departments and as a reference on micro nano electronic science and device design Offers comprehensive coverage of novel nanoscale transistors with quantum confinement effect Provides the keys to understanding the emerging area of the quantum FinFET Written by leading experts in each research area Describes a key enabling technology for research and development of nanofabrication and nanoelectronic devices

High Mobility Materials for CMOS Applications Nadine Collaert, 2018-06-29 High Mobility Materials for CMOS Applications provides a comprehensive overview of recent developments in the field of Si Ge and III V materials and their integration on Si The book covers material growth and integration on Si going all the way from device to circuit design While the book s focus is on digital applications a number of chapters also address the use of III V for RF and analog applications and in optoelectronics With CMOS technology moving to the 10nm node and beyond however severe concerns with power dissipation and performance are arising hence the need for this timely work on the advantages and challenges of the technology Addresses each of the challenges of utilizing high mobility materials for CMOS applications presenting possible solutions and the latest innovations Covers the latest advances in research on heterogeneous integration gate stack device design and scalability Provides a broad overview of the topic from materials integration to circuits

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Electronics & Communication Engineering Vol.-2 YCT Expert Team , All India State PSC AE PSU Electronics Communication Engineering Vol 2 Chapter wise Solved Papers *Fillers and Reinforcements for Advanced Nanocomposites* Yu Dong, Rehan

Umer, Alan Kin Tak Lau, 2015-07-02 Fillers and Reinforcements for Advanced Nanocomposites reviews cutting edge state of the art research on the effective use of nanoscaled fillers and reinforcements to enhance the performance of advanced nanocomposites both in industrial and manufacturing applications It covers a broad range of topics such as nanocelluloses nanotubes nanoplatelets and nanoparticles as well as their extensive applications The chapters provide detailed information on how fillers and reinforcements are used in the fabrication synthesis and characterization of advanced nanocomposites to achieve extraordinary performance of new materials and significant enhancements in their mechanical thermal structural and multi functional properties It also highlights new technologies for the fabrication of advanced nanocomposites using innovative electrospinning techniques Covers topics such as nanocelluloses nanotubes nanoplatelets and nanoparticles as well as their extensive applications Discusses the latest research on the effective use of nanoscaled fillers and reinforcements to enhance the performance of advanced nanocomposites Explains how fillers and reinforcements are used in the fabrication synthesis and characterization of advanced nanocomposites

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