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Robert Oshana



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Formal

Modeling and Analysis of Timed Systems Jean-Francois Raskin, 2007-09-20 This book constitutes the refereed proceedings of the 5th International Conference on Formal Modeling and Analysis of Timed Systems FORMATS 2007 It covers work on foundations and semantics of timed systems examines techniques algorithms data structures and software tools for analyzing timed systems and resolving temporal constraints and details applications like real time software hardware circuits and problems of scheduling in manufacturing and telecommunication

Rootkits and Bootkits Alex Matrosov, Eugene Rodionov, Sergey Bratus, 2019-05-07 Rootkits and Bootkits will teach you how to understand and counter sophisticated advanced threats buried deep in a machine's boot process or UEFI firmware With the aid of numerous case studies and professional research from three of the world's leading security experts you'll trace malware development over time from rootkits like TDL3 to present day UEFI implants and examine how they infect a system persist through reboot and evade security software As you inspect and dissect real malware you'll learn How Windows boots including 32 bit 64 bit and UEFI mode and where to find vulnerabilities The details of boot process security mechanisms like Secure Boot including an overview of Virtual Secure Mode VSM and Device Guard Reverse engineering and forensic techniques for analyzing real malware including bootkits like Rovnix Carberp Gapz TDL4 and the infamous rootkits TDL3 and Festi How to perform static and dynamic analysis using emulation and tools like Bochs and IDA Pro How to better understand the delivery stage of threats against BIOS and UEFI firmware in order to create detection capabilities How to use virtualization tools like VMware Workstation to reverse engineer bootkits and the Intel Chipsec tool to dig into forensic analysis Cybercrime syndicates and malicious actors will continue to write ever more persistent and covert attacks but the game is not lost Explore the cutting edge of malware analysis with Rootkits and Bootkits Covers boot processes for Windows 32 bit and 64 bit operating systems

InfoWorld, 1994-11-14 InfoWorld is targeted to Senior IT professionals Content is segmented into Channels and Topic Centers InfoWorld also celebrates people companies and projects

Silicon Systems For Wireless Lan Zoran Stamenkovic, Gildas Leger, Alberto Bosio, 2020-11-27 Today's integrated silicon circuits and systems for wireless communications are of a huge complexity This unique compendium covers all the steps from the system level to the transistor level necessary to design model verify implement and test a silicon system It bridges the gap between the system world and the transistor world between communication system circuit device and test engineers It is extremely important nowadays and will be more important in the future for communication system and circuit engineers to understand the physical implications of system and circuit solutions based on hardware software co design as well as for device and test engineers to cope with the system and circuit requirements in terms of power speed and data throughput Related Links

Formal Methods and Software Engineering Karin Breitman, Ana Cavalcanti, 2009-11-18 Formal methods for development of computer systems have been extensively studied over the years A range of semantic theories specification languages design techniques and verification methods and tools have been developed and applied to the construction of programs used in

critical applications The challenge now is to scale up formal methods and integrate them into engineering development processes for the correct and efficient construction and maintenance of computer systems in general This requires us to improve the state of the art on approaches and techniques for integration of formal methods into industrial engineering practice including new and emerging practice The now long established series of International Conferences on Formal Engineering Methods brings together those interested in the application of formal engineering methods to computer systems Researchers and practitioners from industry academia and government are encouraged to attend and to help advance the state of the art This volume contains the papers presented at ICFEM 2009 the 11th International Conference on Formal Engineering Methods held during December 9-11 in Rio de Janeiro Brazil

Information Security Policies and Actions in Modern Integrated Systems Mariagrazia Fugini, Carlo Bellettini, 2004-01-01 This work discusses research in theoretical and practical aspects of security in distributed systems in particular in information systems and related security tools Topics include XML based management systems security of multimedia data and technology and use of smart cards

Debugging Linux Systems (Digital Short Cut) Sreekrishnan Venkateswaran, 2009-11-03 Debugging Linux Systems discusses the main tools available today to debug 2.6 Linux Kernels We start by exploring the seemingly esoteric operations of the Kernel Debugger KDB Kernel GNU DeBugger KGDB the plain GNU DeBugger GDB and JTAG debuggers We then investigate Kernel Probes a feature that lets you intrude into a kernel function and extract debug information or apply a medicated patch Analyzing a crash dump can yield clues for postmortem analysis of kernel crashes or hangs so we take a look at Kdump a serviceability tool that collects a system dump after spawning a new kernel Profiling points you to code regions that burn more CPU cycles so we learn to use the OProfile kernel profiler and the gprof application profiler to sense the presence of code bottlenecks Because tracing provides insight into behavioral problems that manifest during interactions between different code modules we delve into the Linux Trace Toolkit a system designed for high volume trace capture The section Debugging Embedded Linux takes a tour of the I/O interfaces commonly found on embedded hardware such as flash memory serial port PCMCIA Secure Digital media USB RTC audio video touch screen and Bluetooth and provides pointers to debug the associated device drivers We also pick up some board level debugging skills with the help of a case study The section Debugging Network Throughput takes you through some device driver design issues and protocol implementation characteristics that can affect the horsepower of your network interface card We end the shortcut by examining several options available in the kernel configuration menu that can emit valuable debug information

VLSI-SoC: Technology Advancement on SoC Design Victor Grimblatt, Chip Hong Chang, Ricardo Reis, Anupam Chattopadhyay, Andrea Calimera, 2022-09-28 This book contains extended and revised versions of the best papers presented at the 29th IFIP WG 10.5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2021 held in Singapore in October 2021 The 12 full papers included in this volume were carefully reviewed and selected from the 44 papers out of 75 submissions presented

at the conference The papers discuss the latest academic and industrial results and developments as well as future trends in the field of System on Chip SoC design considering the challenges of nano scale state of the art and emerging manufacturing technologies In particular they address cutting edge research fields like low power design of RF analog and mixed signal circuits EDA tools for the synthesis and verification of heterogenous SoCs accelerators for cryptography and deep learning and on chip Interconnection system reliability and testing and integration of 3D ICs The conference was held virtually

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Ten Laws for Security Eric Diehl, 2016-11-16 In this book the author presents ten key laws governing information security He addresses topics such as attacks vulnerabilities threats designing security identifying key IP assets authentication and social engineering The informal style draws on his experience in the area of video protection and DRM while the text is supplemented with introductions to the core formal technical ideas It will be of interest to professionals and researchers engaged with information security

Knowledge-Based and Intelligent Information and Engineering Systems, Part II Andreas Koenig, Andreas Dengel, Knut Hinkelmann, Koichi Kise, Robert J. Howlett, Lakhmi C. Jain, 2011-09-15 The four volume set LNAI 6881 LNAI 6884 constitutes the refereed proceedings of the 15th International Conference on Knowledge Based Intelligent Information and Engineering Systems KES 2011 held in Kaiserslautern Germany in September 2011 Part 2 The total of 244 high quality papers presented were carefully reviewed

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Progress in Pattern Recognition, Image Analysis, Computer Vision, and Applications Luis Alvarez, Marta Mejail, Luis Gomez, Julio Jacobo, 2012-08-11 This book constitutes the refereed proceedings of the 17th Iberoamerican Congress on Pattern Recognition CIARP 2012 held in Buenos Aires Argentina in September 2012 The 109 papers presented among them two tutorials and four keynotes were carefully reviewed and selected from various submissions The papers are organized in topical sections on face and iris detection and recognition clustering fuzzy methods human actions and gestures graphs image processing and analysis shape and texture learning mining and neural networks medical images robotics stereo vision and real time remote sensing signal processing speech and handwriting analysis statistical pattern recognition theoretical pattern recognition and video analysis *Proceedings*, 2002

ISTFA 2018: Proceedings from the 44th International Symposium for Testing and Failure Analysis ASM International, 2018-12-01 The International Symposium for Testing and Failure Analysis ISTFA 2018 is co located with the International Test Conference ITC 2018 October 28 to November 1 in Phoenix Arizona USA at the Phoenix Convention Center The theme for the November 2018 conference is Failures Worth Analyzing While technology advances fast and the market demands the latest and the greatest successful companies strive to stay competitive and remain profitable

Software Engineering for Embedded Systems Robert Oshana, 2013-04-01 This Expert Guide gives you the techniques and technologies in software engineering to optimally design and implement your embedded system Written by experts with a solutions focus this encyclopedic reference gives you an indispensable aid to tackling the day to day problems when using software engineering methods to develop your embedded systems With this book you will learn The principles of good architecture for an embedded system Design practices to help make your embedded project successful Details on principles that are often a part of embedded systems including digital signal processing safety critical principles and development processes Techniques for setting up a performance engineering strategy for your embedded system software How to develop user interfaces for embedded systems Strategies for testing and deploying your embedded system and ensuring quality development processes Practical techniques for optimizing embedded software for performance memory and power Advanced guidelines for developing multicore software for embedded systems How to develop embedded software for networking storage and automotive segments How to manage the embedded development process Includes contributions from Frank Schirrmeister Shelly Gretlein Bruce Douglass Erich Styger Gary Stringham Jean Labrosse Jim Trudeau Mike Brogioli Mark Pitchford Catalin Dan Udma Markus Levy Pete Wilson Whit Waldo

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