POKHARA UNIVERSITY

: 2023 Level: Bachelor Semester: Fall Year Full Marks:100 Programme: BCA Pass Marks: 45 Course: Digital Logic System (New) :3hrs. Time

Candidates are required to give their answers in their own words as far as practicable.

The figures in the margin indicate full marks.

Attempt all the questions.

10	a)_	Perform the following conversions:	8
5		i. (ElB.AC5) ₁₆ =(?) ₈ ii. (237) ₈ =(?) ₂	
		iii. (100110101) grey =(?) ₂ iv. (1110) ₂ =(?) ₁₀	
	b)-	Show NAND and NOR gates as universal gates.	7
2.	a)	What is the dual of a function. Explain involution law.	7
	b)-	Simplify the following Boolean function with don't care condition.	8
		$F(A, B, C,D)=\sum m(0,1,3,4,5,6,7,11)$	
		$d(A, B, C,D)=\sum m(8,10,12,15)$	
		Draw the logic circuit using NOR gates only.	
3.	a)-	Implement a circuit full adder.	8
-0	p)-	Convert Excess-3 to BCD code and use K-map for circuit implementation.	7
£4?)	a)-	What is JK flipflop? Explain its operation with the help of logic	8
	574000	circuit and truth table. Explain state diagram and state table. Explain it with example.	7
	b)-	Explain state diagram and state dote. Explain it was stated	8
5.	a)-	Explain PISO shift register. Design a 3-bit synchronous down ripple counter.	7
	p)-	Design a 3-bit synchronous down ripple counter.	8
6	a).	What is status register? Explain about RAM.	7
	b)	Draw the circuit and explain 4-bit ALU.	2×5
7	Writ	e short notes on: (Any two)	200
	a)-	Gray Code	
	A 100 PM		

Postulates of boolean algebra

b)

First Sem Digital Logic Fundamental Question Paper

Robin Munkvold and Line Kolås

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