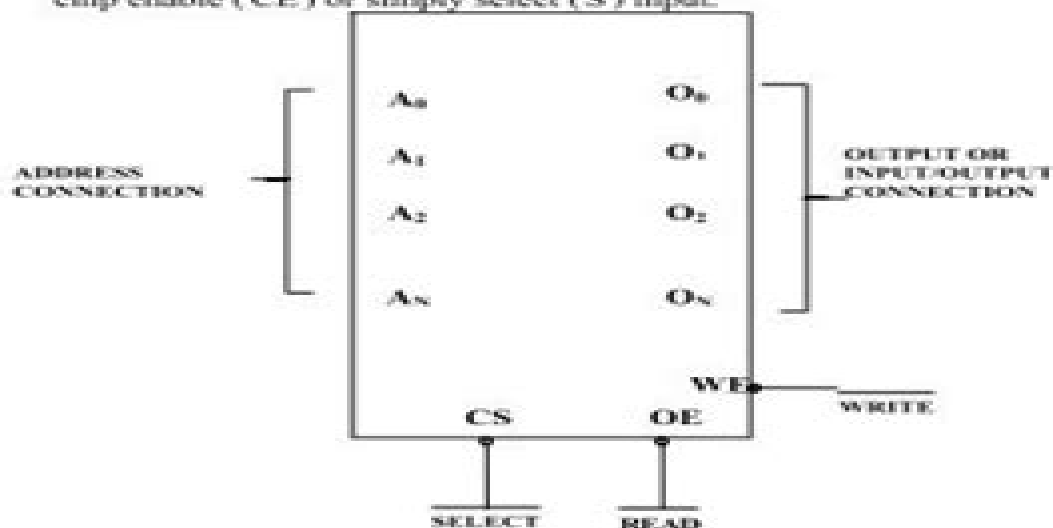


## Module 3

## Learning unit 8: Interface

- We have four common types of memory:
- Read only memory (ROM)
- Flash memory (EEPROM)
- Static Random access memory (SARAM)
- Dynamic Random access memory (DRAM).
- Pin connections common to all memory devices are: The address input, data output or input/outputs, selection input and control input used to select a read or write operation.
- **Address connections:** All memory devices have address inputs that select a memory location within the memory device. Address inputs are labeled from  $A_0$  to  $A_n$ .
- **Data connections:** All memory devices have a set of data outputs or input/outputs. Today many of them have bi-directional common I/O pins.
- **Selection connections:** Each memory device has an input, that selects or enables the memory device. This kind of input is most often called a chip select ( $\overline{CS}$ ), chip enable ( $\overline{CE}$ ) or simply select ( $\overline{S}$ ) input.



#### MEMORY COMPONENT ILLUSTRATING THE ADDRESS, DATA AND CONTROL CONNECTIONS

- RAM memory generally has at least one  $\overline{CS}$  or  $\overline{S}$  input and ROM at least one  $\overline{CE}$ .
- If the  $\overline{CE}$ ,  $\overline{CS}$ ,  $\overline{S}$  input is active the memory device perform the read or write.

# Lecture Notes On 8086 Microprocessor By Nptel

**Tobias Bleicker**



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## **Table of Contents Lecture Notes On 8086 Microprocessor By Nptel**

1. Understanding the eBook Lecture Notes On 8086 Microprocessor By Nptel
  - The Rise of Digital Reading Lecture Notes On 8086 Microprocessor By Nptel
  - Advantages of eBooks Over Traditional Books
2. Identifying Lecture Notes On 8086 Microprocessor By Nptel
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Lecture Notes On 8086 Microprocessor By Nptel
  - User-Friendly Interface

4. Exploring eBook Recommendations from Lecture Notes On 8086 Microprocessor By Nptel
  - Personalized Recommendations
  - Lecture Notes On 8086 Microprocessor By Nptel User Reviews and Ratings
  - Lecture Notes On 8086 Microprocessor By Nptel and Bestseller Lists
5. Accessing Lecture Notes On 8086 Microprocessor By Nptel Free and Paid eBooks
  - Lecture Notes On 8086 Microprocessor By Nptel Public Domain eBooks
  - Lecture Notes On 8086 Microprocessor By Nptel eBook Subscription Services
  - Lecture Notes On 8086 Microprocessor By Nptel Budget-Friendly Options
6. Navigating Lecture Notes On 8086 Microprocessor By Nptel eBook Formats
  - ePub, PDF, MOBI, and More
  - Lecture Notes On 8086 Microprocessor By Nptel Compatibility with Devices
  - Lecture Notes On 8086 Microprocessor By Nptel Enhanced eBook Features
7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Lecture Notes On 8086 Microprocessor By Nptel
  - Highlighting and Note-Taking Lecture Notes On 8086 Microprocessor By Nptel
  - Interactive Elements Lecture Notes On 8086 Microprocessor By Nptel
8. Staying Engaged with Lecture Notes On 8086 Microprocessor By Nptel
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Lecture Notes On 8086 Microprocessor By Nptel
9. Balancing eBooks and Physical Books Lecture Notes On 8086 Microprocessor By Nptel
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Lecture Notes On 8086 Microprocessor By Nptel
10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
11. Cultivating a Reading Routine Lecture Notes On 8086 Microprocessor By Nptel
  - Setting Reading Goals Lecture Notes On 8086 Microprocessor By Nptel
  - Carving Out Dedicated Reading Time

12. Sourcing Reliable Information of Lecture Notes On 8086 Microprocessor By Nptel
  - Fact-Checking eBook Content of Lecture Notes On 8086 Microprocessor By Nptel
  - Distinguishing Credible Sources
13. Promoting Lifelong Learning
  - Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
14. Embracing eBook Trends
  - Integration of Multimedia Elements
  - Interactive and Gamified eBooks

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