

N.B. : (1) Question No.1 is Compulsory.

(2) Solve any four questions from the remaining.

(3) Assume suitable data wherever necessary and justify the same.

- | | | |
|----|--|----|
| 1. | (a) Draw the V-I characteristics of SCR with and without gate control and mark holding current and latching current? Explain the difference between holding current and latching current | 20 |
| | (b) Explain why cycle converters are suitable only for low frequency application? | |
| | (c) Explain the meaning of following instructions
a) LDA address b) MVI M, 12H | |
| | (d) Realize EX-OR gate by NAND gate | |
| | (e) Why RC triggering is preferred over Resistance triggering process? | |
| 2. | (a) Explain single phase full wave fully controlled bridge rectifier with the help of circuit diagram and waveforms. | 10 |
| | (b) Explain the UJT relaxation circuit to trigger SCR along with appropriate waveform. | 10 |
| 3. | (a) Explain with block diagram the armature control method for speed control of a DC shunt motor. | 10 |
| | (b) What is the difference between Natural and Forced commutation? Explain any two forced commutation techniques of SCR with waveforms. | 10 |
| 4. | (a) Explain any two circuits using SCR for the speed control of single phase induction motor. | 10 |
| | (b) Draw the circuit diagram and explain the working of Jones chopper | 10 |
| 5. | (a) Discuss the thyristorised over load and over voltage protection of DC Motor. | 10 |
| | (b) Explain important characteristics of OPAMP | 05 |
| | (c) Draw a neat schematic diagram for OP-AMP in inverting and non-inverting mode | 05 |
| 6. | (a) Which gates are called as Universal gates? Why?
Using K-map reduce following Boolean function and implement it using NAND gates
$f(A,B,C) = \sum m(0,1,3,4,5,6)$ | 10 |
| | (b) Explain the various intercepts of 8085 | 05 |
| | (c) Explain DeMorgan's theorem. | 05 |
| 7. | (a) Explain the architecture of 8085 Microprocessor with block diagram. | 10 |
| | (b) Explain 555 timer as astable multivibrator | 10 |

Industrial Electronics Technology Question Paper

Yanglv Ling



Industrial Electronics Technology Question Paper:

Mechanical Engineering, Industrial Electronics and Informatization B.L. Liu, 2013-02-13 Selected peer reviewed papers from the 2012 International Conference on Mechanical Engineering Industrial Electronics and Informatization MEIEI 2012 December 28 30 2012 Qinhuangdao Hebei China Innovative Algorithms and Techniques in Automation, Industrial Electronics and Telecommunications Tarek Sobh, Khaled Elleithy, Ausif Mahmood, Mohamed Karim, 2007-09-04 Innovative Algorithms and Techniques in Automation Industrial Electronics and Telecommunications includes a set of rigorously reviewed world class manuscripts addressing and detailing state of the art research projects in the areas of Industrial Electronics Technology not only the reviewing submissions and registration processes but also the actual conference Conference participants authors presenters and attendees only needed an internet connection and sound available on their computers in order to be able to contribute and participate in this international ground breaking conference The on line structure of this high quality event allowed academic professionals and industry participants to contribute work and attend world class technical presentations based on rigorously refereed submissions live without the need for investing significant travel funds or time out of the office Suffice to say that CISSE received submissions from more than 70 countries for whose researchers this opportunity presented a much more affordable dynamic and well planned event to attend and submit their work to versus a classic on the ground conference The CISSE conference audio room provided superb audio even over low speed internet connections the ability to display PowerPoint presentations and cross platform compatibility the conferencing software runs on Windows Mac and any other operating system that supports Java In addition the conferencing system allowed for an unlimited number of participants which in turn granted CISSE the opportunity to allow all participants to attend all presentations as opposed to limiting the number of available seats for each session Novel Algorithms and Techniques in Telecommunications, Automation and Industrial Electronics Tarek Sobh, Khaled Elleithy, Ausif Mahmood, Mohammad A. Karim, 2008-08-15 Novel Algorithms and Techniques in Telecommunications Automation and Industrial Electronics includes a set of rigorously reviewed world class manuscripts addressing and detailing state of the art research projects in the areas of Industrial Electronics Technology and Automation Telecommunications and Networking Novel Algorithms and Techniques in Telecommunications Automation and Industrial Electronics includes selected papers from the conference proceedings of the International Conference on Industrial Electronics Technology and Automation IETA 2007 and International Conference on Telecommunications and Networking TeNe 07 which were part of the International Joint Conferences on Computer Information and Systems Sciences and Engineering CISSE 2007 **The Industrial Electronics Handbook** J. David Irwin, 1997-05-09 From traditional topics that form the core of industrial electronics to new and emerging concepts and technologies The Industrial Electronics Handbook in a single volume has the field covered Nowhere else will you find so much information on so many major topics in the field For facts you need every day and for

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