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Intel X86 64 Manual

David Padua

Intel X86 64 Manual:

A Guide to Kernel Exploitation Enrico Perla,Massimiliano Oldani,2010-10-28 A Guide to Kernel Exploitation Attacking the Core discusses the theoretical techniques and approaches needed to develop reliable and effective kernel level exploits and applies them to different operating systems namely UNIX derivatives Mac OS X and Windows Concepts and tactics are presented categorically so that even when a specifically detailed vulnerability has been patched the foundational information provided will help hackers in writing a newer better attack or help pen testers auditors and the like develop a more concrete design and defensive structure The book is organized into four parts Part I introduces the kernel and sets out the theoretical basis on which to build the rest of the book Part II focuses on different operating systems and describes exploits for them that target various bug classes Part III on remote kernel exploitation analyzes the effects of the remote scenario and presents new techniques to target remote issues It includes a step by step analysis of the development of a reliable one shot remote exploit for a real vulnerabilitya bug affecting the SCTP subsystem found in the Linux kernel Finally Part IV wraps up the analysis on kernel exploitation and looks at what the future may hold Covers a range of operating system families UNIX derivatives Mac OS X Windows Details common scenarios such as generic memory corruption stack overflow heap overflow etc issues logical bugs and race conditions Delivers the reader from user land exploitation to the world of kernel land OS exploits attacks with a particular focus on the steps that lead to the creation of successful techniques in order to give to the reader something more than just a set of tricks **Intel 64 and IA-32 Architectures Software Developer's Manual** Intel Corporation,1997 The Shellcoder's Handbook Chris Anley,John Heasman,Felix Lindner,Gerardo Richarte,2011-02-16 This much anticipated revision written by the ultimate group of top security experts in the world features 40 percent new content on how to find security holes in any operating system or application New material addresses the many new exploitation techniques that have been discovered since the first edition including attacking unbreakable software packages such as McAfee s Entercept Mac OS X XP Office 2003 and Vista Also features the first ever published information on exploiting Cisco s IOS with content that has never before been explored The companion Web site features downloadable code files

Handbook of Computer Architecture Anupam Chattopadhyay,2024-12-20 This handbook presents the key topics in the area of computer architecture covering from the basic to the most advanced topics including software and hardware design methodologies It will provide readers with the most comprehensive updated reference information covering applications in single core processors multicore processors application specific processors reconfigurable architectures emerging computing architectures processor design and programming flows test and verification This information benefits the readers as a full and quick technical reference with a high level review of computer architecture technology detailed technical descriptions and the latest practical applications **Introduction to Computer Organization** Robert G. Plantz,2022-01-25 This hands on tutorial is a broad examination of how a modern computer works Classroom tested for over

a decade it gives readers a firm understanding of how computers do what they do covering essentials like data storage logic gates and transistors data types the CPU assembly and machine code Introduction to Computer Organization gives programmers a practical understanding of what happens in a computer when you execute your code Working from the ground up the book starts with fundamental concepts like memory organization digital circuit design and computer arithmetic It then uses C C to explore how familiar high level coding concepts like control flow input output and functions are implemented in assembly language The goal isn t to make you an assembly language programmer but to help you understand what happens behind the scenes when you run your programs Classroom tested for over a decade this book will also demystify topics like How data is encoded in memory How the operating system manages hardware resources with exceptions and interrupts How Boolean algebra is used to implement the circuits that process digital information How a CPU is structured and how it uses buses to execute a program stored in main memory How recursion is implemented in assembly and how it can be used to solve repetitive problems How program code gets transformed into machine code the computer understands You may never have to write x86 64 assembly language or design hardware yourself but knowing how the hardware and software works will make you a better more confident programmer   Practical Binary Analysis Dennis Andriesse,2018-12-11 Stop manually analyzing binary Practical Binary Analysis is the first book of its kind to present advanced binary analysis topics such as binary instrumentation dynamic taint analysis and symbolic execution in an accessible way As malware increasingly obfuscates itself and applies anti analysis techniques to thwart our analysis we need more sophisticated methods that allow us to raise that dark curtain designed to keep us out binary analysis can help The goal of all binary analysis is to determine and possibly modify the true properties of binary programs to understand what they really do rather than what we think they should do While reverse engineering and disassembly are critical first steps in many forms of binary analysis there is much more to be learned This hands on guide teaches you how to tackle the fascinating but challenging topics of binary analysis and instrumentation and helps you become proficient in an area typically only mastered by a small group of expert hackers It will take you from basic concepts to state of the art methods as you dig into topics like code injection disassembly dynamic taint analysis and binary instrumentation Written for security engineers hackers and those with a basic working knowledge of C C and x86 64 Practical Binary Analysis will teach you in depth how binary programs work and help you acquire the tools and techniques needed to gain more control and insight into binary programs Once you ve completed an introduction to basic binary formats you ll learn how to analyze binaries using techniques like the GNU Linux binary analysis toolchain disassembly and code injection You ll then go on to implement profiling tools with Pin and learn how to build your own dynamic taint analysis tools with libdft and symbolic execution tools using Triton You ll learn how to Parse ELF and PE binaries and build a binary loader with libbfd Use data flow analysis techniques like program tracing slicing and reaching definitions analysis to reason about runtime flow of your programs Modify ELF binaries with

techniques like parasitic code injection and hex editing Build custom disassembly tools with Capstone Use binary instrumentation to circumvent anti analysis tricks commonly used by malware Apply taint analysis to detect control hijacking and data leak attacks Use symbolic execution to build automatic exploitation tools With exercises at the end of each chapter to help solidify your skills you ll go from understanding basic assembly to performing some of the most sophisticated binary analysis and instrumentation Practical Binary Analysis gives you what you need to work effectively with binary programs and transform your knowledge from basic understanding to expert level proficiency   A Practical Guide to Fedora and Red Hat Enterprise Linux Mark G. Sobell,2014 A Practical Guide to Fedora and Red Hat Enterprise Linux takes the reader from beginner to advanced Mark Sobell teaches both the hows and the whys of Fedora and Red Hat Enterprise Linux to help readers reach the solution faster than ever Now fully updated for both Fedora Core 19 and Red Hat Enterprise Linux 7 this new edition walks readers through every essential feature and technique they ll need now and for years to come

Practical Reverse Engineering Bruce Dang,Alexandre Gazet,Elias Bachaalany,2014-02-03 Analyzing how hacks are done so as to stop them in the future Reverse engineering is the process of analyzing hardware or software and understanding it without having access to the source code or design documents Hackers are able to reverse engineer systems and exploit what they find with scary results Now the good guys can use the same tools to thwart these threats Practical Reverse Engineering goes under the hood of reverse engineering for security analysts security engineers and system programmers so they can learn how to use these same processes to stop hackers in their tracks The book covers x86 x64 and ARM the first book to cover all three Windows kernel mode code rootkits and drivers virtual machine protection techniques and much more Best of all it offers a systematic approach to the material with plenty of hands on exercises and real world examples Offers a systematic approach to understanding reverse engineering with hands on exercises and real world examples Covers x86 x64 and advanced RISC machine ARM architectures as well as deobfuscation and virtual machine protection techniques Provides special coverage of Windows kernel mode code rootkits drivers a topic not often covered elsewhere and explains how to analyze drivers step by step Demystifies topics that have a steep learning curve Includes a bonus chapter on reverse engineering tools Practical Reverse Engineering Using x86 x64 ARM Windows Kernel and Reversing Tools provides crucial up to date guidance for a broad range of IT professionals   **The Hacker's Guide to OS X** Alijohn Ghassemlouei,Robert Bathurst,Russ Rogers,2012-12-31 Written by two experienced penetration testers the material presented discusses the basics of the OS X environment and its vulnerabilities Including but limited to application porting virtualization utilization and offensive tactics at the kernel OS and wireless level This book provides a comprehensive in depth guide to exploiting and compromising the OS X platform while offering the necessary defense and countermeasure techniques that can be used to stop hackers As a resource to the reader the companion website will provide links from the authors commentary and updates Provides relevant information including some of the latest OS X threats Easily accessible to

those without any prior OS X experience Useful tips and strategies for exploiting and compromising OS X systems Includes discussion of defensive and countermeasure applications and how to use them Covers mobile IOS vulnerabilities **Digital Design and Computer Architecture, ARM Edition** Sarah Harris,David Harris,2015-04-09 Digital Design and Computer Architecture ARM Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor Combining an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor By the end of this book readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works Beginning with digital logic gates and progressing to the design of combinational and sequential circuits this book uses these fundamental building blocks as the basis for designing an ARM processor SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design The companion website includes a chapter on I O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor Features side by side examples of the two most prominent Hardware Description Languages HDLs SystemVerilog and VHDL which illustrate and compare the ways each can be used in the design of digital systems Includes examples throughout the text that enhance the reader s understanding and retention of key concepts and techniques The Companion website includes a chapter on I O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs Bluetooth radios and motors The Companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises **Leveraging Applications of Formal Methods, Verification, and Validation** Tiziana Margaria,Bernhard Steffen,2010-10-19 The two volume set LNCS 6415 and LNCS 6416 constitutes the refereed proceedings of the 4th International Symposium on Leveraging Applications of Formal Methods ISoLA 2010 held in Heraklion Crete Greece in October 2010 The 100 revised full papers presented were carefully revised and selected from numerous submissions and discuss issues related to the adoption and use of rigorous tools and methods for the specification analysis verification certification construction test and maintenance of systems The 46 papers of the first volume are organized in topical sections on new challenges in the development of critical embedded systems formal languages and methods for designing and verifying complex embedded systems worst case traversal time WCTT tools in scientific workflow composition emerging services and technologies for a converging telecommunications Web world in smart environments of the internet of things Web science model transformation and analysis for industrial scale validation

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Computer Organization and Design MIPS Edition David A. Patterson,John L. Hennessy,2020-11-24 Computer Organization and Design The Hardware Software Interface Sixth Edition the leading award winning textbook from Patterson and Hennessy used by more than 40 000 students per year continues to present the most comprehensive and readable introduction to this core computer science topic Improvements to this new release include new sections in each chapter on Domain Specific Architectures DSA and updates on all real world examples that keep it fresh and relevant for a new generation of students Covers parallelism in depth with examples and content highlighting parallel hardware and software topics Includes new sections in each chapter on Domain Specific Architectures DSA Discusses and highlights the Eight Great Ideas of computer architecture including Performance via Parallelism Performance via Pipelining Performance via Prediction Design for Moore s Law Hierarchy of Memories Abstraction to Simplify Design Make the Common Case Fast and Dependability via Redundancy   *Computer Organization and Design* David A. Patterson,John L. Hennessy,2012 Rev ed of Computer organization and design John L Hennessy David A Patterson 1998   **Encyclopedia of Parallel Computing** David Padua,2011-09-08 Containing over 300 entries in an A Z format the Encyclopedia of Parallel Computing provides easy intuitive access to relevant information for professionals and researchers seeking access to any aspect within the broad field of parallel computing Topics for this comprehensive reference were selected written and peer reviewed by an international pool of distinguished researchers in the field The Encyclopedia is broad in scope covering machine organization programming languages algorithms and applications Within each area concepts designs and specific implementations are presented The highly structured essays in this work comprise synonyms a definition and discussion of the topic bibliographies and links to related literature Extensive cross references to other entries within the Encyclopedia support efficient user friendly searchers for immediate access to useful information Key concepts presented in the Encyclopedia of Parallel Computing include laws and metrics specific numerical and non numerical algorithms asynchronous algorithms libraries of subroutines benchmark suites applications sequential consistency and cache coherency machine classes such as clusters shared memory multiprocessors special purpose machines and dataflow machines specific machines such as Cray supercomputers IBM s cell processor and Intel s multicore machines race detection and auto parallelization parallel programming languages synchronization primitives collective operations message passing libraries checkpointing and operating systems Topics covered Speedup Efficiency Isoefficiency Redundancy Amdahls law Computer Architecture Concepts Parallel Machine Designs Benmarks Parallel Programming concepts design Algorithms Parallel applications This

authoritative reference will be published in two formats print and online The online edition features hyperlinks to cross references and to additional significant research Related Subjects supercomputing high performance computing distributed computing **HDF5 Data Architecture and Programming Guide** Richard Johnson,2025-05-30 HDF5 Data Architecture and Programming Guide The HDF5 Data Architecture and Programming Guide offers a definitive in depth exploration of the HDF5 data format designed for engineers scientists and developers tasked with managing and optimizing large complex and heterogeneous datasets Beginning with foundational concepts the guide delves into the origins and rationale behind HDF5 its logical and physical storage models and the core abstractions groups datasets and attributes that enable robust scalable data modeling Readers are introduced to HDF5 s extensible type system advanced referencing mechanisms and the semantics underpinning safe concurrent file access Building on this foundation the book presents actionable strategies for hierarchical namespace organization metadata and attribute schema management and sophisticated storage optimization techniques Coverage extends to chunking compression and filter customization equipping practitioners with the tools necessary to tailor storage layouts for both performance and flexibility Specialized chapters explore high performance parallel I O in distributed environments memory management and efficient data transfer and the guarantees and tools HDF5 offers for data integrity validation and security including encryption and auditing for rigorous scientific workflows A practical and forward looking resource the guide also addresses advanced API usage across multiple languages including C C Python Java and Fortran seamless integration with distributed and cloud storage and interoperability with leading data formats and scientific libraries Best practices and anti patterns are illustrated through architectural blueprints and real world case studies while future directions and evolving standards are discussed to prepare readers for new trends in large scale data management Whether architecting scalable storage systems or harnessing advanced features in production this guide provides comprehensive insight for leveraging HDF5 s full potential Information Security Javier Lopez,Chris J. Mitchell,2015-08-27 This book constitutes the refereed proceedings of the 18th International Conference on Information Security ISC 2015 held in Trondheim Norway in September 2015 The 30 revised full papers presented were carefully reviewed and selected from 103 submissions The papers cover a wide range of topics in the area of cryptography and cryptanalysis and are organized in the following topical sections signatures system and software security block ciphers protocols network and cloud security encryption and fundamentals PUFs and implementation security and key generation biometrics and image security

Digital Design and Computer Architecture, RISC-V Edition Sarah Harris,David Harris,2021-07-12 The newest addition to the Harris and Harris family of Digital Design and Computer Architecture books this RISC V Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Combining an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of a processor By the end of this book readers will be able to build their own

RISC V microprocessor and will have a top to bottom understanding of how it works Beginning with digital logic gates and progressing to the design of combinational and sequential circuits this book uses these fundamental building blocks as the basis for designing a RISC V processor SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design The companion website includes a chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Gives students a full understanding of the RISC V instruction set architecture enabling them to build a RISC V processor and program the RISC V processor in hardware simulation software simulation and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single cycle multicycle and pipelined versions of the RISC V architecture Features a companion website with a bonus chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive problems   Optimization and Learning Bernabé Dorronsoro,Francisco Chicano,Gregoire Danoy,El-Ghazali Talbi,2023-05-26 This book constitutes the refereed proceedings of the 6th International Conference on Optimization and Learning OLA 2023 held in Malaga Spain during May 3 5 2023 The 32 full papers included in this book were carefully reviewed and selected from 78 submissions They were organized in topical sections as follows advanced optimization learning learning methods to enhance optimization tools optimization applied to learning methods and real world applications   Fedora 13 Installation Guide Fedora Documentation Project,2010-07 The official Fedora 13 Installation Guide covers installation of Fedora a Linux distribution built on free and open source software   **Proceedings of Eighth International Congress on Information and Communication Technology** Xin-She Yang,R. Simon Sherratt,Nilanjan Dey,Amit Joshi,2023-07-29 This book gathers selected high quality research papers presented at the Eighth International Congress on Information and Communication Technology held at Brunel University London on 20 23 February 2023 It discusses emerging topics pertaining to information and communication technology ICT for managerial applications e governance e agriculture e education and computing technologies the Internet of Things IoT and e mining Written by respected experts and researchers working on ICT the book offers a valuable asset for young researchers involved in advanced studies The work is presented in four volumes

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