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Reshaping Technical Communication Barbara Mirel,Rachel Spilka,2002-06 This collection offers a broad examination of technical communication as a discipline and a profession and provides insights on its future directions Intended for students scholars and practitioners in technical communication and education

Automotive Embedded Systems Handbook Nicolas Navet,Francoise Simonot-Lion,2017-12-19 A Clear Outline of Current Methods for Designing and Implementing Automotive Systems Highlighting requirements technologies and business models the Automotive Embedded Systems Handbook provides a comprehensive overview of existing and future automotive electronic systems It presents state of the art methodological and technical solutions in the areas of in vehicle architectures multipartner development processes software engineering methods embedded communications and safety and dependability assessment Divided into four parts the book begins with an introduction to the design constraints of automotive embedded systems It also examines AUTOSAR as the emerging de facto standard and looks at how key technologies such as sensors and wireless networks will facilitate the conception of partially and fully autonomous vehicles The next section focuses on networks and protocols including CAN LIN FlexRay and TTCAN The third part explores the design processes of electronic embedded systems along with new design methodologies such as the virtual platform The final section presents validation and verification techniques relating to safety issues Providing domain specific solutions to various technical challenges this handbook serves as a reliable complete and well documented source of information on automotive embedded systems

Handbook of Face Recognition Stan Z. Li,Anil K. Jain,2005-12-06 Although the history of computer aided face recognition stretches back to the 1960s automatic face recognition remains an unsolved problem and still offers a great challenge to computer vision and pattern recognition researchers This handbook is a comprehensive account of face recognition research and technology written by a group of leading international researchers Twelve chapters cover all the sub areas and major components for designing operational face recognition systems Background modern techniques recent results and challenges and future directions are considered The book is aimed at practitioners and professionals planning to work in face recognition or wanting to become familiar with the state of the art technology A comprehensive handbook by

leading research authorities on the concepts methods and algorithms for automated face detection and recognition Essential reference resource for researchers and professionals in biometric security computer vision and video image analysis *Fuel Economy Guide* ,2002 **Area Array Interconnection Handbook** Karl J. Puttlitz,Paul A. Totta,2012-12-06 Microelectronic packaging has been recognized as an important enabler for the solid state revolution in electronics which we have witnessed in the last third of the twentieth century Packaging has provided the necessary external wiring and interconnection capability for transistors and integrated circuits while they have gone through their own spectacular revolution from discrete device to gigascale integration At IBM we are proud to have created the initial simple concept of flip chip with solder bump connections at a time when a better way was needed to boost the reliability and improve the manufacturability of semiconductors The basic design which was chosen for SLT Solid Logic Technology in the 1960s was easily extended to integrated circuits in the 70s and VLSI in the 80s and 90s Three I O bumps have grown to 3000 with even more anticipated for the future The package families have evolved from thick film SLT to thin film metallized ceramic to co fired multi layer ceramic A later family of ceramics with matching expansivity to silicon and copper internal wiring was developed as a predecessor of the chip interconnection revolution in copper multilevel submicron wiring Powerful server packages have been developed in which the combined chip and package copper wiring exceeds a kilometer All of this was achieved with the constant objective of minimizing circuit delays through short efficient interconnects **Japan Company Handbook** ,1990

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