GATE - 1994

Electronics and Communication Engineering

1.1 The Laplace transform of a unit ramp function starting at t = a, is

(a)
$$\frac{1}{(s+a)^2}$$
 (b) $\frac{e^{-aa}}{(s+a)}$
(c) $\frac{e^{-aa}}{2}$ (d) $\frac{a}{a^2}$

- 1.2 The Fourice Series of a odd periodic function. contains only
 - (a) odd harmonics (b) even harmonics. (c) cosine terms (d) sine terms
- A series LCR circuit consisting of R = 10 Ω, $|X_{c}| = 20 \Omega$ and $|X_{c'}| = 20 \Omega$, is connected across an a , c. supply of 200 V rms. The rms voltage
- across the capacitor is (a) 200 Z - 90° V (b) 200 ∠ + 90° V (df) 400 ∠ - 90° V (c) 400 ∠ - 90° V
- 1.4 A ramp voltage, v(t) = 100 volts, is applied to an RC differentiating circuit with $R = 5k \Omega$ and C = 4µF. The maximum output voltage is (b) 2.0 volts (a) 0.2 volts
- (d) 50.0 violes (c) 10.0 volts 1.5 The 3 – dB bandwidth of a typical second-order system with the transfer function

$$\frac{\mathbf{C}(s)}{\mathbf{R}(s)} = \frac{\omega_n^2}{s^2 + 2 \times \omega_n + \omega_n^2}$$
 is given by

(a)
$$\omega_n = \sqrt{1 - 2 \xi^2}$$

(b) $\omega_n = \sqrt{1 - 2 \xi^2} + \sqrt{\xi^4 - \xi^2 + 1}$
(c) $\omega_n = \sqrt{1 - 2 \xi^2} + \sqrt{4 \xi^4 - 4 \xi^2 + 2}$
(d) $\omega_n = \sqrt{1 - 2 \xi^2} + \sqrt{4 \xi^4 - 4 \xi^2 + 2}$

- 1.6 If the open-loop transfer function is a ratio of a numerator polynomial of degree 'm' and a denominator polynomial of degree 'n', then the integer (n-m) represents the number of (a) breakaway points (b) unstable poles (c) separate root loci (d) asymtotes
- 1.7 A small concentration of minority carries is injected into a homogeneous semiconductor crystal at one point. An electric field of 10 V.cm is applied across the crystal and this moves the minority carriers a distance of 1 cm is 20 µ sec. The mobility (in cm2/volt. sec) will be 649 1,000 (b) 2,000
 - (c) 5,000 645 500,000

- 1.8 The threshold voltage of an n channel MOSFET can be increased by (a) increasing the channel dopant concentration
 - (a) reducing the channel dopant concentration
 - (c) reducing the gate-oxide thickness
 - (d) reducing the channel length
- 1.9 A class A transformer coupled, transistor power amplifier is required to deliver a power output of 10 watts. The maximum power rating of the transistor should not be less than 6a) 5 W (b) 10 W
- (c) 20 W 640, 40 W 1.10 Data can be changed from spatial code to temporal code and vice-versa by using
 - (ii) ADCs and DACs (b) shift-registers
 - (c) synchronous counters
 - (df) timmers
- 1.11 The output of a logic gate is '1' when all its inputs are at logic '0'. Then gate is either
 - (a) a NAND or an EX-OR gate
 - (b) a NOR or an EX-NOR gate
 - (c) an OR or an EX-NOR gate (d) an AND or an EX-OR gate
- 1.12 A PLA can be used
 - (a) as a microprocessor
 - (b) as a dynamic memory
 - (c) to realise a sequential logic
 - (d) to realise a combinational logic
- 1.13 A dynamic RAM consists of
 - (a) 6 transistors
 - (b) 2 transistors and 2 capacitors
 - (c) 1 transistor and 1 capacitor
 - (d) 2 capacitors only
- 1.14 $v(t) = 5 [\cos(106 \pi t) \sin(103 \pi t) \times \sin(106 \pi t)]$ represents
 - (a) DSB suppressed carrier signal
 - (b) AM signal
 - (c) SSB upper sideband signal
 - (d) Narrow band FM signal
- 1.15 Increased pulse-width in the flat-top sampling,
 - (a) attenuation of high frequencies in reproduction
 - (b) attenuation of low frequencies in reproduction
 - (c) greater aliasing errors in reproduction
 - (d) no harmful effects in reproduction

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