

EC 2404 - ELECTRONIC SYSTEM DESIGN LAB

1. REGULATION CHARACTERISTICS OF BUCK –BOOST CONVERTER
2. REGULATION CHARACTERISTICS OF FLYBACK CONVERTER
- 3 (i) DESIGN OF AM TRANSCEIVER
- 3 (ii) DESIGN OF FM TRANSCEIVER
4. DESIGN OF WIRELESS DATA MODEM
5. PCB LAYOUT DESIGN USING CAD
6. DESIGN OF DC VOLTAGE REGULATOR USING SCR
7. MICROCONTROLLER BASED SYSTEM DESIGN
8. DESIGN OF PROCESS CONTROL TIMER
9. DESIGN OF INSTRUMENTATION AMPLIFIER
10. DSP BASED DIGITAL FUNCTION GENERATOR
11. SIMULATION OF DC VOLTAGE REGULATOR USING SCR
12. SIMULATION OF AC VOLTAGE CONTROLLER USING SCR

Electronic System Design Lab Manual Ec2404

JL Elias



Electronic System Design Lab Manual Ec2404:

Electronic Design Text and Lab Manual Discovery Press, 2005-01-01 **Lab Manual for Electronic**

Communications Jeffrey S. Beasley, Mark E. Oliver, Jonathan D. Hymer, David Shores, Gary M. Miller, 2013-06-24 This is a student supplement associated with *Electronic Communications A System Approach 1 e* Jeffrey S Beasley Jonathan D Hymer Gary M Miller ISBN 0132988631 *Lab Manual for Electronic Devices, Global Edition* THOMAS L. FLOYD, 2018-06-19 This laboratory manual is carefully coordinated to the text *Electronic Devices Tenth edition Global edition* by Thomas L Floyd The seventeen experiments correspond to the chapters in the text except the first experiment references Chapters 1 and the first part of Chapter 2 All of the experiments are subdivided into two or three Parts With one exception Experiment 12 B the Parts for the all experiments are completely independent of each other The instructor can assign any or all Parts of these experiments and in any order This format provides flexibility depending on the schedule laboratory time available and course objectives In addition experiments 12 through 16 provide two options for experiments These five experiments are divided into two major sections identified as A or B The A experiments continue with the format of previous experiments they are constructed with discrete components on standard protoboards as used in most electronic teaching laboratories The A experiments can be assigned in programs where traditional devices are emphasized Each B experiment has a similar format to the corresponding A experiment but uses a programmable Analog Signal Processor ASP that is controlled by free Computer Aided Design CAD software from the Anadigm company www.anadigm.com These experiments support the Programmable Analog Design feature in the textbook The B experiments are also subdivided into independent Parts but Experiment 12 B Part 1 is a software tutorial and should be performed before any other B experiments This is an excellent way to introduce the ASP technology because no other hardware is required other than a computer running the downloaded software In addition to Experiment 12 B the first 13 steps of Experiment 15 B Part 2 are also tutorial in nature for the AnadigmFilter program This is an amazing active filter design tool that is easy to learn and is included with the AnadigmDesigner2 AD2 CAD software The ASP is part of a Programmable Analog Module PAM circuit board from the Servenger company www.servenger.com that interfaces to a personal computer The PAM is controlled by the AD2 CAD software from the Anadigm company website Except for Experiment 12 B Part 1 it is assumed that the PAM is connected to the PC and AnadigmDesigner2 is running Experiment 16 B Part 3 also requires a spreadsheet program such as Microsoft Excel The PAM is described in detail in the Quick Start Guide Appendix B Instructors may choose to mix A and B experiments with no loss in continuity depending on course objectives and time We recommend that Experiment 12 B Part 1 be assigned if you want students to have an introduction to the ASP without requiring a hardware purchase A text feature is the Device Application DA at the end of most chapters All of the DAs have a related laboratory exercise using a similar circuit that is sometimes simplified to make laboratory time as efficient as possible The same text icon identifies the related DA exercise in

the lab manual One issue is the trend of industry to smaller surface mount devices which are very difficult to work with and are not practical for most lab work For example almost all varactors are supplied as surface mount devices now In reviewing each experiment we have found components that can illustrate the device function with a traditional one The traditional through hole MV2109 varactor is listed as obsolete but will be available for the foreseeable future from Electronix Express www.elexp.com so it is called out in Experiment 3 All components are available from Electronix Express www.elexp.com as a kit of parts see list in Appendix A The format for each experiment has not changed from the last edition and is as follows

Introduction A brief discussion about the experiment and comments about each of the independent Parts that follow Reading assignment in the Floyd text related to the experiment Key Objectives A statement specific to each Part of the experiment of what the student should be able to do Components Needed A list components and small items required for each Part but not including the equipment found at a typical lab station Particular care has been exercised to select materials that are readily available and reusable keeping cost at a minimum Parts There are two or three independent parts to each experiment Needed tables graphs and figures are positioned close to the first referenced location to avoid confusion Step numbering starts fresh with each Part but figures and tables are numbered sequentially for the entire experiment to avoid multiple figures with the same number Conclusion At the end of each Part space is provided for a written conclusion Questions Each Part includes several questions that require the student to draw upon the laboratory work and check his or her understanding of the concepts Troubleshooting questions are frequently presented Multisim Simulation At the end of each A experiment except 1 one or more circuits are simulated in a Multisim computer simulation New Multisim troubleshooting problems have been added to this edition Multisim troubleshooting files are identified with the suffix f1 f2 etc in the file name standing for fault1 fault2 etc Other files with nf as the suffix include demonstrations or practice using instruments such as the Bode Plotter and the Spectrum Analyzer A special icon is shown with all figures that are related to the Multisim simulation Multisim files are found on the website www.pearsonglobaledition.com Floyd Microsoft PowerPoint slides are available at no cost to instructors for all experiments The slides reinforce the experiments with troubleshooting questions and a related problem and are available on the instructor s resource site Each laboratory station should contain a dual variable regulated power supply a function generator a multimeter and a dual channel oscilloscope A list of all required materials is given in Appendix A along with information on acquiring the PAM As mentioned components are also available as a kit from Electronix Express the kit number is 32DBEDFL10

Electronic Devices: Systems & Applications-Lab Manual Thomson Delmar Learning, 2004-08

Fundamentals of Electronic Systems Design Jens Lienig, Hans Bruemmer, 2017-04-25 This textbook covers the design of electronic systems from the ground up from drawing and CAD essentials to recycling requirements Chapter by chapter it deals with the challenges any modern system designer faces The design process and its fundamentals such as technical drawings and CAD electronic system levels assembly and packaging

issues and appliance protection classes reliability analysis thermal management and cooling electromagnetic compatibility EMC all the way to recycling requirements and environmental friendly design principles This unique book provides fundamental complete and indispensable information regarding the design of electronic systems This topic has not been addressed as complete and thorough anywhere before Since the authors are world renown experts it is a foundational reference for today s design professionals as well as for the next generation of engineering students Dr Patrick Groeneveld Synopsys Inc **Lab manual to accompany fundamentals of electronic communications systems** Wayne Tomasi,1988

Laboratory Manual for Electronic Devices and Circuits David A. Bell,2001 This lab manual accompanies Electronic Devices and Circuits 4 e Handbook of Electronic Systems Design Charles A. Harper,1980 **A Design Approach to Accompany - Digital Systems** Ronald J. Tocci,Greg Moss,Neal Widmer,2010-08 The lab manual by Greg Moss A Design Approach features digital logic design using complex programmable logic devices CPLDs or field programmable gate arrays FPGAs In other words this lab manual uses Quartus software rather than the old school hands on lab equipment ISBN 10 0132153815 ISBN 13 9780132153812 *Electronic Systems Technical Reference Manual* Custom Electronic Design and Installation Association,2008-01-01 The Electronic Systems Technical Reference Manual First Edition provides a comprehensive resource for anyone who installs designs or has an interest in residential electronic systems It was created to fill the necessity for a reference book that serves the continuing education needs of our industry The Electronic Systems Technical Reference Manual First Edition consolidates the information that has found to be most applicable to residential technicians and designers The chapters are presented by discipline concepts within the chapter progress from the simple to the most complex Key terms are provided to further define concepts and review questions allow the reader to gauge comprehension The outlines of exam content the Job Task Analyses for both the CEDIA Advanced EST and Designer certifications sample exam questions and a summary of the certification development process are all included in the appendix of the book CEDIA is an international trade association of companies that specialize in planning and installing electronic systems for the home CEDIA provides top ranked educational conferences industry professional training and certification focused on the installation and integration of residential electronic systems that consumers use to enhance their lifestyles As part of its mission statement CEDIA educates residential electronic systems contractors in the global custom electronics industry *Lab Manual* Gregory L. Moss,2004 **Design to Test** John Turino,2012-12-06 This book is the second edition of Design to Test The first edition written by myself and H Frank Binnendyk and first published in 1982 has undergone several printings and become a standard in many companies even in some countries Both Frank and I are very proud of the success that our customers have had in utilizing the information all of it still applicable to today s electronic designs But six years is a long time in any technology field I therefore felt it was time to write a new edition This new edition while retaining the basic testability prin ciples first documented six years ago contains the latest material on state of the art testability

techniques for electronic devices boards and systems and has been completely rewritten and up dated Chapter 15 from the first edition has been converted to an appendix Chapter 6 has been expanded to cover the latest tech nology devices Chapter 1 has been revised and several examples throughout the book have been revised and updated But some times the more things change the more they stay the same All of the guidelines and information presented in this book deal with the three basic testability principles partitioning control and visibility They have not changed in years But many people have gotten smarter about how to implement those three basic test ability principles and it is the aim of this text to enlighten the reader regarding those new and old testability implementation techniques *Electronic Design Automation for IC System Design, Verification, and Testing* Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-12-19 The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logic verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for integrated circuit IC designs design and verification languages digital simulation hardware acceleration and emulation and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on high level synthesis system on chip SoC block based design and back annotating system level models Offering improved depth and modernity Electronic Design Automation for IC System Design Verification and Testing provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals

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Lab Manual Gregory L. Moss,2007 **Digital Design** Thomas Collins,Christopher Twigg,2009 *Lab Manual for Introduction to Electronics* Earl D. Gates,1991 Model-Based Engineering for Complex Electronic Systems Peter Wilson,H. Alan Mantooth,2013-03-13 In the electronics industry today consumer demand for devices with hyper connectivity and mobility has resulted in the development of a complete system on a chip SoC Using the old rule of thumb design methods of the past is no longer feasible for these new complex electronic systems To develop highly successful systems that meet the requirements and quality expectations of customers engineers now need to use a rigorous model based approach in their designs This book provides the definitive guide to the techniques methods and technologies for electronic systems engineers embedded systems engineers and hardware and software engineers to carry out model based electronic system design as well as for students of IC systems design Based on the authors considerable industrial experience the book shows how to implement the methods in the context of integrated circuit design flows Complete guide to methods techniques and technologies of model based engineering design for developing robust electronic systems Written by world experts in model based design who have considerable industrial experience Shows how to adopt the methods using numerous industrial examples in the context of integrated circuit design **Fundamentals of Electronic Devices and Circuits Lab Manual** David Bell,2009-11-22 The laboratory investigations in this manual are designed to demonstrate the theoretical principles set out in the book Fundamentals of Electronic Devices and Circuits 5 e A total of 43 laboratory investigations are offered involving the construction and testing of the circuits discussed in the textbook Each investigation can normally be completed within a two hour period The procedures contain some references to the textbook however all necessary circuit and connection diagrams are provided in the manual so that investigations can also be preformed without the textbook
Electronic Concepts Unit II ,1974

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