## Cork Institute of Technology

#### Higher Certificate in Engineering in Electrical Engineering - Stage 1

#### Summer 2006

#### Industrial Electronics

(Time: 3 Hours)

INSTRUCTIONS:
Answer Five Questions.
All formula and calculations must be shown.

Examiners:

Mr. R. Daly Mr. M. Hennessy Prof. E. McQuade

Q1. (a) Using the reference points in Fig. 1, explain (ignoring the capacitor) the operation of the full-wave bridge rectifier circuit and indicate the polarity at the output terminals C and D.

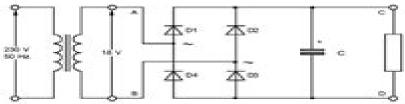


Fig. 1

- (b) Show for the circuit, fully detailed waveforms with voltage and time values, for:
  - the voltage at the terminals AB;
  - the voltage at the terminals CD without the capacitor.

14 meerks

- Q2. (a) Construct a 5-column binary table with appropriate headings to represent the decimal numbers from 0 to 15. Write the decimal digits in the left hand column. <u>5 Marchs</u>
  - (b) Draw the circuit logic symbols and truth tables for, an inverter, a 2-input AND gate and a 2-input XOR gate.
  - (c) Show how the gates in (b) can be connected to compare two data bits A and B for: A<B, A=B and A>B. An output status of 1 is required when the comparison is true.
- Q3. (a) Draw a circuit diagram for an LED connected to a 24V d.c. supply and calculate the resistance required to limit the current in the LED to 50mA. Assume the LED operating voltage to be 3.5V.

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## **Table of Contents Industrial Eletronics Past Papers**

- 1. Understanding the eBook Industrial Electronics Past Papers
  - The Rise of Digital Reading Industrial Eletronics Past Papers
  - Advantages of eBooks Over Traditional Books
- 2. Identifying Industrial Eletronics Past Papers
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
- 3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Industrial Eletronics Past Papers
  - User-Friendly Interface
- 4. Exploring eBook Recommendations from Industrial Eletronics Past Papers
  - Personalized Recommendations
  - Industrial Elctronics Past Papers User Reviews and Ratings

- Industrial Electronics Past Papers and Bestseller Lists
- 5. Accessing Industrial Elctronics Past Papers Free and Paid eBooks
  - o Industrial Eletronics Past Papers Public Domain eBooks
  - Industrial Eletronics Past Papers eBook Subscription Services
  - o Industrial Eletronics Past Papers Budget-Friendly Options
- 6. Navigating Industrial Eletronics Past Papers eBook Formats
  - ∘ ePub, PDF, MOBI, and More
  - Industrial Eletronics Past Papers Compatibility with Devices
  - Industrial Elctronics Past Papers Enhanced eBook Features
- 7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Industrial Electronics Past Papers
  - Highlighting and Note-Taking Industrial Electronics Past Papers
  - Interactive Elements Industrial Electronics Past Papers
- 8. Staying Engaged with Industrial Eletronics Past Papers
  - o Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Industrial Electronics Past Papers
- 9. Balancing eBooks and Physical Books Industrial Electronics Past Papers
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Industrial Eletronics Past Papers
- 10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
- 11. Cultivating a Reading Routine Industrial Electronics Past Papers
  - Setting Reading Goals Industrial Electronics Past Papers
  - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Industrial Eletronics Past Papers
  - Fact-Checking eBook Content of Industrial Eletronics Past Papers
  - Distinguishing Credible Sources

- 13. Promoting Lifelong Learning
  - Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
- 14. Embracing eBook Trends
  - Integration of Multimedia Elements
  - Interactive and Gamified eBooks

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