

Cork Institute of Technology

Higher Certificate in Engineering in Electrical Engineering - Stage 1

Summer 2006

Industrial Electronics

(Time: 3 Hours)

INSTRUCTIONS:

Answer Five Questions.

All formula and calculations must be shown.

Examiners:

Mr. R. Daly

Mr. M. Hennessy

Prof. E. McQuade

- Q1. (a) Using the reference points in Fig. 1, explain (ignoring the capacitor) the operation of the full-wave bridge rectifier circuit and indicate the polarity at the output terminals C and D. 6 marks

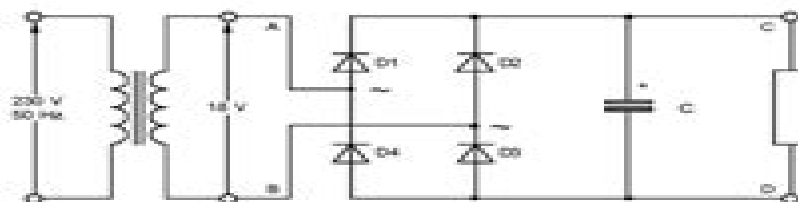


Fig.1

- (b) Show for the circuit, fully detailed waveforms with voltage and time values, for:
- (i) the voltage at the terminals AB;
 - (ii) the voltage at the terminals CD without the capacitor.
- 14 marks
- Q2. (a) Construct a 5-column binary table with appropriate headings to represent the decimal numbers from 0 to 15. Write the decimal digits in the left hand column. 5 Marks
- (b) Draw the circuit logic symbols and truth tables for, an inverter, a 2-input AND gate and a 2-input XOR gate. 7 Marks
- (c) Show how the gates in (b) can be connected to compare two data bits A and B for: $A < B$, $A = B$ and $A > B$. An output status of 1 is required when the comparison is true. 5 Marks
- Q3. (a) Draw a circuit diagram for an LED connected to a 24V d.c. supply and calculate the resistance required to limit the current in the LED to 50mA. Assume the LED operating voltage to be 3.5V. 4 marks

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PT Brinkman



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