

ELECTRICAL ENGINEERING

PAPER-I

1. Consider the following statements with reference to a system with velocity error constant $K_v = 1000$

1. The system is stable.
2. The system is of type 1
3. The test signal used is a step input.

Which of these statements are correct?

- a. 1 and 2
- b. 1 and 3
- c. 2 and 3
- d. 1, 2 and 3

2. Which one of the following statements is NOT of correct?

- a. With the introduction of integral control, the steady state error increases
- b. The generalised error coefficients provide a simple way of determining the nature of the response of a feedback control to any arbitrary input
- c. The generalised error coefficients lead to calculation of complete steady state response without actually solving the system differential equation
- d. For a type-1 system, the steady state error for acceleration input is infinite

3. Consider the following statements with reference to the root loci of the characteristic equation of unity feedback control system with an open loop transfer function of

$$G(s) = \frac{(s+1)(s+3)(s+5)}{s(s+2)}$$

1. Each locus starts at an open loop pole and ends either at an open loop zero or infinity
2. Each locus starts at an open loop pole or infinity and ends at an open loop zero.
3. There are three separate root loci.
4. There are five separate root loci.

Which of these statements are correct ?

- a. 2 and 3
- b. 2 and 4
- c. 1 and 3

4.

- d. 1 and 4



The Bode phase angle plot of a system is shown above.

The type of the system

- a. 0
- b. 1
- c. 2
- d.

5.

The transfer function of a system is given by:

$$G(s) = \frac{K(s+10)^2(s+100)}{s(s+25)}$$

The number of loci terminating at infinity is

- a. 0
- b. 1
- c. 2
- d. 3

6.



The Nyquist plot of a unity feedback system having open $G(s) = \frac{K(s+3)(s+5)}{(s-2)(s-4)}$

loop transfer function for $K = 1$ is as shown above. For the system to be stable, the range of values of K is

- a. $0 < K < 1.33$
- b. $0 < K < 1/1.33$
- c. $K > 1.33$
- d. $K > 1/1.33$

7.

Ies Paper For Electrical Engineering

SJ Ball



Ies Paper For Electrical Engineering:

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