



# education

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**MID-YEAR EXAMINATION**

**GRADE 12**

**MATHEMATICAL LITERACY P2**

**MARKING GUIDELINE**

**JUNE 2023**

**MARKS: 100**

| Symbol/Simbool | Explanation/Verduideliking                                                                                           |
|----------------|----------------------------------------------------------------------------------------------------------------------|
| M              | Method/Metode                                                                                                        |
| M/A            | Method with accuracy/Metode van akkuraatheid                                                                         |
| CA             | Consistent accuracy/Volgehoue akkuraatheid                                                                           |
| A              | Accuracy/Akkuraatheid                                                                                                |
| C              | Conversion/Herleiding                                                                                                |
| S              | Simplification/Vereenvoudiging                                                                                       |
| RT             | Reading from a table/graph/diagram/Lees vanaf tabel/grafiek/diagram                                                  |
| SF             | Correct substitution in a formula/Korrekte vervanging in formule                                                     |
| O              | Opinion/Example/Definition/Explanation/Opinie/Voorbeeld/Definisie/Verduideliking                                     |
| P              | Penalty, e.g., for no units, incorrect rounding off, etc./Penalisasie, bv. vir geen eenhede/verkeerde afronding ens. |
| R              | Rounding off/Afronding                                                                                               |
| NPR            | No penalty for correct rounding/Geen penalisering vir korrekte afronding nie                                         |
| NPU            | No penalty for the units/Geen penalisering vir eenhede nie                                                           |
| AO             | Answer only, if correct, full marks/Slegs antwoord, indien korrek, volpunte                                          |
| MCA            | Method with consistent accuracy/Metode met volgehoue akkuraatheid                                                    |

**This marking guideline consists of 13 pages**

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web represent two unsigned 2 bit numbers the outputs are the four bits of the product of the input numbers express the logic functions for each term in the product on a karnaugh map of the four input variables hence design a multiplier circuit using 4 input nand and inverter gates only a 3 a 2 a1 a0 0001 1100

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web nov 15 2021 logic gates logic gates are used in a computer to transform the 1s and 0s from input wires it accepts inputs and then outputs are results based on their state the logic gate is a small transistor circuit that is part of different forms in an integrated circuit each type of gate has one or usually two inputs and one output

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parity bit is an extra bit included with a message to make the total number of 1 s either even or odd

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