# 8085 – Instruction set list

## 8085 Instructions – Datatransfer

| Opcode | Operand | Explanation of Instruction | Description |
|---|---|---|---|
| MOV | Rd, Rs<br><br>M, Rs<br><br>Rd, M | Copy from source(Rs) to destination(Rd) | This instruction copies the contents of the source register into the destination register; the contents of the source register are not altered. If one of the operands is a memory location, its location is specified by the contents of the HL registers.<br><br>Example: MOV B, C or MOV B, M |
| MVI | Rd, data<br><br>M, data | Move immediate 8-bit | The 8-bit data is stored in the destination register or memory. If the operand is a memory location, its location is specified by the contents of the HL registers.<br><br>Example: MVI B, 57H or MVI M, 57H |
| LDA | 16-bit address | Load accumulator | The contents of a memory location, specified by a 16-bit address in the operand, are copied to the accumulator. The contents of the source are not altered.<br><br>Example: LDA 2034H |
| LDAX | B/D Reg. pair | Load accumulator indirect | The contents of the designated register pair point to a memory location. This instruction copies the contents of that memory location into the accumulator. The contents of either the register pair or the memory location are not altered.<br><br>Example: LDAX B |
| LXI | Reg. pair, 16-bit data | Load register pair immediate | The instruction loads 16-bit data in the register pair designated in the operand.<br><br>Example: LXI H, 2034H or LXI H, XYZ |
| LHLD | 16-bit address | Load H and L registers direct | The instruction copies the contents of the memory location pointed out by the 16-bit address into register L and copies the contents of the next memory location into register H. The contents of source memory locations are not altered.<br><br>Example: LHLD 2040H |

# Intel 8085 Microprocessor Instruction Set Ppt

**Christopher A. Titus,Jonathan A. Titus,David G. Larsen**

**Intel 8085 Microprocessor Instruction Set Ppt:**

   **Interface Age** ,1979      *Ten Days with 8085 MICROPROCESSOR* K. A. KRISHNAMURTHY,Krishnamurthy K. A.,2010-01-01 This text is designed to provide hands on experience to students to help them develop an understanding of the hardware components of a microprocessor and the role of software in programming and interfacing aspects of the microprocessor An 8 bit microprocessor due to its simpler instruction set and architecture is an ideal IC chip for providing the students with a solid foundation for micro processors their principles and applications The concepts of all state of the art processors can be understood easily once the basics of the 8085 are understood Today s sophisticated microprocessors have a semblance of 8085 The presentation style adopted in this book in a way is unique It is a student friendly text written as conversation between the teacher and the students The book lucidly explains the various programming examples in assembly language with a view to enabling students to develop microprocessor based industrial application projects Application programs developed in the book are based on the popular microprocessor kit namely SDA 85 The book is suitable for both diploma and degree level students pursuing courses in Electronics and Electrical Engineering Electronics and Communication Engineering and Information and Communication Technology      **Introduction to Microprocessors with the INTEL 8085** John A. Seeger,1996-06-06 For a one semester introductory course in microprocessors this text covers the basic principles of microprocessors microprocessor systems and interfacing As an example the widely used Intel 8085A microprocessor is described in detail The 8085A provides a case study which any sophomore level engineering technology or computer science student can understand with little difficulty as opposed to the newer 16 or 32 bit microprocessors and which allows for coverage of all the important introductory concepts The book is divided into 15 Chapters Chapter 1 is an introduction to microprocessors including an outline of their historical development and descriptions of several applications The basic architecture of microprocessors and microprocessor systems is covered in Chapters 2 and 3 Chapters 4 and 5 present programming and some basic features of the instructions The 8085A instruction set is covered in detail in Chapters 6 through 10 with examples illustrating its use Chapters 11 through 14 address input output and interfacing with numerous examples Finally Chapter 15 is a brief description of some other important microprocessors      *Microprocessor 8085, 8086* Abhishek Yadav,2008      **Introduction to Microprocessors with the Intel 8085** John A. Seeger,1988 For a one semester introductory course in microprocessors this text covers the basic principles of microprocessors microprocessor systems and interfacing As an example the widely used Intel 8085A microprocessor is described in detail The 8085A provides a case study which any sophomore level engineering technology or computer science student can understand with little difficulty as opposed to the newer 16 or 32 bit microprocessors and which allows for coverage of all the important introductory concepts The book is divided into 15 Chapters Chapter 1 is an introduction to microprocessors including an outline of their historical development and descriptions of several applications The basic architecture of microprocessors and microprocessor systems

is covered in Chapters 2 and 3 Chapters 4 and 5 present programming and some basic features of the instructions The 8085A instruction set is covered in detail in Chapters 6 through 10 with examples illustrating its use Chapters 11 through 14 address input output and interfacing with numerous examples Finally Chapter 15 is a brief description of some other important microprocessors    The 8085 Microprocessor K. Udaya Kumar,2008 Designed for an undergraduate course on the 8085 microprocessor this text provides comprehensive coverage of the programming and interfacing of the 8 bit microprocessor Written in a simple and easy to understand manner this book introduces the reader to the basics and the architecture of the 8085 microprocessor It presents balanced coverage of both hardware and software concepts related to the microprocessor    **Microprocessor 8085 and Its Interfacing** Sunil Mathur,2011-09 This comprehensive and thoroughly updated text now in its second edition continues to provide the complete knowledge about the Intel s 8085 microprocessors its programming and concept of interfacing of memory input output devices and programmable peripheral chips Organized in four parts Part I Chapters 1 9 covers a review of the analog and digital signals as well as hardware and software related aspects of microprocessor 8085 Part II Chapters 10 and 11 discusses memory and input output concepts analog to digital and digital to analog converters and various memory and IO address decoding techniques Part III Chapters 12 17 explains the programmable interfacing chips with extensive interfacing examples Part IV Chapters 18 and 19 presents a brief discussion on other 8 bit microprocessors along with 16 and 32 bit Intel Processors Each topic has been supported with numerous examples that will help students apply the concepts to other microprocessors in the course at advanced level This book is designed specifically for the undergraduate students of electronics and communication engineering computer science and engineering and information technology New to this Edition Chapters on Architecture and Organization of Microprocessor and Instruction Set of 8085 Microprocessor have been revised and modified substantially Multiple choice questions have been added to all the chapters    *Elements of Microprocessors* Atul P. Godse,2007 8085 MicroprocessorBasic 8085 Microprocessor architecture and its functional blocks 8085 Microprocessor IC pinouts and signals address data and control buses clock signals instruction cycles machine cycles and timing states instruction timing diagram Programming of 8085 Microprocessor Basic instruction set of 8085 addressing modes writing assembly language programs looping counting and indexing operations stacks and subroutines conditional call and return instructions debugging programs 8085 Interfacing and InterruptsBus interfacing concepts timing for the execution of input and output I O instructions I O address decoding memory and I O interfacing memory mapped I O interfacing of matrix input keyboard and output display Serial I O lines of 8085 and the implementation asynchronous serial data communication using SID and SOD lines interrupt structure of 8085 RST restart instructions vectored interrupt interrupt process and timing diagram of interrupt instruction execution 8259A interrupt controller principles block I O data transfer direct memory access techniques Programmable Interface and Peripheral DevicesProgramming and applications of 8455 8156 programmable I O ports and

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