

8085 - Instruction set list

8085 Instructions - Datatransfer

| Opcode | Operand | Explanation of Instruction | Description |
|-------------|---|---|--|
| MOV | Rd, Rs M, Rs Rd, M | Copy from source(Rs) to destination(Rd) | <p>This instruction copies the contents of the source register into the destination register; the contents of the source register are not altered. If one of the operands is a memory location, its location is specified by the contents of the HL registers.</p> <p>Example: MOV B, C or MOV B, M</p> |
| MVI | Rd, data M, data | Move immediate 8-bit | <p>The 8-bit data is stored in the destination register or memory. If the operand is a memory location, its location is specified by the contents of the HL registers.</p> <p>Example: MVI B, 57H or MVI M, 57H</p> |
| LDA | 16-bit address | Load accumulator | <p>The contents of a memory location, specified by a 16-bit address in the operand, are copied to the accumulator. The contents of the source are not altered.</p> <p>Example: LDA 2034H</p> |
| LDAX | B/D Reg. pair | Load accumulator indirect | <p>The contents of the designated register pair point to a memory location. This instruction copies the contents of that memory location into the accumulator. The contents of either the register pair or the memory location are not altered.</p> <p>Example: LDAX B</p> |
| LXI | Reg. pair, 16-bit data | Load register pair immediate | <p>The instruction loads 16-bit data in the register pair designated in the operand.</p> <p>Example: LXI H, 2034H or LXI H, XYZ</p> |
| LHLD | 16-bit address | Load H and L registers direct | <p>The instruction copies the contents of the memory location pointed out by the 16-bit address into register L and copies the contents of the next memory location into register H. The contents of source memory locations are not altered.</p> <p>Example: LHLD 2040H</p> |

Intel 8085 Instruction Set

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