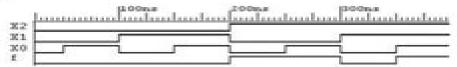
2-41-



2-42.



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module circuit 4 53(X, Y, Z, K) input X, Y, Z; output F; assign F = (X & Z) | (Z & -Y); cridinodule

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