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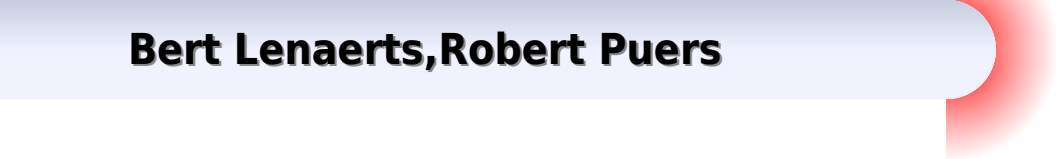
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Mentor Graphics Eldo User Manual

Bert Lenaerts, Robert Puers



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MOSFET Modeling & BSIM3 User's Guide Yuhua Cheng, Chenming Hu, 2007-05-08 Circuit simulation is essential in integrated circuit design and the accuracy of circuit simulation depends on the accuracy of the transistor model BSIM3v3 BSIM for Berkeley Short channel IGFET Model has been selected as the first MOSFET model for standardization by the Compact Model Council a consortium of leading companies in semiconductor and design tools In the next few years many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3 This will require many device engineers and most circuit designers to learn the basics of BSIM3 MOSFET Modeling BSIM3 User's Guide explains the detailed physical effects that are important in modeling MOSFETs and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters It is the first book devoted to BSIM3 It treats the BSIM3 model in detail as used in digital analog and RF circuit design It covers the complete set of models i e I V model capacitance model noise model parasitics model substrate current model temperature effect model and non quasi static model MOSFET Modeling BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's guide to the device or circuit design engineers who use the BSIM3 model in digital analog circuit design RF modeling statistical modeling and technology prediction This book is written for circuit designers and device engineers as well as device scientists worldwide It is also suitable as a reference for graduate courses and courses in circuit design or device modelling Furthermore it can be used as a textbook for industry courses devoted to BSIM3 MOSFET Modeling BSIM3 User's Guide is comprehensive and practical It is balanced between the background information and advanced discussion of BSIM3 It is helpful to experts and students alike

Omnidirectional Inductive Powering for Biomedical Implants Bert Lenaerts, Robert Puers, 2008-10-14 Omnidirectional Inductive Powering for Biomedical Implants investigates the feasibility of inductive powering for capsule endoscopy and freely moving systems in general The main challenge is the random position and orientation of the power receiving system with respect to the emitting magnetic field Where classic inductive powering assumes a predictable or fixed alignment of the respective coils the remote system is now free to adopt just any orientation while still maintaining full power capabilities Before elaborating on different approaches towards omnidirectional powering the design and optimisation of a general inductive power link is discussed in all its aspects Special attention is paid to the interaction of the inductive power link with the patient's body Putting theory into practice the implementation of an inductive power link for a capsule endoscope is included in a separate chapter

The System Designer's Guide to VHDL-AMS Peter J. Ashenden, Gregory D. Peterson, Darrell A. Teegarden, 2002-09-10 The demand is exploding for complete integrated systems that sense process manipulate and control complex entities such as sound images text motion and environmental conditions These systems from hand held devices to automotive sub systems to aerospace vehicles employ electronics to manage and adapt to a world that is predominantly neither digital nor electronic To respond to

this design challenge the industry has developed and standardized VHDL AMS a unified design language for modeling digital analog mixed signal and mixed technology systems VHDL AMS extends VHDL to bring the successful HDL modeling methodology of digital electronic systems design to these new design disciplines Gregory Peterson and Darrell Teegarden join best selling author Peter Ashenden in teaching designers how to use VHDL AMS to model these complex systems This comprehensive tutorial and reference provides detailed descriptions of both the syntax and semantics of the language and of successful modeling techniques It assumes no previous knowledge of VHDL but instead teaches VHDL and VHDL AMS in an integrated fashion just as it would be used by designers of these complex integrated systems Explores the design of an electric powered unmanned aerial vehicle system UAV in five separate case studies to illustrate mixed signal mixed technology power systems communication systems and full system modeling **Silicon-on-insulator Technology and Devices XI**

Electrochemical Society. Meeting, 2003 *Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation* Nadine Azemard, 2007-08-21 This volume features the refereed proceedings of the 17th International Workshop on Power and Timing Modeling Optimization and Simulation Papers cover high level design low power design techniques low power analog circuits statistical static timing analysis power modeling and optimization low power routing optimization security and asynchronous design low power applications modeling and optimization and more

Circuit Design for Reliability Ricardo Reis, Yu Cao, Gilson Wirth, 2014-11-08 This book presents physical understanding modeling and simulation on chip characterization layout solutions and design techniques that are effective to enhance the reliability of various circuit units The authors provide readers with techniques for state of the art and future technologies ranging from technology modeling fault detection and analysis circuit hardening and reliability management *LNA-ESD Co-Design for Fully Integrated CMOS Wireless Receivers* Paul Leroux, Michiel Steyaert, 2006-03-30 LNA ESD Co Design for Fully Integrated CMOS Wireless Receivers fits in the quest for complete CMOS integration of wireless receiver front ends With a combined discussion of both RF and ESD performance it tackles one of the final obstacles on the road to CMOS integration The book is conceived as a design guide for those actively involved in the design of CMOS wireless receivers The book starts with a comprehensive introduction to the performance requirements of low noise amplifiers in wireless receivers Several popular topologies are explained and compared with respect to future technology and frequency scaling The ESD requirements are introduced and related to the state of the art protection devices and circuits LNA ESD Co Design for Fully Integrated CMOS Wireless Receivers provides an extensive theoretical treatment of the performance of CMOS low noise amplifiers in the presence of ESD protection circuitry The influence of the ESD protection parasitics on noise figure gain linearity and matching are investigated Several RF ESD co design solutions are discussed allowing both high RF performance and good ESD immunity for frequencies up to and beyond 5 GHz Special attention is also paid to the layout of both active and passive components LNA ESD Co Design for Fully Integrated CMOS Wireless Receivers offers the reader intuitive insight in

the LNA's behavior as well as the necessary mathematical background to optimize its performance. All material is experimentally verified with several CMOS implementations among which a fully integrated GPS receiver front end. The book is essential reading for RF design engineers and researchers in the field and is also suitable as a text book for an advanced course on the subject.

Wireless Radio-Frequency Standards and System Design: Advanced Techniques Cornetta, Gianluca, Santos, David J., Vazquez, Jose Manuel, 2012-01-31

Radio frequency RF integrated circuits in CMOS technology are gaining increasing popularity in the commercial world and CMOS technology has become the dominant technology for applications such as GPS receivers, GSM cellular transceivers, wireless LAN and wireless short range personal area networks based on IEEE 802.15.1 Bluetooth or IEEE 802.15.4 ZigBee standards. Furthermore, the increasing interest in wireless technologies and the widespread of wireless communications has prompted an ever increasing demand for radio frequency transceivers. *Wireless Radio Frequency Standards and System Design: Advanced Techniques* provides perspectives on radio frequency circuit and systems design covering recent topics and developments in the RF area. Exploring topics such as LNA linearization, behavioral modeling and co-simulation of analog and mixed signal complex blocks for RF applications, integrated passive devices for RF ICs and baseband design techniques and wireless standards, this is a comprehensive reference for students as well as practicing professionals.

Extreme Environment Electronics John D. Cressler, H. Alan Mantooth, 2017-12-19

Unfriendly to conventional electronic devices, circuits and systems, extreme environments represent a serious challenge to designers and mission architects. The first truly comprehensive guide to this specialized field, *Extreme Environment Electronics* explains the essential aspects of designing and using devices, circuits and electronic systems intended to operate in extreme environments including across wide temperature ranges and in radiation intense scenarios such as space. The *Definitive Guide to Extreme Environment Electronics* featuring contributions by some of the world's foremost experts in extreme environment electronics, the book provides in-depth information on a wide array of topics. It begins by describing the extreme conditions and then delves into a description of suitable semiconductor technologies and the modeling of devices within those technologies. It also discusses reliability issues and failure mechanisms that readers need to be aware of as well as best practices for the design of these electronics. Continuing beyond just the paper design of building blocks, the book rounds out coverage of the design realization process with verification techniques and chapters on electronic packaging for extreme environments. The final set of chapters describes actual chip level designs for applications in energy and space exploration. Requiring only a basic background in electronics, the book combines theoretical and practical aspects in each self-contained chapter. Appendices supply additional background material. With its broad coverage and depth and the expertise of the contributing authors, this is an invaluable reference for engineers, scientists and technical managers as well as researchers and graduate students. A hands-on resource, it explores what is required to successfully operate electronics in the most demanding conditions.

VLSI: Integrated Systems on Silicon Ricardo A. Reis, Luc

Claesen,2013-06-05 This book contains the papers that have been presented at the ninth Very Large Scale Integrated Systems conference VLSI 97 that is organized biannually by IFIP Working Group 10.5. It took place at Hotel Serra Azul in Gramado, Brazil, from 26-30 August 1997. Previous conferences have taken place in Edinburgh, Trondheim, Vancouver, Munich, Grenoble, and Tokyo. The papers in this book report on all aspects of importance to the design of the current and future integrated systems. The current trend towards the realization of versatile Systems on a Chip requires attention of embedded hardware, software, systems, dedicated ASIC hardware, sensors and actuators, mixed analog/digital design, video and image processing, low power, battery operation, and wireless communication. The papers as presented in this book have been organized in two tracks where one is dealing with VLSI System Design and Applications and the other presents VLSI Design Methods and CAD. The following topics are addressed: VLSI System Design and Applications Track: VLSI for Video and Image Processing, Microsystem and Mixed mode design, Communication and Memory System Design, Core voltage, Low power Analog Circuits, High Speed Circuit Techniques, Application Specific DSP Architectures. VLSI Design Methods and CAD Track: Specification and Simulation at System Level, Synthesis and Technology Mapping, CAD Techniques for Low Power Design, Physical Design Issues in Sub-micron Technologies, Architectural Design and Synthesis, Testing in Complex Mixed Analog and Digital Systems.

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