

8085 - Instruction set list

8085 Instructions - Datatransfer

Opcode	Operand	Explanation of Instruction	Description
MOV	Rd, Rs M, Rs Rd, M	Copy from source(Rs) to destination(Rd)	This instruction copies the contents of the source register into the destination register; the contents of the source register are not altered. If one of the operands is a memory location, its location is specified by the contents of the HL registers. Example: MOV B, C or MOV B, M
MVI	Rd, data M, data	Move immediate 8-bit	The 8-bit data is stored in the destination register or memory. If the operand is a memory location, its location is specified by the contents of the HL registers. Example: MVI B, 57H or MVI M, 57H
LDA	16-bit address	Load accumulator	The contents of a memory location, specified by a 16-bit address in the operand, are copied to the accumulator. The contents of the source are not altered. Example: LDA 2034H
LDAX	B/D Reg. pair	Load accumulator indirect	The contents of the designated register pair point to a memory location. This instruction copies the contents of that memory location into the accumulator. The contents of either the register pair or the memory location are not altered. Example: LDAX B
LXI	Reg. pair, 16-bit data	Load register pair immediate	The instruction loads 16-bit data in the register pair designated in the operand. Example: LXI H, 2034H or LXI H, XYZ
LHLD	16-bit address	Load H and L registers direct	The instruction copies the contents of the memory location pointed out by the 16-bit address into register L and copies the contents of the next memory location into register H. The contents of source memory locations are not altered. Example: LHLD 2040H

Intel Manual For 8085

Dr. Rekha Annigeri



Intel Manual For 8085:

Introduction to Microprocessors with the INTEL 8085 John A. Seeger, 1996-06-06 For a one semester introductory course in microprocessors this text covers the basic principles of microprocessors microprocessor systems and interfacing As an example the widely used Intel 8085A microprocessor is described in detail The 8085A provides a case study which any sophomore level engineering technology or computer science student can understand with little difficulty as opposed to the newer 16 or 32 bit microprocessors and which allows for coverage of all the important introductory concepts The book is divided into 15 Chapters Chapter 1 is an introduction to microprocessors including an outline of their historical development and descriptions of several applications The basic architecture of microprocessors and microprocessor systems is covered in Chapters 2 and 3 Chapters 4 and 5 present programming and some basic features of the instructions The 8085A instruction set is covered in detail in Chapters 6 through 10 with examples illustrating its use Chapters 11 through 14 address input output and interfacing with numerous examples Finally Chapter 15 is a brief description of some other important microprocessors

The 8085 Microprocessor: Architecture, Programming and Interfacing: Architecture, Programming and Interfacing K. Udaya Kumar, B. S. Umashankar, 2008 The 8085 Microprocessor Architecture Programming and Interfacing is designed for an undergraduate course on the 8085 microprocessor this text provides comprehensive coverage of the programming and interfacing of the 8 bit microprocessor Written in a simple and easy to understand manner this book introduces the reader to the basics and the architecture of the 8085 microprocessor It presents balanced coverage of both hardware and software concepts related to the microprocessor

The 8085 Microprocessor K. Udaya Kumar, 2008 Designed for an undergraduate course on the 8085 microprocessor this text provides comprehensive coverage of the programming and interfacing of the 8 bit microprocessor Written in a simple and easy to understand manner this book introduces the reader to the basics and the architecture of the 8085 microprocessor It presents balanced coverage of both hardware and software concepts related to the microprocessor

MICROPROCESSOR 8085 Dr. Rekha Annigeri, 2017-06-22 The 8085 microprocessor introduced by Intel in 1976 is an 8 bit microprocessor that forms the core of many educational and embedded system projects It operates on an 8 bit data bus and a 16 bit address bus which allows it to access up to 64KB of memory The 8085 microprocessor is known for its simplicity making it a popular choice for those new to microprocessor programming and design Its architecture includes 74 instructions and supports operations such as data transfer arithmetic logical branching and control instructions The 8085 microprocessor is equipped with five 8 bit registers B C D E H and L a 16 bit stack pointer and a 16 bit program counter These registers facilitate data manipulation and address handling within the processor The accumulator an essential part of the 8085 is an 8 bit register that plays a critical role in arithmetic and logical operations The processor operates at a clock speed of 3 MHz which was quite advanced for its time allowing it to perform up to 0.5 MIPS Million Instructions Per Second One of the significant features of the 8085

microprocessor is its interrupt system which provides five interrupt inputs allowing it to respond to external events promptly. These interrupts are vectored meaning they automatically branch to specific memory locations to execute the interrupt service routines. This feature is particularly useful in real time applications where the microprocessor needs to handle multiple tasks simultaneously. The 8085 also includes a Serial Input Output control which is essential for communication with peripheral devices. This feature allows the microprocessor to be integrated into more complex systems enabling it to communicate with other devices and systems effectively. The simplicity of the 8085's instruction set and architecture makes it an excellent tool for learning the fundamentals of microprocessor programming and understanding the basic principles of how microprocessors interact with memory and peripheral devices. Its legacy continues in educational settings where it serves as a foundation for understanding more advanced microprocessor architectures.

8085/8085 Assembly Language Programming Manual Intel Corporation, 1981

Microprocessors and Microcomputer-Based System Design Mohamed Rafiquzzaman, 2021-11-01

Microprocessors and Microcomputer Based System Design Second Edition builds on the concepts of the first edition. It discusses the basics of microprocessors various 32 bit microprocessors the 8085 microprocessor the fundamentals of peripheral interfacing and Intel and Motorola microprocessors. This edition includes new topics such as floating point arithmetic Program Array Logic and flash memories. It covers the popular Intel 80486 80960 and Motorola 68040 as well as the Pentium and PowerPC microprocessors. The final chapter presents system design concepts applying the design principles covered in previous chapters to sample problems.

MICROPROCESSORS AND MICROCONTROLLERS :: ARCHITECTURE, PROGRAMMING AND SYSTEM DESIGN 8085, 8086, 8051, 8096 KANT, KRISHNA, 2014-01-01

This book provides the students with a solid foundation in the technology of microprocessors and microcontrollers their principles and applications. It comprehensively presents the material necessary for understanding the internal architecture as well as system design aspects of Intel's legendary 8085 and 8086 microprocessors and Intel's 8051 and 8096 microcontrollers. The book throughout maintains an appropriate balance between the basic concepts and the skill sets needed for system design. Besides the book lucidly explains the hardware architecture the instruction set and programming support chips peripheral interfacing and cites several relevant examples to help the readers develop a complete understanding of industrial application projects. Several system design case studies are included to reinforce the concepts discussed. With exhaustive coverage and practical approach the book would be indispensable to undergraduate students of Electrical and Electronics Electronics and Communication and Electronics and Instrumentation Engineering. It can be used for a variety of courses in Microprocessors Microcontrollers and Embedded System Design. The second edition of the book introduces additional topics like I/O interfacing and programming serial interface programming delay programming using 8086 and 8051. Besides many more examples and case studies have been added.

Principles of Electrical Sciences Mr. Rohit Manglik, 2024-03-06

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