

HSPICE® Reference Manual: Commands and Control Options

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SYNOPSYS®

Hspice 2010 Manual

RJ Shavelson



Hspice 2010 Manual:

CMOS Sigma-Delta Converters Jose M. de la Rosa, Rocio del Rio, 2013-03-13 A comprehensive overview of Sigma Delta Analog to Digital Converters ADCs and a practical guide to their design in nano scale CMOS for optimal performance This book presents a systematic and comprehensive compilation of sigma delta converter operating principles the new advances in architectures and circuits design methodologies and practical considerations going from system level specifications to silicon integration packaging and measurements with emphasis on nanometer CMOS implementation The book emphasizes practical design issues from high level behavioural modelling in MATLAB SIMULINK to circuit level implementation in Cadence Design FrameWork II As well as being a comprehensive reference to the theory the book is also unique in that it gives special importance on practical issues giving a detailed description of the different steps that constitute the whole design flow of sigma delta ADCs The book begins with an introductory survey of sigma delta modulators their fundamentals architectures and synthesis methods covered in Chapter 1 In Chapter 2 the effect of main circuit error mechanisms is analysed providing the necessary understanding of the main practical issues affecting the performance of sigma delta modulators The knowledge derived from the first two chapters is presented in the book as an essential part of the systematic top down bottom up synthesis methodology of sigma delta modulators described in Chapter 3 where a time domain behavioural simulator named SIMSIDES is described and applied to the high level design and verification of sigma delta ADCs Chapter 4 moves farther down from system level to the circuit and physical level providing a number of design recommendations and practical recipes to complete the design flow of sigma delta modulators To conclude the book Chapter 5 gives an overview of the state of the art sigma delta ADCs which are exhaustively analysed in order to extract practical design guidelines and to identify the incoming trends design challenges as well as practical solutions proposed by cutting edge designs Offers a complete survey of sigma delta modulator architectures from fundamentals to state of the art topologies considering both switched capacitor and continuous time circuit implementations Gives a systematic analysis and practical design guide of sigma delta modulators from a top down bottom up perspective including mathematical models and analytical procedures behavioural modeling in MATLAB SIMULINK macromodeling and circuit level implementation in Cadence Design FrameWork II chip prototyping and experimental characterization Systematic compilation of cutting edge sigma delta modulators Complete description of SIMSIDES a time domain behavioural simulator implemented in MATLAB SIMULINK Plenty of examples case studies and simulation test benches covering the different stages of the design flow of sigma delta modulators A number of electronic resources including SIMSIDES the statistical data used in the state of the art survey as well as many design examples and test benches are hosted on a companion website Essential reading for Researchers and electronics engineering practitioners interested in the design of high performance data converters integrated in nanometer CMOS technologies mixed signal designers [Advances in Information and Communication](#)

Networks Kohei Arai,Supriya Kapoor,Rahul Bhatia,2018-12-05 The book gathering the proceedings of the Future of Information and Communication Conference FICC 2018 is a remarkable collection of chapters covering a wide range of topics in areas of information and communication technologies and their applications to the real world It includes 104 papers and posters by pioneering academic researchers scientists industrial engineers and students from all around the world which contribute to our understanding of relevant trends of current research on communication data science ambient intelligence networking computing security and Internet of Things This book collects state of the art chapters on all aspects of information science and communication technologies from classical to intelligent and covers both theory and applications of the latest technologies and methodologies Presenting state of the art intelligent methods and techniques for solving real world problems along with a vision of the future research this book is an interesting and useful resource

Sigma-Delta Converters: Practical Design Guide Jose M. de la Rosa,2018-08-22 Thoroughly revised and expanded to help readers systematically increase their knowledge and insight about Sigma Delta Modulators Sigma Delta Modulators SDMs have become one of the best choices for the implementation of analog digital interfaces of electronic systems integrated in CMOS technologies Compared to other kinds of Analog to Digital Converters ADCs SDMs cover one of the widest conversion regions of the resolution versus bandwidth plane being the most efficient solution to digitize signals in an increasingly number of applications which span from high resolution low bandwidth digital audio sensor interfaces and instrumentation to ultra low power biomedical systems and medium resolution broadband wireless communications Following the spirit of its first edition Sigma Delta Converters Practical Design Guide 2nd Edition takes a comprehensive look at SDMs their diverse types of architectures circuit techniques analysis synthesis methods and CAD tools as well as their practical design considerations It compiles and updates the current research reported on the topic and explains the multiple trade offs involved in the whole design flow of Sigma Delta Modulators from specifications to chip implementation and characterization The book follows a top down approach in order to provide readers with the necessary understanding about recent advances trends and challenges in state of the art SDMs It makes more emphasis on two key points which were not treated so deeply in the first edition It includes a more detailed explanation of SDMs implemented using Continuous Time CT circuits going from system level synthesis to practical circuit limitations It provides more practical case studies and applications as well as a deeper description of the synthesis methodologies and CAD tools employed in the design of converters Sigma Delta Converters Practical Design Guide 2nd Edition serves as an excellent textbook for undergraduate and graduate students in electrical engineering as well as design engineers working on SD data converters who are looking for a uniform and self contained reference in this hot topic With this goal in mind and based on the feedback received from readers the contents have been revised and structured to make this new edition a unique monograph written in a didactical pedagogical and intuitive style

Power Electronics and Control Techniques for Maximum Energy Harvesting in Photovoltaic Systems Nicola

Femia,2017-07-12 Incentives provided by European governments have resulted in the rapid growth of the photovoltaic PV market Many PV modules are now commercially available and there are a number of power electronic systems for processing the electrical power produced by PV systems especially for grid connected applications Filling a gap in the literature Power Electronics and Control Techniques for Maximum Energy Harvesting in Photovoltaic Systems brings together research on control circuits systems and techniques dedicated to the maximization of the electrical power produced by a photovoltaic PV source Tools to Help You Improve the Efficiency of Photovoltaic Systems The book supplies an overview of recent improvements in connecting PV systems to the grid and highlights various solutions that can be used as a starting point for further research and development It begins with a review of methods for modeling a PV array working in uniform and mismatched conditions The book then discusses several ways to achieve the best maximum power point tracking MPPT performance A chapter focuses on MPPT efficiency examining the design of the parameters that affect algorithm performance The authors also address the maximization of the energy harvested in mismatched conditions in terms of both power architecture and control algorithms and discuss the distributed MPPT approach The final chapter details the design of DC DC converters which usually perform the MPPT function with special emphasis on their energy efficiency Get Insights from the Experts on How to Effectively Implement MPPT Written by well known researchers in the field of photovoltaic systems this book tackles state of the art issues related to how to extract the maximum electrical power from photovoltaic arrays under any weather condition Featuring a wealth of examples and illustrations it offers practical guidance for researchers and industry professionals who want to implement MPPT in photovoltaic systems

Variation-Aware Design of Custom Integrated Circuits: A Hands-on Field Guide Trent McConaghy,Kristopher Breen,Jeffrey Dyck,Amit

Gupta,2012-10-02 This book targets custom IC designers who are encountering variation issues in their designs especially for modern process nodes at 45nm and below such as statistical process variations environmental variations and layout effects It teaches them the state of the art in Variation Aware Design tools which help the designer to analyze quickly the variation effects identify the problems and fix the problems Furthermore this book describes the algorithms and algorithm behavior performance limitations which is of use to designers considering these tools designers using these tools CAD researchers and CAD managers

Compact Models for Integrated Circuit Design Samar K. Saha,2018-09-03 Compact Models for Integrated Circuit Design Conventional Transistors and Beyond provides a modern treatise on compact models for circuit computer aided design CAD Written by an author with more than 25 years of industry experience in semiconductor processes devices and circuit CAD and more than 10 years of academic experience in teaching compact modeling courses this first of its kind book on compact SPICE models for very large scale integrated VLSI chip design offers a balanced presentation of compact modeling crucial for addressing current modeling challenges and understanding new models for emerging devices Starting from basic semiconductor physics and covering state of the art device regimes from conventional micron to nanometer this

text Presents industry standard models for bipolar junction transistors BJTs metal oxide semiconductor MOS field effect transistors FETs FinFETs and tunnel field effect transistors TFETs along with statistical MOS models Discusses the major issue of process variability which severely impacts device and circuit performance in advanced technologies and requires statistical compact models Promotes further research of the evolution and development of compact models for VLSI circuit design and analysis Supplies fundamental and practical knowledge necessary for efficient integrated circuit IC design using nanoscale devices Includes exercise problems at the end of each chapter and extensive references at the end of the book Compact Models for Integrated Circuit Design Conventional Transistors and Beyond is intended for senior undergraduate and graduate courses in electrical and electronics engineering as well as for researchers and practitioners working in the area of electron devices However even those unfamiliar with semiconductor physics gain a solid grasp of compact modeling concepts from this book **Source-Synchronous Networks-On-Chip** Ayan Mandal, Sunil P. Khatri, Rabi

Mahapatra, 2013-11-19 This book describes novel methods for network on chip NoC design using source synchronous high speed resonant clocks The authors discuss NoCs from the bottom up providing circuit level details before providing architectural simulations As a result readers will get a complete picture of how a NoC can be designed and optimized Using the methods described in this book readers are enabled to design NoCs that are 5X better than existing approaches in terms of latency and throughput and can also sustain a significantly greater amount of traffic *Digital VLSI Design with Verilog*

John Michael Williams, 2014-06-17 This book is structured as a step by step course of study along the lines of a VLSI integrated circuit design project The entire Verilog language is presented from the basics to everything necessary for synthesis of an entire 70 000 transistor full duplex serializer deserializer including synthesizable PLLs The author includes everything an engineer needs for in depth understanding of the Verilog language Syntax synthesis semantics simulation and test Complete solutions for the 27 labs are provided in the downloadable files that accompany the book For readers with access to appropriate electronic design tools all solutions can be developed simulated and synthesized as described in the book A partial list of design topics includes design partitioning hierarchy decomposition safe coding styles back annotation wrapper modules concurrency race conditions assertion based verification clock synchronization and design for test A concluding presentation of special topics includes System Verilog and Verilog AMS *Nonlinear Circuit Simulation and Modeling*

José Carlos Pedro, David E. Root, Jianjun Xu, Luís Côtimos Nunes, 2018-06-14 Discover the nonlinear methods and tools needed to design real world microwave circuits with this tutorial guide Balancing theoretical background with practical tools and applications it covers everything from the basic properties of nonlinear systems such as gain compression intermodulation and harmonic distortion to nonlinear circuit analysis and simulation algorithms and state of the art equivalent circuit and behavioral modeling techniques Model formulations discussed in detail include time domain transistor compact models and frequency domain linear and nonlinear scattering models Learn how to apply these tools to designing

real circuits with the help of a power amplifier design example which covers all stages from active device model extraction and the selection of bias and terminations through to performance verification. Realistic examples, illustrative insights and clearly conveyed mathematical formalism make this an essential learning aid for both professionals working in microwave and RF engineering and graduate students looking for a hands-on guide to microwave circuit design.

Design of CMOS Millimeter-Wave and Terahertz Integrated Circuits with Metamaterials Hao Yu, Yang Shang, 2015-10-19. This book shows that with the use of metamaterials one can have coherent THz signal generation, amplification, transmission and detection for phase arrayed CMOS transistors with significantly improved performance. Offering detailed coverage from device to system, the book describes the design and application of metamaterials in actual CMOS integrated circuits. It includes real circuit examples and chip demonstrations with measurement results and also evaluates system performance after CMOS-based system on-chip integration. The book reflects the latest research progress and provides a state-of-the-art reference on CMOS-based metamaterial devices and mm-wave and THz systems.

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web sep 13 2021 english the model allows you vary the speed of both the ice and mg2 mg2 is the larger of the 2 electric motor generators and is also referred to as the traction motor because its speed rpm or revolutions per minute has a fixed relationship to the speed of the wheels addeddate 2021 09 13 22 22 27 emulator ruffle swf

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web power split device which is a single pg in this paper in this section the basic mechanisms of a single pg and possible configurations are described for further developments in clutch placements fig 1 shows a pg and its equivalent lever diagram 15 the three nodes on the lever diagram represent the ring gear carrier

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web jul 18 2016 model four touring perhaps this belongs in the newbie forum but i m trying to understand the functioning of the psd i understand that initially mg2 may power the car with the ice stopped this means that mg1 is turning and because it is a permanent magnet device it should be generating power

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web feb 10 2015 i ran across this page yesterday and was fascinated by the inner workings of the power split device psd i also noticed that there was enough information there for me to put together an animation and working interactive demo of the psd with controls for input from ice and mg2 so i spent a couple hours putting it together in maya

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web nov 6 2016 introduction many resources and threads exist that describe the prius power split device they correctly describe how rpms can be calculated and how torque is split but i did not find any that would correctly describe how the power is split the solution seems easy at first power is rpm multiplied by torque

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an epicyclic or planetary gear similar to those used in automatic transmissions however its use in the prius is very different as i hope to explain

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