

# GUIDE TO NETWORK SECURITY

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# Guide To Network Security Mattord

**L Reisser**



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web sep 3 2023 first let s put the circuit concept from above into a more understandable block diagram now that we see what we will need for the circuit to work let s start with the main and most important part the power off delay timer the idea that when you press the switch vout will appear for the time that you press and hold the switch is well known

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web the below figure is the schematic of a simple automatic on off timer with a fixed timing resistor and capacitor so the time period after which this circuit will automatically turn on off the output is fixed and can be found out by using the formula mentioned in the calculation section

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web jun 5 2022 upon application of input voltage the time delay t1 begins at the end of the time delay t1 the output is energized and remains in that condition for the time delay t2 at the end of this time delay t2 the output is de energized input voltage must be removed to reset the time delay relay

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web in a ladder diagram the two poles of the power source are drawn as vertical rails of a ladder with horizontal rungs showing the switch contacts relay contacts relay coils and final control elements lamps solenoid coils motors drawn in between the power rails ladder diagram symbols

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web also called normally closed off delay relays one shot timers provide a single contact pulse of specified duration for each coil energization transition from coil off to coil on recycle timers provide a repeating sequence of on off contact pulses as long as the coil is maintained in an energized state

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web now if you want to reverse the operation of the circuit simply wire the blue and red wires together from the timer and

connect it to the photo eye as shown in the figure now the circuit is reversed for example the light will turn off at night and switch on in the sunshine during the day good to know it is recommended to add a relay

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web this type of positive feedback is also called hysteresis and provides a fast transition from the on to off state once turning off starts the off delay timing is mostly a function of c1 and r6 the exact delay is a bit hard to predict because it depends on the gain of the transistors particularly of q1

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