

## **I. VERILOG INTRODUCTION**

Verilog is a hardware description language (HDL) that describes the functionality of hardware design and the synthesis tool converts hardware descriptions into an actual design that has combinational and sequential elements. Verilog language is simpler than VHDL. Verilog is based on C language, whereas VHDL is based on Ada and Pascal languages. Latest Verilog standard: IEEE Standard 1364-2005.

Verilog is basically a structural and behavior language and defines four abstraction levels to implement modules. With respect to the external environment, the module is viewed as identical irrespective of abstraction levels. But internal module implementation differs based on abstraction as described below.

1. Gate level – The module implementation is similar to the gate-level design description in terms of logic gates and interconnections between them.
2. Dataflow level – The module implementation depends on data flow specification i.e. how data flows and processes in the design circuit.
3. Switch level – The module implementation requires switch level knowledge to implement a design in terms of storage nodes, switches. This is the lowest level of abstraction.
4. Behavior level – The module implementation is similar to C language programming that includes algorithmic level implementation without worrying about hardware implementation details.

Above abstraction levels are also commonly mentioned with modeling terminology. The design can be implemented with a combination of gate-level, data flow, and behavioral modeling. The commonly heard term RTL (Register Transfer Level) in digital design is used for a combination of data flow and behavior modeling.

# Measurements Lab Manual 3rd Year Vtu

**JE Gale**



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**Measurement Principles Lab Manual : Mechanical Engineering Technician Program, ENG8313** Tessier, Y. (Yves), Algonquin College, 1997 *Physical Measurements* Ralph Smith Minor, 1916 *A Student's Manual of a Laboratory Course in Physical Measurements* Wallace Clement Sabine, 1893 *A Manual of Physical Measurements* Anthony Zeleny, 2015-06-27 Excerpt from *A Manual of Physical Measurements* This manual is an outline of the laboratory experiments given in the one year and the two year courses in general Physics at the University of Minnesota The laboratory work in these courses supplements the lectures and recitations and occupies one two hour period a week for two and four semesters respectively The experiments given in a junior course of one semester in electrical measurements are also included in the section on electricity It is taken for granted that the student has acquired a general knowledge of a subject before it is considered in the laboratory and no attempt is made in the manual at completeness in subject matter or in explanations The work is done under the guidance of an instructor who furnishes any additional information necessary The student should feel that acceptable results depend upon his own ability to properly adjust the apparatus and he alone should plan and execute the details of the experiments subject of course to the criticism of the instructor We here wish to thank Prof J Zeleny and Mr Paul E Klopsteg for valuable suggestions criticisms and assistance during the preparation of this manual About the Publisher Forgotten Books publishes hundreds of thousands of rare and classic books Find more at [www.forgottenbooks.com](http://www.forgottenbooks.com) This book is a reproduction of an important historical work Forgotten Books uses state of the art technology to digitally reconstruct the work preserving the original format whilst repairing imperfections present in the aged copy In rare cases an imperfection in the original such as a blemish or missing page may be replicated in our edition We do however repair the vast majority of imperfections successfully any imperfections that remain are intentionally left to preserve the state of such historical works

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