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FUNDAMENTALS OF MICROELECTRONICS

WITH ROBOTICS AND BIOENGINEERING APPLICATIONS

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including noise and nonlinearity with ties to analog design microwave theory and communication systems An intuitive treatment of modulation theory and wireless standards from the standpoint of the RF IC designer Transceiver architectures such as heterodyne sliding IF directconversion image reject and low IF topologies Low noise amplifiers including cascode common gate and commonsource topologies noise cancelling schemes and reactance cancelling configurations Passive and active mixers including their gain and noise analysis and new mixer topologies Voltage controlled oscillators phase noise mechanisms and various VCO topologies dealing with noisepower tuning trade offs All new coverage of passive devices such as integrated inductors MOS varactors and transformers A chapter on the analysis and design of phase locked loops with emphasis on low phase noise and low spur levels Two chapters on integer N and fractional N synthesizers including the design of frequency dividers Power amplifier principles and circuit topologies along with transmitter architectures such as polar modulation and outphasing

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web sending end power circle diagram the circle drawn with sending end true and reactive power components as the horizontal and vertical coordinates is known as the sending end power circle diagram similar procedure can be followed for determining the data required for drawing the sending end power circle diagram for long transmission line as

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web circle diagram constructed for problem 1 determine the send end voltage and the reactive power supplied by the synchronous condenser when the overall power factor at the receiving end is a unity and b 0.9 leading 214kv 19.3 kvar 202 kv 38.6 kvar 4 a three phase 60hz transmission line is 250 mi long the voltage at the sending end is

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