

Introduction to NIOS Capacitor

The Metal-Oxide-Semiconductor (MOS) structure, universally referred to as oxides, forming the basis of the working of the MOS Field Effect Transistors which is the backbone of the integrated circuits, lends itself as a design determining the quality of the process used in the fabrication of an inter. Maintaining the quality and reliability of gate oxide is one of the most challenging tasks in any semiconductor fabrication. Electrical characterisation monitoring is critical for maintaining gate oxide uniformity. Many electrical characterisation techniques have been developed over the years to characterise gate oxide. However, the most commonly used tool for studying gate oxide quality is Capacitance-Voltage (C-V) technique. C-V test results offer a wealth of device information including bulk and interface charges and many MOS device param

The importance of C-V measurement techniques is that a large number of parameters can be extracted from the high frequency C-V curves that is also the quasi-static C-V. These parameters can provide critical device information. One can divide the parameters roughly into three groups. It includes typical MOS device parameters such as flat band voltage, threshold voltage, etc. The next group, oxide charge parameters, includes interface trap charge density, etc. The third group consists of doping-related parameters. The following parameters can be extracted from MOS capacitor measurements:

- Guide (dielectric) thickness
- Charges in the guide
- Guide breakdown strength
- Conductivity type
- Doping concentration

Internet Lab Manual

Yan Bai



Internet Lab Manual:

Decoding **Internet Lab Manual**: Revealing the Captivating Potential of Verbal Expression

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