

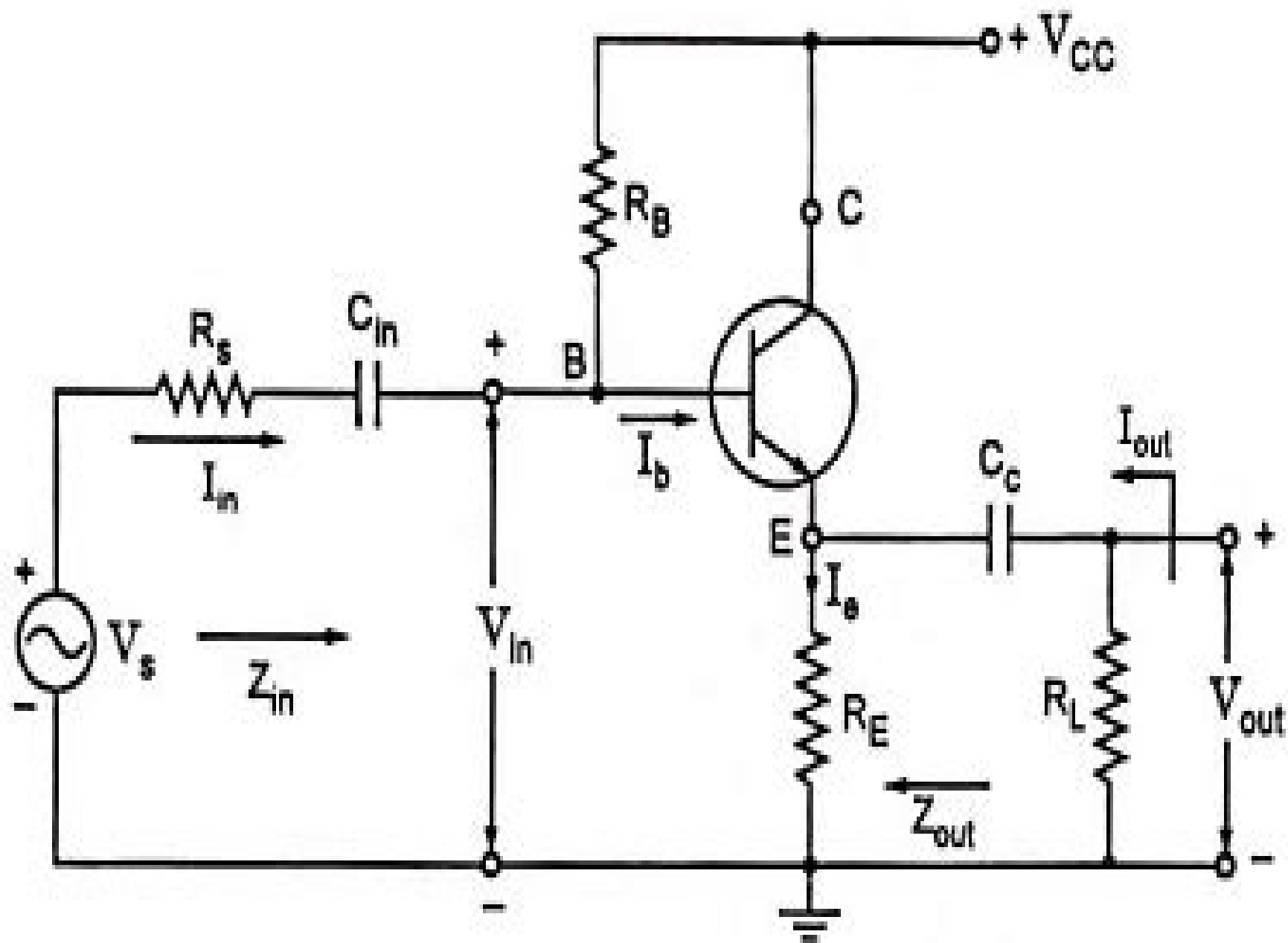


Fig. 19.43 *Emitter Follower Circuit*

Emitter Follower Circuit Analysis

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