

Practical 1

Date: 3rd Jan.

Aim: To Study about Microwind tool and λ (Lambda) Rules for Layout Generation.

Objective:

1. To be familiar with tool.
2. To learn about λ (Lambda) Rules for 90 nm Technology.

Microwind Getting Started:

The present experiment is a guide to using the « **Microwind** » educational software on a PC computer.

The MICROWIND program allows the student to design and simulate an integrated circuit. The package itself contains a library of common logic and analog ICs to view and simulate. MICROWIND includes all the commands for a mask editor as well as new original tools never gathered before in a single module. You can gain access to *Circuit Simulation* by pressing one single key. The electric extraction of your circuit is automatically performed and the analog simulator produces voltage and current curves immediately.

A specific command displays the characteristics of pMOS and nMOS, where the size of the device and the process parameters can be very easily changed. Altering the MOS model parameters and, then, seeing the effects on the V_d and I_d curves constitutes a good interactive tutorial on devices.

The *Process Simulator* shows the layout in a vertical perspective, as when fabrication has been completed. This feature is a significant aid to supplement the descriptions of fabrication found in most textbooks.

The *Logic Cell Compiler* is a particularly sophisticated tool enabling the automatic design of a CMOS circuit corresponding to your logic description in VERILOG. The DSCH software, which is a user-friendly schematic editor and a logic simulator presented in a companion manual, is used to generate this Verilog description. The cell is created in compliance with the environment, design rules and fabrication specifications.

A set of CMOS processes ranging from 1.2 μ m down to state-of-the-art 0.25 μ m are proposed.

To use the MICROWIND program use the following procedure:

- ❶ Go to the directory in which the software has been copied

Microwind Lab Manual

R Barnett



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