

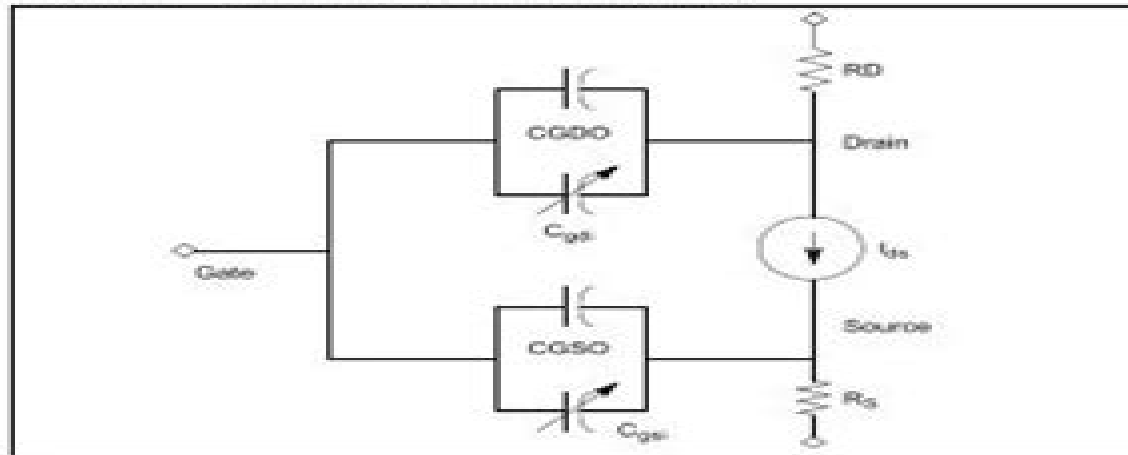
Cgd, Cgs

$$C_{gd} = C_{gdi} + CGDO \quad C_{gs} = C_{gsi} + CGSO$$

LEVEL 40 Model Topology

Figure 34 shows the topology of the LEVEL 40 model.

Figure 34 LEVEL 40 HP a-Si TFT Topology



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Hspice Mosfet Models Manual Texas Am University

**Mourad Fakhfakh, Esteban Tlelo-
Cautle, Patrick Siarry**



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