

PRE SECONDARY SCHOLARSHIP EXAMINATION (Std. 8th), FEBRUARY 2020

Seat No.

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Set

A

Paper No. II

Medium : English

Time : 1:30 to 3:00

Pages : 16

Third Language and Intelligence Test

Total Marks : 150

Read the following instructions carefully before you answer the questions.

- (1) This question paper contains two sections. In Section I there are 1 to 25 questions of Third Language and in Section II there are 26 to 75 questions of Intelligence Test. All 75 questions are compulsory.
- (2) All questions carry 2 marks each.
- (3) Time limit to solve the question paper is 90 minutes.
- (4) Separate answer sheet is given to record the answers. Each question has been given four alternatives. Read them carefully. Select and shade the correct answer in the answer sheet.
For Example :- If the correct alternative for Q. No. 6 is 2, then record your answer as follows :
Question No. 6 - ① ● ③ ④
- (5) Maximum 20% questions are having two correct alternatives. Candidate has to darken both the circles with correct answers. These questions will have the instruction 'Select two correct alternatives'.
For Example :- If the correct alternative for Q. No. 8 is 2 & 3, then record your answer as follows :
Question No. 8 - ① ● ● ④
- (6) Answer recorded in any other form will not be considered. Such answer will be credited with 'Zero' mark.
- (7) Use blue or black ball pen to darken the circle. Circles darkened by pencil will not be credited.
- (8) Answer once recorded can not be changed.
- (9) The rough work is to be done in the space given under each page or on the last page of the question paper.
- (10) The exam has limited time, in case you are not able to solve a question, kindly solve the next question. In the end if time permits you can try to solve the unsolved questions.
- (11) If you find any question incomplete or with error, then do not ask anything to the Supervisor or Centre Incharge.
- (12) For any error / mistake / objection issues, school or parents should not send written application to Block Education Officer or Education Officer. These error / mistake / objection issues should be register online through respective school login only.
- (13) The error / mistake / objection issues should be register online within 10 - days from the publication of Interim answer key on MSCE website.
- (14) Suggestion & decision of expert committee will be applicable on printing or any other mistake in question paper.

SI

Msce English Question

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web feb 16 2023 by using the coding style above there is no question about which version of bottom is wanted the version in the library my lib1 is the correct one care should be taken when choosing library names for vhdl files while a library called work is a common library name for many projects the tool will handle it a little differently than

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to describe the input and output ports of this circuit 1 entity circuit 1 is 2 port a in std logic 3 b in std logic 4 out1 out std logic 5 end circuit 1

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web basic logic gates esd chapter 2 figure 2 3 every vhdl design description consists of at least one entity architecture pair or one entity with multiple architectures the entity section of the hdl design is used to declare the i o ports of the circuit while the description code resides within architecture portion

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web quartus and vhdl code in this section you should learn how to implement a vhdl code for single cycle data path before we go any further let s define some of the terms that we use throughout the book entity all designs are expressed in terms of entities an entity is the most basic building block in a design

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