

Name _____
Date _____
Period _____

Integer Performance Task

Instructions: You are in charge of your family's money this month. Use the register page to keep track of expenses.

Objective: Find out if you have enough money to go to the Halloween Dance at the end of the month which is \$25 per person.

1. You start with \$500 in your checking account. (see transaction 1)
2. You pay \$10 each week for your brother's lunch account. There are 4 weeks this month. (see transaction 2)
3. Pay the electric bill: \$125
4. Babysit your neighbor and earn \$25
5. Go to Face-Be! with friends and spend \$5.50 on a Nacho Fries Box
6. Fill up your mom's gas tank. Spend \$45
7. Buy three tickets for the Dodge game. Each ticket costs \$15
8. "Overtime check" Deposit: \$250
9. Go to AMPM and get Fajitas, a slurpee and a hot dog. Spend \$6.25
10. Sell 4 pairs of shoes at a garage sale for \$10 each
11. Check your balance!

Register check: your account should currently be at \$652.25. Go back and check your calculations if your register is off!!

12. Grocery shop. Spend \$125
13. Buy school supplies. Spend \$28.25
14. Your mom tells you to DEPOSIT the money in 2 and give half to your sister!
15. Buy 2 halloween costumes for \$40 each
16. Lose 2 teeth. Get \$5 from the tooth fairy for each tooth.
17. Fill up the gas tank. Spend \$40
18. Go to the store and buy a gallon of milk. Spend \$1.50
19. Go to the movies on discount day. Spend \$6.50
20. Check your balance!

Final check: Do you have enough money to go to go to the dance? Do you have enough to pay for your sister and brother to go too? Explain:

Integer Performance Task

**Dino Quintero,Luis Carlos Cruz
Huertas,Tsuyoshi Kamenoue,Wainer
dos Santos Moschetta,Mauricio Faria
de Oliveira,Georgy E Pavlov,Alexander
Pozdneev,IBM Redbooks**

Integer Performance Task:

Designing Assessment for Mathematics Eileen Depka, 2007-05-31 This updated edition provides strategies for deepening students understanding of math concepts involving students in standards based rubric development and using rubric data to improve instruction

High-Performance Scientific Computing Michael W. Berry, Kyle A. Gallivan, Efstratios Gallopoulos, Ananth Grama, Bernard Philippe, Yousef Saad, Faisal Saied, 2012-01-18 This book presents the state of the art in parallel numerical algorithms applications architectures and system software The book examines various solutions for issues of concurrency scale energy efficiency and programmability which are discussed in the context of a diverse range of applications Features includes contributions from an international selection of world class authorities examines parallel algorithm architecture interaction through issues of computational capacity based codesign and automatic restructuring of programs using compilation techniques reviews emerging applications of numerical methods in information retrieval and data mining discusses the latest issues in dense and sparse matrix computations for modern high performance systems multicores manycores and GPUs and several perspectives on the Spike family of algorithms for solving linear systems presents outstanding challenges and developing technologies and puts these in their historical context

Delphi High Performance Primož Gabrijelčič, 2018-02-26 Build fast scalable and high performing applications with Delphi Key Features Build efficient and concurrent applications in Delphi with focused examples Identify performance bottlenecks and apply the correct algorithm to increase the performance of applications Delve into parallel programming and memory management to optimize your code Book Description Delphi is a cross platform Integrated Development Environment IDE that supports rapid application development for Microsoft Windows Apple Mac OS X Google Android iOS and now Linux with RAD Studio 10.2 This book will be your guide to build efficient high performance applications with Delphi The book begins by explaining how to find performance bottlenecks and apply the correct algorithm to fix them It will teach you how to improve your algorithms before taking you through parallel programming You ll then explore various tools to build highly concurrent applications After that you ll delve into improving the performance of your code and master cross platform RTL improvements Finally we ll go through memory management with Delphi and you ll see how to leverage several external libraries to write better performing programs By the end of the book you ll have the knowledge to create high performance applications with Delphi What you will learn Find performance bottlenecks and easily mitigate them Discover different approaches to fix algorithms Understand parallel programming and work with various tools included with Delphi Master the RTL for code optimization Explore memory managers and their implementation Leverage external libraries to write better performing programs Who this book is for This book is for Delphi developers who would like to build high performance applications with Delphi Prior knowledge of Delphi is assumed

Architecture of High Performance Computers Volume II R. IBBETT, 2013-11-11

Performance Evaluation and Benchmarking. Traditional - Big Data - Internet of Things Raghunath

Nambiar,Meikel Poess,2017-02-17 This book constitutes the thoroughly refereed post conference proceedings of the 8th TPC Technology Conference on Performance Evaluation and Benchmarking TPCTC 2016 held in conjunction with the 41st International Conference on Very Large Databases VLDB 2016 in New Delhi India in September 2016 The 9 papers presented were carefully reviewed and selected from 20 submissions They reflect the rapid pace at which industry experts and researchers develop innovative techniques for evaluation measurement and characterization of complex systems

POWER8 High-performance Computing Guide IBM Power System S822LC (8335-GTB) Edition Dino

Quintero,Joseph Apuzzo,John Dunham,Mauricio Faria de Oliveira,Markus Hilger,Desnes Augusto Nunes Rosario,Wainer dos Santos Moschetta,Alexander Pozdneev,IBM Redbooks,2017-08-04 This IBM Redbooks publication documents and addresses topics to provide step by step customizable application and programming solutions to tune application and workloads to use IBM Power Systems™ hardware architecture This publication explores tests and documents the solution to use the architectural technologies and the software solutions that are available from IBM to help solve challenging technical and business problems This publication also demonstrates and documents that the combination of IBM high performance computing HPC solutions hardware and software delivers significant value to technical computing clients who are in need of cost effective highly scalable and robust solutions First the book provides a high level overview of the HPC solution including all of the components that makes the HPC cluster IBM Power System S822LC 8335 GTB software components interconnect switches and the IBM Spectrum™ Scale parallel file system Then the publication is divided in three parts Part 1 focuses on the developers Part 2 focuses on the administrators and Part 3 focuses on the evaluators and planners of the solution The IBM Redbooks publication is targeted toward technical professionals consultants technical support staff IT Architects and IT Specialists who are responsible for delivering cost effective HPC solutions that help uncover insights from vast amounts of client s data so they can optimize business results product development and scientific discoveries Essentials of Computer

Architecture Douglas Comer,2024-05-20 This easy to read textbook provides an introduction to computer architecture focusing on the essential aspects of hardware that programmers need to know Written from a programmer s point of view Essentials of Computer Architecture Third Edition covers the three key aspects of architecture processors physical and virtual memories and input output I O systems This third edition is updated in view of advances in the field Most students only have experience with high level programming languages and almost no experience tinkering with electronics and hardware As such this text is revised to follow a top down approach moving from discussions on how a compiler transforms a source program into binary code and data to explanations of how a computer represents data and code in binary Additional chapters cover parallelism and data pipelining assessing the performance of computer systems and the important topic of power and energy consumption Exclusive to this third edition a new chapter explains multicore processors and how coherence hardware provides a consistent view of the values in memory even though each core has its own cache Suitable

for a one semester undergraduate course this clear concise and easy to read textbook offers an ideal introduction to computer architecture for students studying computer programming

Essentials of Computer Architecture, Second Edition Douglas Comer, 2017-01-06 This easy to read textbook provides an introduction to computer architecture while focusing on the essential aspects of hardware that programmers need to know The topics are explained from a programmer's point of view and the text emphasizes consequences for programmers Divided in five parts the book covers the basics of digital logic gates and data paths as well as the three primary aspects of architecture processors memories and I/O systems The book also covers advanced topics of parallelism pipelining power and energy and performance A hands on lab is also included The second edition contains three new chapters as well as changes and updates throughout *IBM*

High-Performance Computing Insights with IBM Power System AC922 Clustered Solution Dino Quintero, Miguel Gomez Gonzalez, Ahmad Y Hussein, Jan-Frode Myklebust, IBM Redbooks, 2019-05-02 This IBM Redbooks publication documents and addresses topics to set up a complete infrastructure environment and tune the applications to use an IBM POWER9™ hardware architecture with the technical computing software stack This publication is driven by a CORAL project solution It explores tests and documents how to implement an IBM High Performance Computing HPC solution on a POWER9 processor based system by using IBM technical innovations to help solve challenging scientific technical and business problems This book documents the HPC clustering solution with InfiniBand on IBM Power Systems™ AC922 8335 GTH and 8335 GTX servers with NVIDIA Tesla V100 SXM2 graphics processing units GPUs with NVLink software components and the IBM Spectrum™ Scale parallel file system This solution includes recommendations about the components that are used to provide a cohesive clustering environment that includes job scheduling parallel application tools scalable file systems administration tools and a high speed interconnect This book is divided into three parts Part 1 focuses on the planners of the solution Part 2 focuses on the administrators and Part 3 focuses on the developers This book targets technical professionals consultants technical support staff IT architects and IT specialists who are responsible for delivering cost effective HPC solutions that help uncover insights among clients data so that they can act to optimize business results product development and scientific discoveries

High Performance Parallel Runtimes Michael Klemm, Jim Cownie, 2021-02-08 This book focuses on the theoretical and practical aspects of parallel programming systems for today's high performance multi core processors and discusses the efficient implementation of key algorithms needed to implement parallel programming models Such implementations need to take into account the specific architectural aspects of the underlying computer architecture and the features offered by the execution environment This book briefly reviews key concepts of modern computer architecture focusing particularly on the performance of parallel codes as well as the relevant concepts in parallel programming models The book then turns towards the fundamental algorithms used to implement the parallel programming models and discusses how they interact with modern processors While the book will focus on the general mechanisms we will mostly use the Intel

processor architecture to exemplify the implementation concepts discussed but will present other processor architectures where appropriate All algorithms and concepts are discussed in an easy to understand way with many illustrative examples figures and source code fragments The target audience of the book is students in Computer Science who are studying compiler construction parallel programming or programming systems Software developers who have an interest in the core algorithms used to implement a parallel runtime system or who need to educate themselves for projects that require the algorithms and concepts discussed in this book will also benefit from reading it You can find the source code for this book at <https://github.com/parallel-runtimes/lomp>

Proceedings: Supercomputer design, performance evaluation, and performance education, 1987 *Computer Performance Evaluation Users Group (CPEUG)* Computer Performance Evaluation Users Group, 1980 **Implementing an IBM High-Performance Computing Solution on IBM Power System S822LC** Dino Quintero, Luis Carlos Cruz Huertas, Tsuyoshi Kamenoue, Wainer dos Santos Moschetta, Mauricio Faria de Oliveira, Georgy E Pavlov, Alexander Pozdneev, IBM Redbooks, 2016-07-25 This IBM Redbooks publication demonstrates and documents that IBM Power Systems™ high performance computing and technical computing solutions deliver faster time to value with powerful solutions Configurable into highly scalable Linux clusters Power Systems offer extreme performance for demanding workloads such as genomics finance computational chemistry oil and gas exploration and high performance data analytics This book delivers a high performance computing solution implemented on the IBM Power System S822LC The solution delivers high application performance and throughput based on its built for big data architecture that incorporates IBM POWER8 processors tightly coupled Field Programmable Gate Arrays FPGAs and accelerators and faster I/O by using Coherent Accelerator Processor Interface CAPI This solution is ideal for clients that need more processing power while simultaneously increasing workload density and reducing datacenter floor space requirements The Power S822LC offers a modular design to scale from a single rack to hundreds simplicity of ordering and a strong innovation roadmap for graphics processing units GPUs This publication is targeted toward technical professionals consultants technical support staff IT Architects and IT Specialists responsible for delivering cost effective high performance computing HPC solutions that help uncover insights from their data so they can optimize business results product development and scientific discoveries

Computer Performance Evaluation: Modelling Techniques and Tools Tony Field, Peter G. Harrison, Jeremy Bradley, Uli Harder, 2003-08-01 This book constitutes the refereed proceedings of the 12th International Conference on Modelling Techniques and Tools for Computer Performance Evaluation TOOLS 2002 held in London UK in April 2002 The 18 revised full papers and six tool papers presented together with an invited contribution were carefully reviewed and selected from 57 submissions Among the topics addressed are generic techniques like stochastic process algebras and the analysis of Petri nets and Markov chains as well as the development and employment of tools in areas such as the Internet software performance engineering parallel systems real time systems and transaction processing **Computerworld**, 1993-01-11

For more than 40 years Computerworld has been the leading source of technology news and information for IT influencers worldwide Computerworld's award winning Web site Computerworld.com twice monthly publication focused conference series and custom research form the hub of the world's largest global IT media network

Performance Evaluation and Benchmarking Lizy Kurian John, Lieven Eeckhout, 2018-10-03 Computer and microprocessor architectures are advancing at an astounding pace However increasing demands on performance coupled with a wide variety of specialized operating environments act to slow this pace by complicating the performance evaluation process Carefully balancing efficiency and accuracy is key to avoid slowdowns and such a balance can be achieved with an in depth understanding of the available evaluation methodologies Performance Evaluation and Benchmarking outlines a variety of evaluation methods and benchmark suites considering their strengths weaknesses and when each is appropriate to use Following a general overview of important performance analysis techniques the book surveys contemporary benchmark suites for specific areas such as Java embedded systems CPUs and Web servers Subsequent chapters explain how to choose appropriate averages for reporting metrics and provide a detailed treatment of statistical methods including a summary of statistics how to apply statistical sampling for simulation how to apply SimPoint and a comprehensive overview of statistical simulation The discussion then turns to benchmark subsetting methodologies and the fundamentals of analytical modeling including queuing models and Petri nets Three chapters devoted to hardware performance counters conclude the book Supplying abundant illustrations examples and case studies Performance Evaluation and Benchmarking offers a firm foundation in evaluation methods along with up to date techniques that are necessary to develop next generation architectures

Information Security Applications Brent ByungHoon Kang, Taesoo Kim, 2018-06-22 This book constitutes the thoroughly refereed post conference proceedings of the 18th International Conference on Information Security Applications WISA 2017 held on Jeju Island Korea in August 2017 The 12 revised full papers and 15 short papers presented in this volume were carefully reviewed and selected from 53 submissions The papers are organized in topical sections such as attack and defense theory in security web security and emerging technologies systems security and authentication crypto protocols and attack detections and legal aspects

Performance Measurement in Corporate Governance Alex Manzoni, Sardar M. N. Islam, 2009-04-30 In the global knowledge economy corporate governance organisational behaviour and performance of the supply chain are becoming increasingly important aspects of the evaluation of an enterprise The subject of this book is the development of a contemporary organisation behaviour performance measurement OBPM model for enterprises in the modern economy The fields of organisation behaviour and supply chain management are integrated with an Open Socio Technical Systems theory of management and the application of Operations Research to corporate governance for the measurement of organisation performance This book thereby offers a new and innovative quantitative approach to qualitative concepts of corporate performance measurement and makes a significant contribution to the fields of management theory supply chain

management as well as operations research *Proceedings of the 5th International Workshop on Reconfigurable Communication-centric Systems on Chip 2010 - ReCoSoC'10* Michael Hübner, 2010 *High Performance Computing: Technology, Methods and Applications* J.J. Dongarra, L. Grandinetti, J. Kowalik, G.R. Joubert, 1995-09-13 High Performance Computing is an integrated computing environment for solving large scale computational demanding problems in science engineering and business Newly emerging areas of HPC applications include medical sciences transportation financial operations and advanced human computer interface such as virtual reality High performance computing includes computer hardware software algorithms programming tools and environments plus visualization The book addresses several of these key components of high performance technology and contains descriptions of the state of the art computer architectures programming and software tools and innovative applications of parallel computers In addition the book includes papers on heterogeneous network based computing systems and scalability of parallel systems The reader will find information and data relative to the two main thrusts of high performance computing the absolute computational performance and that of providing the most cost effective and affordable computing for science industry and business The book is recommended for technical as well as management oriented individuals

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