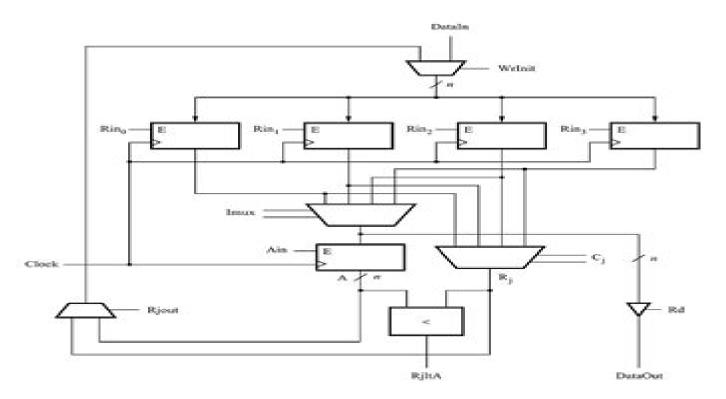
From the ASM chart, we can see that the datapath circuit needs a multiplexer to allow the operation $R_i \leftarrow R_j$. An appropriate datapath is shown below.



An ASM chart for the control circuit is

<u>Fundamentals Of Digital Logic With Verilog Design</u> <u>Solutions Manual</u>

Gideon Langholz, Abraham Kandel, Joe L. Mott

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