

QUESTION PAPER CODE : 11343
B.E./B.TECH. DEGREE EXAMINATION,
NOVEMBER/DECEMBER 2012.

Sixth Semester
Electronics and Communication Engineering
EC 2354/EC64—VLSI DESIGN
(Regulation 2008)

(Common to PTEC 2354—VLSI Design for B.E. (Part-Time)
Fifth Semester-Electronics and Communication
Engineering-Regulation 2009)

Time : Three hours

Maximum : 100 marks

Answer ALL questions.

PART A—(10 × 2 = 20 marks)

1. Why the tunneling current is higher for NMOS transistors than PMOS transistors with silica gate?
2. What is the objective of layout rules?
3. Give the effect of supply voltage and temperature variations on the CMOS system performance.
4. What are the factors that cause static power dissipation in CMOS circuits?
5. State the reasons for the speed advantage of CVSL family.
6. Mention the qualities of an ideal sequencing method.
7. Distinguish testers and test fixtures.
8. What are the stages at which a chip can be tested?
9. Write the Verilog module for a 1-bit full adder.
10. Give an example for implicit continuous assignment.

PART B—(5 × 16 = 80 marks)

- 11.(a)(i) Explain the different steps involved in n-well CMOS fabrication process with neat diagrams. (10)

Low Power Vlsi Design Question Paper

**Rakesh Kumar Garg,Ashish
Dixit,Pavan Yadav**



Low Power Vlsi Design Question Paper:

Basic Electronics Rakesh Kumar Garg, Ashish Dixit, Pavan Yadav, 2008 **VLSI-SoC: Design for Reliability, Security, and Low Power** Youngsoo Shin, Chi Ying Tsui, Jae-Joon Kim, Kiyoungh Choi, Ricardo Reis, 2016-09-12 This book contains extended and revised versions of the best papers presented at the 23rd IFIP WG 10.5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2015 held in Daejeon Korea in October 2015. The 10 papers included in the book were carefully reviewed and selected from the 44 full papers presented at the conference. The papers cover a wide range of topics in VLSI technology and advanced research. They address the current trend toward increasing chip integration and technology process advancements bringing about new challenges both at the physical and system design levels as well as in the test of these systems Low-Power CMOS Design Anantha Chandrakasan, Robert W. Brodersen, 1998-02-11 This collection of important papers provides a comprehensive overview of low power system design from component technologies and circuits to architecture system design and CAD techniques. LOW POWER CMOS DESIGN summarizes the key low power contributions through papers written by experts in this evolving field Digest of Technical Papers, 1999 **Proceedings, 1997 International Symposium on Low Power Electronics and Design** IEEE Circuits and Systems Society, 1997 **A Handbook On Multidisciplinary Approaches In Research (Volume-1)** Er. Sandeep Bishla, Dr. Sahab Ram Kumawat, 2023-04-28 This chapter has a dual purpose. In the first place, the authors provide a real world example of interdisciplinary research by discussing the two chapter examples they worked on while editing a book full of multidisciplinary cases. The authors' purpose is to provide a realistic picture of how the theoretical aim of interdisciplinary research might be realised in practice in contrast to the numerous theoretical descriptions that have been published on the topic. The author gives the present conceptual understanding of the multidisciplinary before elaborating on the practical use of these ideas in light of the common restrictions that many academics encounter today while undertaking cooperative research. The book provides suggestions on how to improve cross disciplinary work in the future and share their own experiences conducting interdisciplinary studies. Students' expectations about their own Internet computer skills and their capacity to complete online courses are explored in this book with research on online education self efficacy. The relevance of culture in the workplace is shown by the fact that several studies have examined the connection between business culture and factors like productivity and lifespan. As well as having a significant impact on a broad variety of organisational processes, employees' performance has long been seen as a critical component in integrating the various business cultures within corporate group organisation. This book aims to chart the history of the electrochemical science from its inception as a separate discipline to the present day *VLSI Design 2001 : Fourteenth International Conference on VLSI Design* VLSI Society of India, 2001 The International Conference on VLSI Design was started in 1985 as a workshop and from this start has grown into an international conference on VLSI design. The proceedings are dedicated to all aspects of integrated circuit

design technology and related computer aided design CAD Scientific and Technical Aerospace Reports ,1995

Proceedings of ASP-DAC/VLSI Design 2002 ,2002 Papers from a January 2002 conference are organized into four sessions each on low power design synthesis testing layout and interconnects and technology as well as two sessions each on embedded systems verification and VLSI architecture one session on analog design and one session on hot c *Eleventh International Conference on VLSI Design* VLSI Society of India,IEEE Circuits and Systems Society,1997 Areas covered in this work include physical design synthesis delay test and timing high level synthesis hardware software co design low power design verification VLSI synthesis testability enhancement asynchronous design diagnosis test and fault modelling and mixed signal design *Energy-Efficient Distributed Computing Systems* Albert Y. Zomaya,Young Choon Lee,2012-07-26 The energy consumption issue in distributed computing systems raises various monetary environmental and system performance concerns Electricity consumption in the US doubled from 2000 to 2005 From a financial and environmental standpoint reducing the consumption of electricity is important yet these reforms must not lead to performance degradation of the computing systems These contradicting constraints create a suite of complex problems that need to be resolved in order to lead to greener distributed computing systems This book brings together a group of outstanding researchers that investigate the different facets of green and energy efficient distributed computing Key features One of the first books of its kind Features latest research findings on emerging topics by well known scientists Valuable research for grad students postdocs and researchers Research will greatly feed into other technologies and application domains **Proceedings of the ... ACM Great Lakes Symposium on VLSI.** ,2005 **VLSI Systems and Computations** H.T. Kung,R. Sproull,G. Steele,2012-12-06 The papers in this book were presented at the CMU Conference on VLSI Systems and Computations held October 19 21 1981 in Pittsburgh Pennsylvania The conference was organized by the Computer Science Department Carnegie Mellon University and was partially supported by the National Science Foundation and the Office of Naval Research These proceedings focus on the theory and design of computational systems using VLSI Until very recently integrated circuit research and development were concentrated in the device physics and fabrication design disciplines and in the integrated circuit industry itself Within the last few years a community of researchers is growing to address issues closer to computer science the relationship between computing structures and the physical structures that implement them the specification and verification of computational processes implemented in VLSI the use of massively parallel computing made possible by VLSI the design of special purpose computing architectures and the changes in general purpose computer architecture that VLSI makes possible It is likely that the future exploitation of VLSI technology depends as much on structural and design innovations as on advances in fabrication technology The book is divided into nine sections Invited Papers Six distinguished researchers from industry and academia presented invited papers Models of Computation The papers in this section deal with abstracting the properties of VLSI circuits into models that can be used to analyze the chip area time or energy required

for a particular computation Proceedings of the First International Conference on Advanced Data and Information Engineering (DaEng-2013) Tutut Herawan, Mustafa Mat Deris, Jemal Abawajy, 2013-12-14 The proceeding is a collection of research papers presented at the International Conference on Data Engineering 2013 DaEng 2013 a conference dedicated to address the challenges in the areas of database information retrieval data mining and knowledge management thereby presenting a consolidated view to the interested researchers in the aforesaid fields The goal of this conference was to bring together researchers and practitioners from academia and industry to focus on advanced on data engineering concepts and establishing new collaborations in these areas The topics of interest are as follows but are not limited to Database theory Data management Data mining and warehousing Data privacy security Information retrieval integration and visualization Information system Knowledge discovery in databases Mobile grid and cloud computing Knowledge based Knowledge management Web data services and intelligence **Proceedings of the ASP-DAC ... Asia and South Pacific Design Automation Conference** ,2002 **Electrical & Electronics Abstracts** ,1997 *VLSI Science and Technology* ,1984 *Proceedings of the ... Midwest Symposium on Circuits and Systems* ,1999 **IEEE Computer Society Workshop on VLSI 2000** Asim Smailagic, Robert W. Brodersen, H. De Man, 2000 Contains 23 papers from the April 2000 workshop which identified system level design as a dominant VLSI research theme for the next decade System design is converging on a model which combines general purpose commodity chips and full custom mixed analogy with digital application specific integrated circuits integrated via programmable gate arrays on custom printed circuit boards or complete silicon boards creating a system on a chip Some of the papers discuss the constraints of complexity power consumption heat dissipation mechanical packaging ergonomics and design effort Other major topics are timing issues analysis and synthesis of asynchronous circuits and advances in multiplier design No subject index Annotation copyrighted by Book News Inc Portland OR *VLSI Science and Technology/1984* Kenneth E. Bean, George A. Rozgonyi, 1984

The book delves into Low Power Vlsi Design Question Paper. Low Power Vlsi Design Question Paper is a vital topic that must be grasped by everyone, ranging from students and scholars to the general public. The book will furnish comprehensive and in-depth insights into Low Power Vlsi Design Question Paper, encompassing both the fundamentals and more intricate discussions.

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