

8086 Instruction Set

Data Movement — Abbreviations

src = source (מקור)

dest = destination (יעד)

acc = accumulator (AL or AX)

PTR = pointer (מצביע)

DWORD = double word (32 bits)

. = concatenation (AX = AH.AL)

Data Movement — Conventions

AX ← BX

Copy BX to AX

REGS[AX] ←  **REGS[BX]**


AL ← [1000]

Copy memory byte at address 1000 to AL

REGS[AL] ←  **MEM[1000]**

AL ← [BX]

Copy memory byte at address stored in BX to AL

REGS[AL] ←  **MEM[REGS[BX]]**

Data Movement — Scope

BYTE PTR[1000]

Memory byte at address 1000

WORD PTR[1000]

Memory word (2 bytes) at address 1000 and address 1001

DWORD PTR[1000]

Memory dword (4 bytes) at addresses
1000, 1001, 1002, 1003

AX ← [BX]

AX ← WORD PTR[BX]

AL ← [BX] and AH ← [BX+1]

REGS[AL] ←, MEM[REGS[BX]]

REGS[AH] ←, MEM[REGS[BX] + 1]

Instructions Set Of 8086

Mr. Rohit Manglik



Instructions Set Of 8086:

Advanced Microprocessor & Microcontrollers S. K. Venkata Ram, 2004 *Microprocessor 8086 : Architecture, Programming and Interfacing* Mathur Sunil, 2010-12 Primarily intended for the undergraduate students of electronics and communication engineering computer science and engineering and information technology this book skilfully integrates both the hardware and software aspects of the 8086 microprocessor It offers the students an up to date account of the state of the art microprocessors and therefore can be regarded as an incomparable source of information on recently developed microprocessor chips The book covers the advanced microprocessor architecture of the Intel microprocessor family from 8086 to Pentium 4 The text is organized in four parts Part I Chapters 1 7 includes a detailed description of the architecture organization instruction set and assembler directives of microprocessor 8086 Part II Chapters 8 11 discusses the math coprocessor multiprocessing and multiprogramming the different types of data transfer schemes and memory concepts Part III Chapters 12 15 covers programmable interfacing chips with the help of extensive interfacing examples Part IV Chapters 16 18 deals with advanced processors from 80186 to Pentium 4 This well organized and student friendly text should prove to be an invaluable asset to the students as well as the practising engineers **KEY FEATURES** Gives elaborate programming examples to develop the analytical ability of students Provides solved examples covering different types of typical interfacing problems to develop the practical skills of students Furnishes chapter end exercises to reinforce the understanding of the subject *Microprocessors and Microcomputer-Based System Design* Mohamed Rafiquzzaman, 2021-11-01 Microprocessors and Microcomputer Based System Design Second Edition builds on the concepts of the first edition It discusses the basics of microprocessors various 32 bit microprocessors the 8085 microprocessor the fundamentals of peripheral interfacing and Intel and Motorola microprocessors This edition includes new topics such as floating point arithmetic Program Array Logic and flash memories It covers the popular Intel 80486 80960 and Motorola 68040 as well as the Pentium and PowerPC microprocessors The final chapter presents system design concepts applying the design principles covered in previous chapters to sample problems **DIGITAL ELECTRONICS - II**, 2025-03-21 TP SOLVED SERIES For BCA Bachelor of Computer Applications Part II Fourth Semester Rashtrasant Tukadoji Maharaj Nagpur University RTMNU **Computer Organization** Mr. Rohit Manglik, 2024-05-24 Studies computer architecture and organization Covers processors memory and I O systems providing a foundation for designing and understanding computing systems **Computer Architecture and Organization: From 8085 to core2Duo & beyond** Subrata Ghoshal, 2011 The book uses microprocessors 8085 and above to explain the various concepts It not only covers the syllabi of most Indian universities but also provides additional information about the latest developments like Intel Core II Duo making it one of the most updated textbook in the market The book has an excellent pedagogy sections like food for thought and quicksand corner make for an interesting read **Computer Architecture** John Y. Hsu, 2017-12-19 With the new developments in computer architecture fairly recent

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