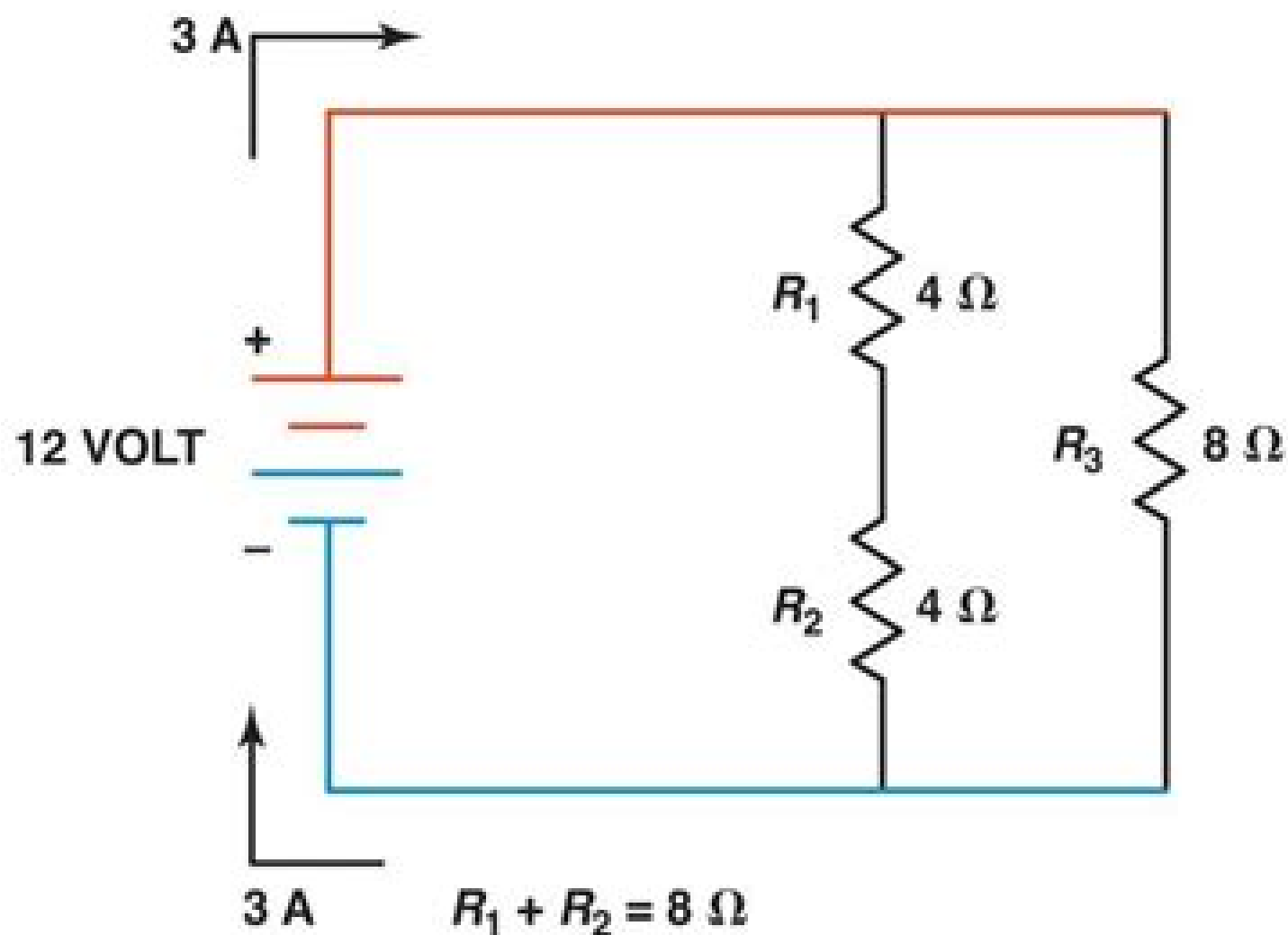




Figure 41-25 Solving a series-parallel circuit problem.



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precise in relation to the great imprecision of the world around us This book aims at attracting researchers and engineers both in the fields of industrial electronics IE and computational intelligence CI By approaching the different viewpoints of IE and CI people it is hoped to provide practicing engineers with new solutions to the demanding real world problems The applications are divided into two categories Electric Power Applications and Emerging Applications

Proceedings of the IEEE International Symposium on Industrial Electronics ,2001

Analysis and Simulation of Noise in Nonlinear Electronic Circuits and Systems Alper Demir,Alberto Sangiovanni-Vincentelli,2012-12-06 In electronic circuit and system design the word noise is used to refer to any undesired excitation on the system In other contexts noise is also used to refer to signals or excitations which exhibit chaotic or random behavior The source of noise can be either internal or external to the system For instance the thermal and shot noise generated within integrated circuit devices are internal noise sources and the noise picked up from the environment through electromagnetic interference is an external one Electromagnetic interference can also occur between different components of the same system In integrated circuits the signals in one part of the system can propagate to the other parts of the same system through electromagnetic coupling power supply lines and the substrate For instance in a mixed signal IC the switching activity in the digital parts of the circuit can adversely affect the performance of the analog section of the circuit by traveling through the power supply lines and the substrate Prediction of the effect of these noise sources on the performance of an electronic system is called noise analysis or noise simulation A methodology for the noise analysis or simulation of an electronic system usually has the following four components

2 NOISE IN NONLINEAR ELECTRONIC CIRCUITS Mathematical representations or models for the noise sources Mathematical model or representation for the system that is under the influence of the noise sources

Electronic Design Automation for IC Implementation, Circuit Design, and Process Technology Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-02-03 The second of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic RTL to GDSII a file format used to transfer data of semiconductor physical layout design flow analog mixed signal design physical verification and technology computer aided design TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability DFM at the nanoscale power supply network design and analysis design modeling and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on 3D circuit integration and clock design Offering improved depth and modernity Electronic Design Automation for

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