

(N.B. : All questions are compulsory.)

1. (a) Give classification of memory. 10
(b) What is latch ? Describe its function.
2. Attempt any three of the following :— 15
 - (a) Distinguish between SRAM and DRAM.
 - (b) Explain encoder with suitable example.
 - (c) Explain tri-state buffer in detail.
 - (d) Explain related to memory :—
 - (i) Expanding memory. (ii) Expanding word size.
3. Attempt any three of the following :— 15
 - (a) State functions of ALU of 8085.
 - (b) What is flag ? List and Describe the flags of 8085.
 - (c) Write short note on 8085 Address, Data and Control Bus.
 - (d) State and describe the interrupt signal available in 8085.
4. Attempt any three of the following :— 15
 - (a) Classify the instructions on the basis of byte length with example.
 - (b) Write an assembly language program to find the smallest number from memory block starting from 2000 H to 2005 H and store largest number in 2020 H.
 - (c) Explain following instructions in 8085 :—

(i) ADI Data 8	(iv) J-condition address
(ii) STAX rp	(v) RAR.
(iii) ANA r	
 - (d) Explain the programming model of 8085.
5. Attempt any three of the following :— 15
 - (a) What are the benefits of Raid ?
 - (b) What is cache memory ? Why it is needed ?
 - (c) What is fetch, Execute and Instruction cycle ?
 - (d) Explain the features of PCI Bus.
6. Attempt any three of the following :— 15
 - (a) Explain the features of 8051 micro-controller.
 - (b) Explain all 8051 flags of micro-controller.
 - (c) Explain the operations of stack in 8051 micro-controller.
 - (d) Explain how will you select internal memory bank with suitable example.
7. Attempt any three of the following :— 15
 - (a) Explain the features of Input/output ports of 8051.
 - (b) Explain various addressing modes of 8051.
 - (c) Explain the following instructions of 8051 :—

(i) MOVX A, Rp	(iv) MUL AB
(ii) INC A	(v) RLA.
(iii) Add R _i R _j .	
 - (d) Write a program to perform in 8051 BCD addition of two-8bit numbers. Data is stored in data memory location 51H, 52H and store the result in data memory. Location 53H and carry in 54H.

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Jianjun Gao



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Table of Contents Model Question Paper Of Advanced Microprocessor 2013

1. Understanding the eBook Model Question Paper Of Advanced Microprocessor 2013
 - The Rise of Digital Reading Model Question Paper Of Advanced Microprocessor 2013
 - Advantages of eBooks Over Traditional Books
2. Identifying Model Question Paper Of Advanced Microprocessor 2013
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Model Question Paper Of Advanced Microprocessor 2013

- User-Friendly Interface
- 4. Exploring eBook Recommendations from Model Question Paper Of Advanced Microprocessor 2013
 - Personalized Recommendations
 - Model Question Paper Of Advanced Microprocessor 2013 User Reviews and Ratings
 - Model Question Paper Of Advanced Microprocessor 2013 and Bestseller Lists
- 5. Accessing Model Question Paper Of Advanced Microprocessor 2013 Free and Paid eBooks
 - Model Question Paper Of Advanced Microprocessor 2013 Public Domain eBooks
 - Model Question Paper Of Advanced Microprocessor 2013 eBook Subscription Services
 - Model Question Paper Of Advanced Microprocessor 2013 Budget-Friendly Options
- 6. Navigating Model Question Paper Of Advanced Microprocessor 2013 eBook Formats
 - ePub, PDF, MOBI, and More
 - Model Question Paper Of Advanced Microprocessor 2013 Compatibility with Devices
 - Model Question Paper Of Advanced Microprocessor 2013 Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Model Question Paper Of Advanced Microprocessor 2013
 - Highlighting and Note-Taking Model Question Paper Of Advanced Microprocessor 2013
 - Interactive Elements Model Question Paper Of Advanced Microprocessor 2013
- 8. Staying Engaged with Model Question Paper Of Advanced Microprocessor 2013
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Model Question Paper Of Advanced Microprocessor 2013
- 9. Balancing eBooks and Physical Books Model Question Paper Of Advanced Microprocessor 2013
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Model Question Paper Of Advanced Microprocessor 2013
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Model Question Paper Of Advanced Microprocessor 2013
 - Setting Reading Goals Model Question Paper Of Advanced Microprocessor 2013

- Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Model Question Paper Of Advanced Microprocessor 2013
 - Fact-Checking eBook Content of Model Question Paper Of Advanced Microprocessor 2013
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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