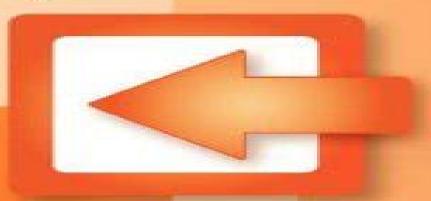
Embedded System Design

Modeling, Synthesis and Verification



Daniel D. Gajski Samar Abdi Andreas Gerstlauer Gunar Schirner

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