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web i2c module modes and features the i2c module provides the following operational modes and features master mode slave mode with byte nacking multi master mode dedicated receive and transmit buffers up to four dedicated slave address registers 1

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web the i 2 c master is byte oriented and interrupt based the number of interrupts generated is kept at a minimum by automatic handling of most incidents the software driver complexity and code size are reduced by auto triggering of operations and a special smart mode which can be enabled by the smart mode enable bit in the control b register

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web lbidirectional i2c stands for inter integrated circuit communications i2c is implemented in the picmicro by a hardware module called the master synchronous serial port known as the mssp module

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web the master synchronous serial port mssp is an integrated serial communications module the mssp contains two sub modules spi serial peripheral interface i2c inter integrated circuit the inter integrated circuit commonly referred to as i2c is a synchronous two wire bidirectional serial communications bus

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web jun 23 2016 key features of the i2c module include the following independent master and slave logic multi master support which prevents message losses in arbitration detects 7 bit and 10 bit device addresses with configurable address masking in slave mode detects general call addresses as defined in the i2c protocol automatic sclx clock

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