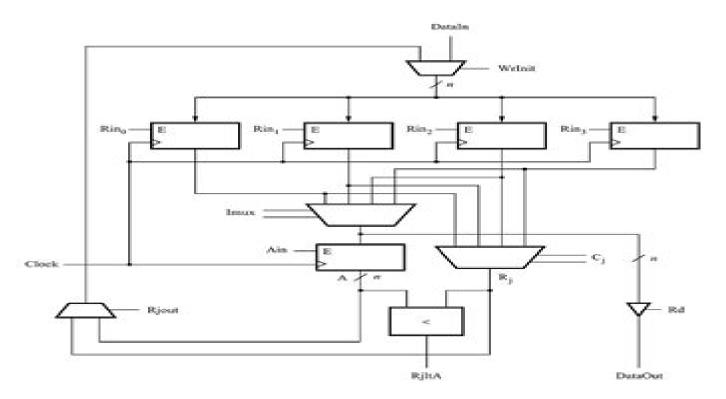
From the ASM chart, we can see that the datapath circuit needs a multiplexer to allow the operation  $R_i \leftarrow R_j$ . An appropriate datapath is shown below.



An ASM chart for the control circuit is

# <u>Fundamentals Of Digital Logic With Verilog Design</u> <u>Solutions Manual</u>

Lord Brown, Stephen D. Brown, Zvonko G. Vranesic

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teach the student to use the Quartus CAD the book includes three tutorials

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concepts are illustrated by using small examples which are easy to understand Then a modular approach is used to show how larger circuits are designed VHDL is a complex language so it is introduced gradually in the book Each VHDL feature is presented as it becomes pertinent for the circuits being discussed While it includes a discussion of VHDL the book provides thorough coverage of the fundamental concepts of logic circuit design independent of the use of VHDL and CAD tools A CD ROM containg all of the VHDL design examples used in the book as well Altera's Quartus II CAD software is included free with every text

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