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<b>Question Paper Code : 21266</b>
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B.E./B.Tech. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2012.

Sixth Semester

Electronics and Communication Engineering

080290040 — MICROCONTROLLERS AND RISC ARCHITECTURE

(Regulation 2008)

Time : Three hours

Maximum : 100 marks

Answer ALL questions.

PART A — (10 × 2 = 20 marks)

1. How does the pin  $\overline{EA}$  of 8051 help in distinguishing the address range of program memory?
2. Show the circuit used for power down operation of 8051.
3. What are the data types and operand addressing supported in 8051?
4. The carry flag is originally set. The data present at the input port 3 is C5H. The data previously written to output port 1 is 35H calculate the port 1 contents after the execution of the following instructions.  
MOV P1.3, C  
MOV C, P3.3  
MOV P1.2, C
5. How do you generate the data corresponding to the step angle required in stepped motor interfacing?
6. Find an application that uses the auto - reload mode of timer 2 in 8051. Justify your answer.
7. List the features of ARM architecture that are not shared by most other RISC architectures.
8. Give an example for pre-indexed addressing mode of ARM.
9. Tabulate the arm- exceptions with their vector address.
10. What are the common sources of memory fault allowed by ARM processors?

# Microcontroller And Risc Architecture Question Paper

**YCT Expert Team**



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*Microprocessor and Microcontroller Interview Questions:* Anita Gehlot Rajesh Singh, 2020-01-01 Crack the Microprocessor and Microcontroller Interview Description Book gives you a complete idea about the Microcontroller and Microprocessor. It starts from a very basic concept like a number system then explains the digital circuit. This book is a complete set of interview questions and answers with plenty of screenshots. Book takes you on a journey to Microprocessor 8085 Peripheral Devices and Interfacing AVR ATmega32 Interfacing of Input Output Device. Book also covers the descriptive questions, multiple choice questions along with answers which are asked during an interview. Key features: An ample number of diagrams are used to illustrate the subject matter for easy understanding. Set of review questions with answers are added at the end for better understanding. Includes basic to advanced interview questions on 8085, 8086, 89C51 PIC and AVR interfacing of input output devices. It will help to enhance the programming skills of the reader. What will you learn: Basics to an advanced interview question for microprocessor 8085, 8086 and microcontroller 89C51 PIC and AVR. Question on interfacing of input output devices. Who this book is for: Engineering students pursuing a course in electrical and electronics, electronics and communication, computer science and information technology who wish to learn about Microprocessor, Microcontroller and crack an interview. Table of Contents: 1. Number Systems 2. Digital Circuit 3. Microprocessor 8085 4. Peripheral Devices and Interfacing 5. AVR ATmega32 6. Interfacing of Input Output Device 7. Exercise 8. Descriptive Type Questions 9. Multiple Choice Questions.

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**Safety of Computer Control Systems 1992 (SAFECOMP' 92)** H.H. Frey,2014-05-23 SAFECOMP 92 advances the state of the art reviews experiences of the past years considers the guidance now available and identifies the skills methods tools and techniques required for the safety of computer control systems *Dependable Computing - EDCC 2005* Mario Dal Cin,2005-04-13 This book constitutes the refereed proceedings of the 5th European Dependable Computing Conference EDCC 2005 held in Budapest Hungary in April 2005 The 21 revised full papers 5 revised practical experience reports and 4 prototype description papers presented together with the abstract of a keynote and 2 fast track papers were carefully reviewed and selected from 90 submissions The papers are organized in topical sections on distributed algorithms

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