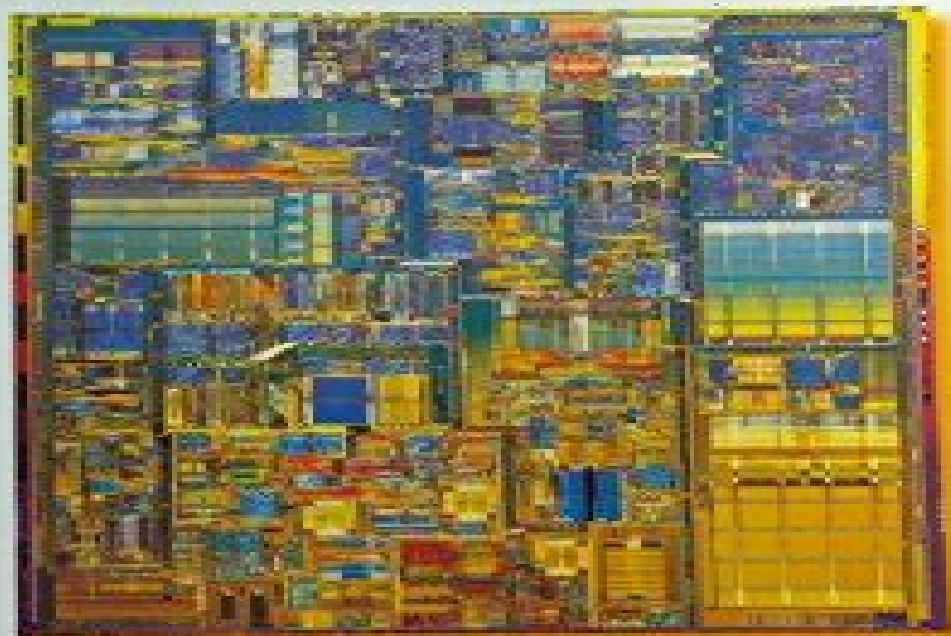




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VOLUME 1: BASIC ARCHITECTURE



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of a reliable one shot remote exploit for a real vulnerability a bug affecting the SCTP subsystem found in the Linux kernel Finally Part IV wraps up the analysis on kernel exploitation and looks at what the future may hold Covers a range of operating system families UNIX derivatives Mac OS X Windows Details common scenarios such as generic memory corruption stack overflow heap overflow etc issues logical bugs and race conditions Delivers the reader from user land exploitation to the world of kernel land OS exploits attacks with a particular focus on the steps that lead to the creation of successful techniques in order to give to the reader something more than just a set of tricks

Hardware Acceleration of Computational Holography Tomoyoshi Shimobaba, Tomoyoshi Ito, 2023-07-17 This book explains the hardware implementation of computational holography and hardware acceleration techniques along with a number of concrete example source codes that enable fast computation Computational holography includes computer based holographic technologies such as computer generated hologram and digital holography for which acceleration of wave optics computation is highly desirable This book describes hardware implementations on CPUs Central Processing Units GPUs Graphics Processing Units and FPGAs Field Programmable Gate Arrays This book is intended for readers involved in holography as well as anyone interested in hardware acceleration

Microprocessor 1 Philippe Darche, 2020-10-29 Since its commercialization in 1971 the microprocessor a modern and integrated form of the central processing unit has continuously broken records in terms of its integrated functions computing power low costs and energy saving status Today it is present in almost all electronic devices Sound knowledge of its internal mechanisms and programming is essential for electronics and computer engineers to understand and master computer operations and advanced programming concepts This book in five volumes focuses more particularly on the first two generations of microprocessors those that handle 4 and 8 bit integers Microprocessor 1 the first of five volumes presents the computation function recalls the memory function and clarifies the concepts of computational models and architecture A comprehensive approach is used with examples drawn from current and past technologies that illustrate theoretical concepts making them accessible

IA-32 Intel Architecture Optimization Reference Manual Intel Corporation, 2005

Intel 64 and IA-32 Architectures Optimization Reference Manual Intel Corporation, 1997

Microprocessor 4 Philippe Darche, 2021-02-17 Since its commercialization in 1971 the microprocessor a modern and integrated form of the central processing unit has continuously broken records in terms of its integrated functions computing power low costs and energy saving status Today it is present in almost all electronic devices Sound knowledge of its internal mechanisms and programming is essential for electronics and computer engineers to understand and master computer operations and advanced programming concepts This book in five volumes focuses more particularly on the first two generations of microprocessors those that handle 4 and 8 bit integers Microprocessor 4 the fourth of five volumes addresses the software aspects of this component Coding of an instruction addressing modes and the main features of the Instruction Set Architecture ISA of a generic component are presented Furthermore two approaches are discussed for altering the flow of

execution using mechanisms of subprogram and interrupt A comprehensive approach is used with examples drawn from current and past technologies that illustrate theoretical concepts making them accessible

Handbook of Computer Architecture Anupam Chattopadhyay, 2024-12-20 This handbook presents the key topics in the area of computer architecture covering from the basic to the most advanced topics including software and hardware design methodologies It will provide readers with the most comprehensive updated reference information covering applications in single core processors multicore processors application specific processors reconfigurable architectures emerging computing architectures processor design and programming flows test and verification This information benefits the readers as a full and quick technical reference with a high level review of computer architecture technology detailed technical descriptions and the latest practical applications

Programmable Digital Signal Processors Yu Hen Hu, 2001-12-06 Presents the latest developments in the programming and design of programmable digital signal processors PDSPs with very long instruction word VLIW architecture algorithm formulation and implementation and modern applications for multimedia processing communications and industrial control

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