

MOSFET Cross Reference Guide

Industry Part Number	Recommended Fairchild Device	Package
2N7000	2N7000	TO-92, N
2N7002	2N7002	SOT-23, N
BS-170	BS-170	TO-92, N
BS270	BS270	TO-92, N
BSS100	BSS100	TO-92, N
BSS110	BSS110	TO-92, P
BSS123	BSS123	SOT-23, N
BSS138	BSS138	SOT-23, N
BSS84	BSS84	SOT-23, P
IRF1010E	NDP7061L	TO-220, N
IRF1010N	NDP7060L	TO-220, N
IRF1104	NDP7052L	TO-220, N
IRF1310N	NDP710A	TO-220, N
IRF1310S	NDB710A	TO-263, N
IRF2807	NDP706A	TO-220, N
IRF2807S	NDB408A	TO-263, N
IRF3205	NDP7052L	TO-220, N
IRF3710	NDP710A	TO-220, N
IRF510	NDP410A	TO-220, N
IRF520	NDP410A	TO-220, N
IRF520N	NDP510A	TO-220, N
IRF520S	NDB410A	TO-263, N
IRF530	NDP510A	TO-220, N
IRF530N	NDP510A	TO-220, N
IRF540	NDP610A	TO-220, N
IRF540N	NDP710A	TO-220, N
IRF540S	NDP610A	TO-220, N
IRF7101	NDS9956A	SO-8, Dual N
IRF7102	NDS9959	SO-8, Dual N
IRF7103	NDS9955	SO-8, Dual N
IRF7104	NDS9953A	SO-8, Dual P
IRF7105	NDS9952A	SO-8, Comp N/P
IRF7106	NDS9958	SO-8, Comp N/P
IRF7107	NDS9958	SO-8, Comp N/P
IRF7201	FDS6612A	SO-8, N
IRF7202	NDS9400A	SO-8, P

Industry Part Number	Recommended Fairchild Device	Package
IRF7203	FDS9435A	SO-8, P
IRF7204	NDS8434A	SO-8, P
IRF7205	FDS9435A	SO-8, P
IRF7206	FDS9435A	SO-8, P
IRF7301	FDS8926A	SO-8, Dual N
IRF7303	FDS8930A	SO-8, Dual N
IRF7304	FDS8934A	SO-8, Dual P
IRF7306	FDS8947A	SO-8, Dual P
IRF7307	FDS8928A	SO-8, Comp N/P
IRF7309	FDS8958A	SO-8, Comp N/P
IRF7311	FDS8926A	SO-8, Dual N
IRF7312	FDS8912A	SO-8, Dual N
IRF7314	FDS8934A	SO-8, Dual P
IRF7316	FDS8947A	SO-8, Dual P
IRF7317	FDS8928A	SO-8, Comp N/P
IRF7319	FDS8958A	SO-8, Comp N/P
IRF7389	FDS8958A	SO-8, Comp N/P
IRF7401	NDS8426A	SO-8, N
IRF7403	FDS6612A	SO-8, N
IRF7404	NDS8434A	SO-8, P
IRF7406	FDS9435A	SO-8, P
IRF7413	FDS6690A	SO-8, N
IRF7413A	FDS6690A	SO-8, N
IRF7416	NDS8435A	SO-8, P
IRF7501*	NDH8301N	Micro8, Dual N
IRF7503*	NDH8503N	Micro8, Dual N
IRF7504*	NDH8302P	Micro8, Dual P
IRF7506*	NDH8504P	Micro8, Dual P
IRF7507*	NDH8320C	Micro8, Comp N/P
IRF7509*	NDH8520C	Micro8, Comp N/P
IRF7601*	NDH831N	Micro8, N
IRF7603*	NDH8436	Micro8, N
IRF7604*	NDH830P	Micro8, P
IRF7606	NDH8447	Micro8, P
IRF7805	FDS6690A	SO-8, N
IRF7807	FDS6612A	SO-8, N

Note:

* SuperSOT-8 footprint not equal to TSSOP-8

Fairchild Mosfet Reference Guide

Linden T. Harrison



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Robust Electronic Design Reference Book: no special title John R. Barnes, 2004 If you design electronics for a living you need Robust Electronic Design Reference Book Written by a working engineer who has put over 115 electronic products into production at Sycor IBM and Lexmark Robust Electronic Design Reference covers all the various aspects of designing and developing electronic devices and systems that Work Are safe and reliable Can be manufactured tested repaired and serviced May be sold and used worldwide Can be adapted or enhanced to meet new and changing requirements *Current Sources and Voltage References* Linden T. Harrison, 2005-08-22 Current Sources and Voltage References provides fixed well regulated levels of current or voltage within a circuit These are two of the most important building blocks of analog circuits and are typically used in creating most analog IC designs Part 1 shows the reader how current sources are created how they can be optimized and how they can be utilized by the OEM circuit designer The book serves as a must have reference for the successful development of precision circuit applications It shows practical examples using either BJTs FETs precision op amps or even matched CMOS arrays being used to create highly accurate current source designs ranging from nanoAmps to Amps In each chapter the most important characteristics of the particular semiconductor type being studied are carefully reviewed This not only serves as a helpful refresher for experienced engineers but also as a good foundation for all EE student coursework and includes device models and relevant equations Part 2 focuses on semiconductor voltage references from their design to their various practical enhancements It ranges from the simple Zener diode to today s most advanced topologies including Analog Devices XFET and Intersil s FGATM invented while this book was being written Over 300 applications and circuit diagrams are shown throughout this easy to read practical reference book Discusses how to design low noise precision current sources using matched transistor pairs Explains the design of high power current sources with power MOSFETs Gives proven techniques to reduce drift and improve accuracy in voltage references **Power Electronics and Control Techniques for Maximum Energy Harvesting in Photovoltaic Systems** Nicola Femia, Giovanni Petrone, Giovanni Spagnuolo, Massimo Vitelli, 2017-07-12 Incentives provided by European governments have resulted in the rapid growth of the photovoltaic PV market Many PV modules are now commercially available and there are a number of power electronic systems for processing the electrical power produced by PV systems especially for grid connected applications Filling a gap in the literature Power Electronics and Control Techniques for Maximum Energy Harvesting in Photovoltaic Systems brings together research on control circuits systems and techniques dedicated to the maximization of the electrical power produced by a photovoltaic PV source Tools to Help You Improve the Efficiency of Photovoltaic Systems The book supplies an overview of recent improvements in connecting PV systems to the grid and highlights various solutions that can be used as a starting point for further research and development It begins with a review of methods for modeling a PV array working in uniform and mismatched conditions The book then discusses several ways to achieve the best maximum

power point tracking MPPT performance A chapter focuses on MPPT efficiency examining the design of the parameters that affect algorithm performance The authors also address the maximization of the energy harvested in mismatched conditions in terms of both power architecture and control algorithms and discuss the distributed MPPT approach The final chapter details the design of DC DC converters which usually perform the MPPT function with special emphasis on their energy efficiency Get Insights from the Experts on How to Effectively Implement MPPT Written by well known researchers in the field of photovoltaic systems this book tackles state of the art issues related to how to extract the maximum electrical power from photovoltaic arrays under any weather condition Featuring a wealth of examples and illustrations it offers practical guidance for researchers and industry professionals who want to implement MPPT in photovoltaic systems **The COMPLETE**

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IC Master ,2000

Fundamentals Of Solid-state

Electronics: Study Guide Chih Tang Sah,1994-02-08 This companion to Fundamentals of Solid State Electronics provides a helpful summary of the main text for students and lecturers alike The clear typeface large font and point form layout are designed to produce viewgraphs for lectures and to provide ample margins for study notes This Study Guide comes complete with a detailed description of two one semester solid state electronics core courses taught to about 80 100 sophomore junior students each time four years apart It links the contents of the one semester lecture course to the textbook **Electronic**

Design ,1998

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reliability techniques with examples from electronics design for the benefit of engineers It presents the application of de rating FMEA overstress analyses and reliability improvement tests for designing reliable electronic equipment Adequate information is provided for designing computerized reliability database system to support the application of the techniques by designers Pedantic terms and the associated mathematics of reliability engineering discipline are excluded for the benefit of comprehensiveness and practical applications This book offers excellent support for electrical and electronics engineering students and professionals bridging academic curriculum with industrial expectations Smart Systems Integration and

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Provides a comprehensive overview of smart systems design focusing on design challenges and cutting edge solutions
Enables development of a co simulation and co design environment that accounts for the peculiarities of the basic
subsystems and components to be integrated Describes development of modeling and design techniques methods and tools
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