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<b>Question Paper Code : 21266</b>
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B.E./B.Tech. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2012.

Sixth Semester

Electronics and Communication Engineering

080290040 — MICROCONTROLLERS AND RISC ARCHITECTURE

(Regulation 2008)

Time : Three hours

Maximum : 100 marks

Answer ALL questions.

PART A — (10 × 2 = 20 marks)

1. How does the pin  $\overline{EA}$  of 8051 help in distinguishing the address range of program memory?
2. Show the circuit used for power down operation of 8051.
3. What are the data types and operand addressing supported in 8051?
4. The carry flag is originally set. The data present at the input port 3 is C5H. The data previously written to output port 1 is 35H calculate the port 1 contents after the execution of the following instructions.  
MOV P1.3, C  
MOV C, P3.3  
MOV P1.2, C
5. How do you generate the data corresponding to the step angle required in stepped motor interfacing?
6. Find an application that uses the auto - reload mode of timer 2 in 8051. Justify your answer.
7. List the features of ARM architecture that are not shared by most other RISC architectures.
8. Give an example for pre-indexed addressing mode of ARM.
9. Tabulate the arm- exceptions with their vector address.
10. What are the common sources of memory fault allowed by ARM processors?

# Microcontroller And Risc Architecture Question Paper

**Sabine Zange**



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