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Name :

Third Semester B.Tech. Engineering Degree (Regular/Supplementary) Examination, November 2010 2K6 CS/IT 305 : SWITCHING THEORY AND LOGIC DESIGN

Time: 3 Hours

Max. Marks: 100

Instruction: Answer all questions.

- 1. a) Give symbols and truth tables of electronic gates and contact networks.
 - b) Define sum of products and product of sum with examples.
- c) What are universal gates and its properties? Give suitable example.
 - d) Write a note on look ahead adders.
 - e) What do you mean by path sensitisation?
 - f) What do you mean by programmable logic arrays? Give example.
 - g) Write a note on Flip-flops. Give truth table for SR and JK Flip-flops.
 - h) What do you mean by synchronous counter? How it differs from asynchronous counter? (5×8=40)
- 2. A) i) Simplify following K-map using sum of products.

10

AB⊸ CD ⊥	00	01	11	10
00	1		1	1
01	1			
11	1		1	1
10	1	1	1	1

verify using truth table.

What do you mean by normal and canonical forms? Give suitable example.

5

High Speed Switching Architecture University Question Paper

Allan Gottlieb, Yao Li, Eugen Schenfeld

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Japan Telecommunications ,1988

Conference on Information Sciences and Systems ,1994 Conference Record ,2001 **Conference Proceedings** ,1989 IEEE International Symposium on Information Theory, The Railway and Engineering Review Walter Mason 1994 IEEE 13th Annual International Phoenix Conference on Computers and Communications, 1994 Camp, 1910 Proceedings of the Third International Conference on Massively Parallel Processing Using Physics Briefs ,1993 Optical Interconnections Allan Gottlieb, Yao Li, Eugen Schenfeld, 1996 Contains papers from the October 1996 conference focusing on the potentials and impacts that optical interconnections can have on massively parallel processing Offers sections on comparative studies for optical interconnects interconnection networks and system architectures WDM in MPP systems tradeoffs in intra system optical interconnects scalable interconnection networks architecture issues guided wave components for optical interconnects and multiprocessor networks and systems Includes discussion of the roles of university and industry in developing systems For researchers in computer science engineering and optics No index Annotation copyrighted by Book News Inc Portland OR The Second RIKEN International Symposium on Symbolic and Algebraic Computation by Computers Nobuyuki Inada, Takashi S?ma, 1985 This proceedings is based on research work on formula manipulation and computer algebra culminating in the design and construction of a formula manipulation machine at Fourth IEE Conference on Telecommunications, 18-21 April 1993 Institution of RIKEN known as the FLATS project Electrical Engineers. Electronics Division, 1993 NBS Special Publication ,1973 Los Angeles Magazine ,2003-11 Los Angeles magazine is a regional magazine of national stature Our combination of award winning feature writing investigative reporting service journalism and design covers the people lifestyle culture entertainment fashion art and architecture and news that define Southern California Started in the spring of 1961 Los Angeles magazine has been addressing the needs and interests of our region for 48 years The magazine continues to be the definitive resource for an affluent population that is intensely interested in a lifestyle that is uniquely Southern Californian Japanese Technical Abstracts ,1987

Technical Program, Conference Record, 1999

Proceedings of the ...

Proceedings of the 33rd IEEE Conference on Decision and Control IEEE Control Systems Society,1994 ESN
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Symposium on Simulation ,2003 The growing awareness of the effects that simulation is having on the way we design our
computing communication and control systems is leading to an increased demand for a better understanding of all aspects of
simulation ANSS 03 covers broad topics in the areas of distributed systems network modeling and advances in simulation
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