

Ivan Radojevic · Zoran Salcic

Embedded Systems Design Based on Formal Models of Computation

Embedded Systems Design Based On Formal Models Of Computation

Richard Zurawski



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Design Principles for Embedded Systems KCS Murti, 2021-09-20 The book is designed to serve as a textbook for courses offered to graduate and undergraduate students enrolled in electronics and electrical engineering and computer science. This book attempts to bridge the gap between electronics and computer science students, providing complementary knowledge that is essential for designing an embedded system. The book covers key concepts tailored for embedded system design in one place. The topics covered in this book are models and architectures, Executable Specific Languages, SystemC, Unified Modeling Language, real time systems, real time operating systems, networked embedded systems, Embedded Processor architectures and platforms that are secured and energy efficient. A major segment of embedded systems needs hard real time requirements. This textbook includes real time concepts including algorithms and real time operating system standards like POSIX threads. Embedded systems are mostly distributed and networked for deterministic responses. The book covers how to design networked embedded systems with appropriate protocols for real time requirements. Each chapter contains 2-3 solved case studies and 10 real world problems as exercises to provide detailed coverage and essential pedagogical tools that make this an ideal textbook for students enrolled in electrical and electronics engineering and computer science programs.

Software Engineering and Formal Methods Alessandro Cimatti, Marjan Sirjani, 2017-08-24 This book constitutes the refereed proceedings of the 15th International Conference on Software Engineering and Formal Methods (SEFM 2017) held in Trento, Italy, in September 2017. The 17 full papers and 6 short papers presented were carefully reviewed and selected from 102 submissions. The papers deal with a large range of topics in the following research areas: new frontiers in software architecture, software verification and testing, software development methods, application and technology transfer, security and safety, and design principles.

Modeling Embedded Systems and SoC's Axel Jantsch, 2004 System level design is a critical component for the methods to develop designs more productively. But there are a number of challenges in implementing system level modeling. This book addresses

that need by developing organizing principles for understanding assessing and comparing the different models of computation in system level modeling **Embedded Systems Handbook 2-Volume Set** Richard Zurawski,2018-10-08

During the past few years there has been an dramatic upsurge in research and development implementations of new technologies and deployments of actual solutions and technologies in the diverse application areas of embedded systems These areas include automotive electronics industrial automated systems and building automation and control Comprising 48 chapters and the contributions of 74 leading experts from industry and academia the Embedded Systems Handbook Second Edition presents a comprehensive view of embedded systems their design verification networking and applications The contributors directly involved in the creation and evolution of the ideas and technologies presented offer tutorials research surveys and technology overviews exploring new developments deployments and trends To accommodate the tremendous growth in the field the handbook is now divided into two volumes New in This Edition Processors for embedded systems Processor centric architecture description languages Networked embedded systems in the automotive and industrial automation fields Wireless embedded systems Embedded Systems Design and Verification Volume I of the handbook is divided into three sections It begins with a brief introduction to embedded systems design and verification The book then provides a comprehensive overview of embedded processors and various aspects of system on chip and FPGA as well as solutions to design challenges The final section explores power aware embedded computing design issues specific to secure embedded systems and web services for embedded devices Networked Embedded Systems Volume II focuses on selected application areas of networked embedded systems It covers automotive field industrial automation building automation and wireless sensor networks This volume highlights implementations in fast evolving areas which have not received proper coverage in other publications Reflecting the unique functional requirements of different application areas the contributors discuss inter node communication aspects in the context of specific applications of networked embedded systems

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Proceedings of the 5th International Workshop on Reconfigurable Communication-centric Systems on Chip 2010 - ReCoSoC'10 Michael Hübner,2010 **Languages, Design Methods, and Tools for Electronic**

System Design Frank Oppenheimer,Julio Luis Medina Pasaje,2015-12-11 This book brings together a selection of the best papers from the seventeenth edition of the Forum on specification and Design Languages Conference FDL which took place on October 14 16 2014 in Munich Germany FDL is a well established international forum devoted to dissemination of research results practical experiences and new ideas in the application of specification design and verification languages to the design modeling and verification of integrated circuits complex hardware software embedded systems and mixed technology systems

EDA for IC System Design, Verification, and Testing Louis Scheffer,Luciano Lavagno,Grant Martin,2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The first volume EDA for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logical verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for IC designs design and verification languages digital simulation hardware acceleration and emulation and much more Save on the complete set

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