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web simply defined latch up is a functional chip failure associated with excessive current going through the chip caused by weak circuit design in some cases latch up can be a temporary condition that can be resolved by power cycle but

unfortunately it can also cause a fatal chip failure cmos latch up

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web may 10 2020 technically latch up is the phenomena of activating the parasitic bits in a cmos circuit which forms a low impedance path between the power and ground terminals this low impedance path draws a large current and heats up the ic integrated chip which cause permanent damage of ic