



III SEMESTER B.TECH (ELECTRICAL & ELECTRONICS ENGINEERING)

END SEMESTER EXAMINATIONS, NOVEMBER 2018

SUBJECT: DIGITAL ELECTRONIC CIRCUITS [ELE 2102]

REVISED CREDIT SYSTEM

Time: 3 Hours

Date: 24, November 2018

Max. Marks: 50

Instructions to Candidates:

- ❖ Answer ALL the questions.
- ❖ Missing data may be suitably assumed.

- 1A. Given the network of Fig Q1A, determine the functions f_2 and f_3 if $f_1 = B$ and the overall function is to be

$$F(A,B,C,D) = \prod M(1,3,9,11,12,13,14) + d(2,8)$$

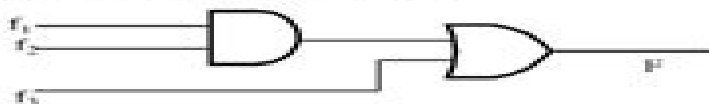


Fig Q1A

(03)

- 1B. Show how $F = A + B + \bar{C}$ can be implemented with one 2 input NAND gate and one 2 input NOR gate.

(02)

- 1C. Obtain a minimal SOP expression using Tabular method for the following equation.

$$F(A, B, C, D) = \sum m(2, 4, 7, 9, 11, 12, 14) + d(0, 5, 6, 13, 15)$$

(05)

- 2A. Design a 2 bit comparator circuit as shown in the Fig Q2A using 1:16 D-mux and residual - gates



Fig Q2A

(04)

- 2B. Write the truth table and design BCD to Seven-segment decoder circuit using common anode configuration

(04)

- 2C. Implement , 8-bit addition and subtraction operation using IC 74LS283

(02)

- 3A. Design asynchronous mod-16 up/down counter using positive edge triggered T Flip-flops. Draw the waveform for the same.

(03)

- 3B. Design a one minute counter using decade 7490 IC's

(03)

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