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Robert Love

Third Edition

Linux Kernel Development

A thorough guide to the design and implementation of the Linux Kernel

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concerns and debugging techniques This book covers the most interesting features of the Linux 2.6 kernel including the CFS scheduler preemptive kernel block I/O layer and I/O schedulers The third edition of Linux Kernel Development includes new and updated material throughout the book An all new chapter on kernel data structures Details on interrupt handlers and bottom halves Extended coverage of virtual memory and memory allocation Tips on debugging the Linux kernel In depth coverage of kernel synchronization and locking Useful insight into submitting kernel patches and working with the Linux kernel community

Linux System Programming Robert Love, 2007-09-18 This book is about writing software that makes the most effective use of the system you're running on code that interfaces directly with the kernel and core system libraries including the shell text editor compiler debugger core utilities and system daemons The majority of both Unix and Linux code is still written at the system level and Linux System Programming focuses on everything above the kernel where applications such as Apache bash cp vim Emacs gcc gdb glibc ls mv and X exist Written primarily for engineers looking to program better at the low level this book is an ideal teaching tool for any programmer Even with the trend toward high level development either through web software such as PHP or managed code C someone still has to write the PHP interpreter and the C virtual machine Linux System Programming gives you an understanding of core internals that makes for better code no matter where it appears in the stack Debugging high level code often requires you to understand the system calls and kernel behavior of your operating system too Key topics include An overview of Linux the kernel the C library and the C compiler Reading from and writing to files along with other basic file I/O operations including how the Linux kernel implements and manages file I/O Buffer size management including the Standard I/O library Advanced I/O interfaces memory mappings and optimization techniques The family of system calls for basic process management Advanced process management including real time processes File and directories creating moving copying deleting and managing them Memory management interfaces for allocating memory managing the memory you have and optimizing your memory access Signals and their role on a Unix system plus basic and advanced signal interfaces Time sleeping and clock management starting with the basics and continuing through POSIX clocks and high resolution timers With Linux System Programming you will be able to take an in depth look at Linux from both a theoretical and an applied perspective as you cover a wide range of programming topics

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abstract a wideband 2 2 slot element for a 60 ghz antenna array is designed by making use of two double sided printed circuit boards pcbs the upper pcb contains the four radiating cavity backed slots where the cavity is formed in substrate integrated waveguide siw using metalized via holes

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apr 3 2020 a novel low profile substrate integrated waveguide siw cavity backed slot antenna with an enhanced gain is proposed by etching four parallel transverse slots at the edges of the hexagonal siw cavi

development of a wideband hexagonal siw cavity backed slot antenna - Aug 20 2023

sep 1 2021 in this paper a novel wideband hexagonal cavity backed slot antenna array waveguide siw is proposed first a single element low profile wideband linearly polarized cavity backed antenna using two slots is designed and analyzed the antenna bandwidth is increased by nrws which makes both hybrid modes of tm input impedance keywords

a high performance siw slot antenna array for mmwave radar - May 17 2023

jul 24 2023 the feed network of the antenna is composed of an air filled rectangular waveguide rwg to substrate integrated waveguide siw transition and a power division feed network finally the array element and the feed network are combined to form an 8 times 16 slot array the overall simulation design is carried out in hfss