

MULTIPLE CHOICE Choose the one alternative that best completes the statement or answers the question.

1) For this circuit, determine the load-line intersection with the two axis.

1) _____



- A) $V_D = 10\text{ V}$ and $I_D = 1\text{ mA}$
 C) $V_D = 10\text{ V}$ and $I_D = 10\text{ mA}$

- B) $V_D = 1\text{ V}$ and $I_D = 1\text{ mA}$
 D) $V_D = 1\text{ V}$ and $I_D = 10\text{ mA}$

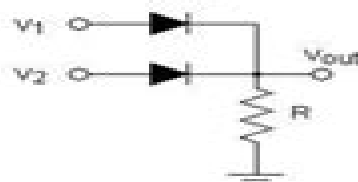
2) If one silicon diode and one germanium diode are connected in series, the voltage drop across the combination of the two diodes will be equal to _____

2) _____

- A) the forward drop equal to that of the silicon diode
 B) the forward drop equal to that of the difference of the voltage drops across the two diodes
 C) the forward drop equal to that of the sum of the voltage drops across the two diodes
 D) the forward drop equal to that of the germanium diode

3) Name the logic gate that is formed by this circuit.

3) _____

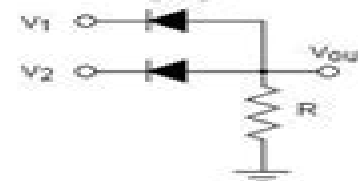


- A) positive logic AND gate
 C) negative logic OR gate

- B) negative logic AND gate
 D) positive logic OR gate

4) Name the logic gate that is formed by this circuit.

4) _____

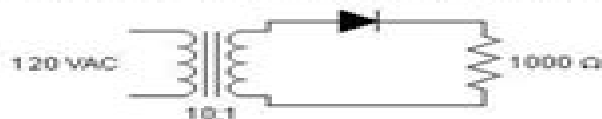


- A) positive logic AND gate
 C) positive logic OR gate

- B) negative logic OR gate
 D) negative logic AND gate

5) The current flows through the load resistor in this circuit during the _____.

5) _____



- A) The diode will block all current and there will be no current flowing through the load.
 B) positive half cycle of the input waveform

Engineering Circuit Analysis 8th Solution Manual

**Jun Wang,Xin Li,Chris Allison,Judy
Hohorst**



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eylül de devlet başkanı olarak görevini sürdürdü

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a Silicon Valley business his mother is Jewish and his father was of Portuguese and Swedish

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Republic in the Spanish Civil War 1936-39 thereafter he was the head of the government of Spain until 1973 and head of state
until his death in 1975 learn more about Franco in this article