

TYPICAL QUESTIONS & ANSWERS

PART - I

OBJECTIVE TYPE QUESTIONS

Each Question carries 2 marks.

Choose the correct or best alternative in the following:

- Q.1** If the crystal oscillator is operating at 15 MHz, the PCLK output of 8284 is
(A) 2.5 MHz. (B) 5 MHz.
(C) 7.5 MHz. (D) 10 MHz.

Ans: (A)

- Q.2** In which T-state does the CPU send the address to memory or I/O and the ALE signal for demultiplexing
(A) T1. (B) T2.
(C) T3. (D) T4.

Ans: During the first clocking period in a bus cycle, which is called T1, the address of the memory or I/O location is sent out and the control signals ALE, DT/R⁺ and IO/M⁺ are also output. Hence answer is (A).

- Q.3** If a 1M×1 DRAM requires 4 ms for a refresh and has 256 rows to be refreshed, no more than _____ of time must pass before another row is refreshed.
(A) 64 ms. (B) 4 ms.
(C) 0.5 ms. (D) 15.625 μs.

Ans: Answer is (B)

- Q.4** In a DMA write operation the data is transferred
(A) from I/O to memory. (B) from memory to I/O.
(C) from memory to memory. (D) from I/O to I/O.

Ans: A DMA writes operation transfers data from an I/O device to memory. Hence answer is (A).

- Q.5** Which type of JMP instruction assembles if the distance is 0020 h bytes
(A) near. (B) far.
(C) short. (D) none of the above.

Ans: The three byte near jump allows a branch or jump within ± 32K bytes. Hence answer is (A).

- Q.6** A certain SRAM has $\overline{CS} = 0$, $\overline{WE} = 0$ and $\overline{OE} = 1$. In which of the following modes this SRAM is operating

(A) Read

(B) Write

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