HSPICE® Reference Manual: Commands and Control Options

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SYNOPSYS®

Hspice 2010 Manual

Hao Yu,Yang Shang

Hspice 2010 Manual:

Advances in Information and Communication Networks Kohei Arai,Supriya Kapoor,Rahul Bhatia,2018-12-05 The book gathering the proceedings of the Future of Information and Communication Conference FICC 2018 is a remarkable collection of chapters covering a wide range of topics in areas of information and communication technologies and their applications to the real world It includes 104 papers and posters by pioneering academic researchers scientists industrial engineers and students from all around the world which contribute to our understanding of relevant trends of current research on communication data science ambient intelligence networking computing security and Internet of Things This book collects state of the art chapters on all aspects of information science and communication technologies from classical to intelligent and covers both theory and applications of the latest technologies and methodologies Presenting state of the art intelligent methods and techniques for solving real world problems along with a vision of the future research this book is an interesting and useful resource **CMOS Sigma-Delta Converters** Jose M. de la Rosa,Rocio del Rio,2013-03-13 A comprehensive overview of Sigma Delta Analog to Digital Converters ADCs and a practical guide to their design in nano scale CMOS for optimal performance This book presents a systematic and comprehensive compilation of sigma delta converter operating principles the new advances in architectures and circuits design methodologies and practical considerations going from system level specifications to silicon integration packaging and measurements with emphasis on nanometer CMOS implementation The book emphasizes practical design issues from high level behavioural modelling in MATLAB SIMULINK to circuit level implementation in Cadence Design FrameWork II As well as being a comprehensive reference to the theory the book is also unique in that it gives special importance on practical issues giving a detailed description of the different steps that constitute the whole design flow of sigma delta ADCs The book begins with an introductory survey of sigma delta modulators their fundamentals architectures and synthesis methods covered in Chapter 1 In Chapter 2 the effect of main circuit error mechanisms is analysed providing the necessary understanding of the main practical issues affecting the performance of sigma delta modulators The knowledge derived from the first two chapters is presented in the book as an essential part of the systematic top down bottom up synthesis methodology of sigma delta modulators described in Chapter 3 where a time domain behavioural simulator named SIMSIDES is described and applied to the high level design and verification of sigma delta ADCs Chapter 4 moves farther down from system level to the circuit and physical level providing a number of design recommendations and practical recipes to complete the design flow of sigma delta modulators To conclude the book Chapter 5 gives an overview of the state of the art sigma delta ADCs which are exhaustively analysed in order to extract practical design guidelines and to identify the incoming trends design challenges as well as practical solutions proposed by cutting edge designs Offers a complete survey of sigma delta modulator architectures from fundamentals to state of the art topologies considering both switched capacitor and continuous time circuit implementations Gives a

systematic analysis and practical design guide of sigma delta modulators from a top down bottom up perspective including mathematical models and analytical procedures behavioural modeling in MATLAB SIMULINK macromodeling and circuit level implementation in Cadence Design FrameWork II chip prototyping and experimental characterization Systematic compilation of cutting edge sigma delta modulators Complete description of SIMSIDES a time domain behavioural simulator implemented in MATLAB SIMULINK Plenty of examples case studies and simulation test benches covering the different stages of the design flow of sigma delta modulators A number of electronic resources including SIMSIDES the statistical data used in the state of the art survey as well as many design examples and test benches are hosted on a companion website Essential reading for Researchers and electronics engineering practitioners interested in the design of high performance data converters integrated in nanometer CMOS technologies mixed signal designers Power Electronics and Control Techniques for Maximum Energy Harvesting in Photovoltaic Systems Nicola Femia,Giovanni Petrone,Giovanni Spagnuolo,Massimo Vitelli,2017-07-12 Incentives provided by European governments have resulted in the rapid growth of the photovoltaic PV market Many PV modules are now commercially available and there are a number of power electronic systems for processing the electrical power produced by PV systems especially for grid connected applications Filling a gap in the literature Power Electronics and Control Techniques for Maximum Energy Harvesting in Photovoltaic Systems brings together research on control circuits systems and techniques dedicated to the maximization of the electrical power produced by a photovoltaic PV source Tools to Help You Improve the Efficiency of Photovoltaic Systems The book supplies an overview of recent improvements in connecting PV systems to the grid and highlights various solutions that can be used as a starting point for further research and development It begins with a review of methods for modeling a PV array working in uniform and mismatched conditions The book then discusses several ways to achieve the best maximum power point tracking MPPT performance A chapter focuses on MPPT efficiency examining the design of the parameters that affect algorithm performance The authors also address the maximization of the energy harvested in mismatched conditions in terms of both power architecture and control algorithms and discuss the distributed MPPT approach The final chapter details the design of DC DC converters which usually perform the MPPT function with special emphasis on their energy efficiency Get Insights from the Experts on How to Effectively Implement MPPT Written by well known researchers in the field of photovoltaic systems this book tackles state of the art issues related to how to extract the maximum electrical power from photovoltaic arrays under any weather condition Featuring a wealth of examples and illustrations it offers practical guidance for researchers and industry professionals who want to implement MPPT in photovoltaic systems *Compact Models for Integrated Circuit Design* Samar K. Saha,2018-09-03 Compact Models for Integrated Circuit Design Conventional Transistors and Beyond provides a modern treatise on compact models for circuit computer aided design CAD Written by an author with more than 25 years of industry experience in semiconductor processes devices and circuit CAD and more than 10 years of

academic experience in teaching compact modeling courses this first of its kind book on compact SPICE models for very large scale integrated VLSI chip design offers a balanced presentation of compact modeling crucial for addressing current modeling challenges and understanding new models for emerging devices Starting from basic semiconductor physics and covering state of the art device regimes from conventional micron to nanometer this text Presents industry standard models for bipolar junction transistors BJTs metal oxide semiconductor MOS field effect transistors FETs FinFETs and tunnel field effect transistors TFETs along with statistical MOS models Discusses the major issue of process variability which severely impacts device and circuit performance in advanced technologies and requires statistical compact models Promotes further research of the evolution and development of compact models for VLSI circuit design and analysis Supplies fundamental and practical knowledge necessary for efficient integrated circuit IC design using nanoscale devices Includes exercise problems at the end of each chapter and extensive references at the end of the book Compact Models for Integrated Circuit Design Conventional Transistors and Beyond is intended for senior undergraduate and graduate courses in electrical and electronics engineering as well as for researchers and practitioners working in the area of electron devices However even those unfamiliar with semiconductor physics gain a solid grasp of compact modeling concepts from this book

Source-Synchronous Networks-On-Chip Ayan Mandal,Sunil P. Khatri,Rabi Mahapatra,2013-11-19 This book describes novel methods for network on chip NoC design using source synchronous high speed resonant clocks The authors discuss NoCs from the bottom up providing circuit level details before providing architectural simulations As a result readers will get a complete picture of how a NoC can be designed and optimized Using the methods described in this book readers are enabled to design NoCs that are 5X better than existing approaches in terms of latency and throughput and can also sustain a significantly greater amount of traffic *Sigma-Delta Converters: Practical Design Guide* Jose M. de la Rosa,2018-08-22 Thoroughly revised and expanded to help readers systematically increase their knowledge and insight about Sigma Delta Modulators Sigma Delta Modulators SDMs have become one of the best choices for the implementation of analog digital interfaces of electronic systems integrated in CMOS technologies Compared to other kinds of Analog to Digital Converters ADCs Ms cover one of the widest conversion regions of the resolution versus bandwidth plane being the most efficient solution to digitize signals in an increasingly number of applications which span from high resolution low bandwidth digital audio sensor interfaces and instrumentation to ultra low power biomedical systems and medium resolution broadband wireless communications Following the spirit of its first edition Sigma Delta Converters Practical Design Guide 2nd Edition takes a comprehensive look at SDMs their diverse types of architectures circuit techniques analysis synthesis methods and CAD tools as well as their practical design considerations It compiles and updates the current research reported on the topic and explains the multiple trade offs involved in the whole design flow of Sigma Delta Modulators from specifications to chip implementation and characterization The book follows a top down approach in order to provide readers with the necessary

understanding about recent advances trends and challenges in state of the art Ms It makes more emphasis on two key points which were not treated so deeply in the first edition It includes a more detailed explanation of Ms implemented using Continuous Time CT circuits going from system level synthesis to practical circuit limitations It provides more practical case studies and applications as well as a deeper description of the synthesis methodologies and CAD tools employed in the design of converters Sigma Delta Converters Practical Design Guide 2nd Edition serves as an excellent textbook for undergraduate and graduate students in electrical engineering as well as design engineers working on SD data converters who are looking for a uniform and self contained reference in this hot topic With this goal in mind and based on the feedback received from readers the contents have been revised and structured to make this new edition a unique monograph written in a didactical pedagogical and intuitive style **Nonlinear Circuit Simulation and Modeling** José Carlos Pedro,David E. Root,Jianjun Xu,Luís Cótimos Nunes,2018-06-14 Discover the nonlinear methods and tools needed to design real world microwave circuits with this tutorial guide Balancing theoretical background with practical tools and applications it covers everything from the basic properties of nonlinear systems such as gain compression intermodulation and harmonic distortion to nonlinear circuit analysis and simulation algorithms and state of the art equivalent circuit and behavioral modeling techniques Model formulations discussed in detail include time domain transistor compact models and frequency domain linear and nonlinear scattering models Learn how to apply these tools to designing real circuits with the help of a power amplifier design example which covers all stages from active device model extraction and the selection of bias and terminations through to performance verification Realistic examples illustrative insights and clearly conveyed mathematical formalism make this an essential learning aid for both professionals working in microwave and RF engineering and graduate students looking for a hands on guide to microwave circuit design **Nanoelectronics, Circuits and Communication Systems** Vijay Nath,J. K. Mandal,2020-04-01 This book features selected papers presented at the Fourth International Conference on Nanoelectronics Circuits and Communication Systems NCCS 2018 Covering topics such as MEMS and nanoelectronics wireless communications optical communications instrumentation signal processing the Internet of Things image processing bioengineering green energy hybrid vehicles environmental science weather forecasting cloud computing renewable energy RFID CMOS sensors actuators transducers telemetry systems embedded systems and sensor network applications in mines it offers a valuable resource for young scholars researchers and academics alike *Digital VLSI Design with Verilog* John Michael Williams,2014-06-17 This book is structured as a step by step course of study along the lines of a VLSI integrated circuit design project The entire Verilog language is presented from the basics to everything necessary for synthesis of an entire 70 000 transistor full duplex serializer deserializer including synthesizable PLLs The author includes everything an engineer needs for in depth understanding of the Verilog language Syntax synthesis semantics simulation and test Complete solutions for the 27 labs are provided in the downloadable files that accompany the book For readers with access to

appropriate electronic design tools all solutions can be developed simulated and synthesized as described in the book A partial list of design topics includes design partitioning hierarchy decomposition safe coding styles back annotation wrapper modules concurrency race conditions assertion based verification clock synchronization and design for test A concluding presentation of special topics includes System Verilog and Verilog AMS **Advanced Semiconductor-on-Insulator Technology and Related Physics 15** Yasuhisa Omura,2011-04 This is the continuation of the long running Silicon on Insulator Technology and Devices symposium The issue of ECS Transactions covers recent significant advances in SOI technologies SOI based nanoelectronics and innovative applications including scientific interests It will be of interest to materials and device scientists as well as to process and applications oriented engineers and scientists *Design Exploration of Emerging Nano-scale Non-volatile Memory* Hao Yu,Yuhao Wang,2014-04-18 This book presents the latest techniques for characterization modeling and design for nano scale non volatile memory NVM devices Coverage focuses on fundamental NVM device fabrication and characterization internal state identification of memristic dynamics with physics modeling NVM circuit design and hybrid NVM memory system design space optimization The authors discuss design methodologies for nano scale NVM devices from a circuits systems perspective including the general foundations for the fundamental memristic dynamics in NVM devices Coverage includes physical modeling as well as the development of a platform to explore novel hybrid CMOS and NVM circuit and system design Offers readers a systematic and comprehensive treatment of emerging nano scale non volatile memory NVM devices Focuses on the internal state of NVM memristic dynamics novel NVM readout and memory cell circuit design and hybrid NVM memory system optimization Provides both theoretical analysis and practical examples to illustrate design methodologies Illustrates design and analysis for recent developments in spin toque transfer domain wall racetrack and memristors *Design of CMOS Millimeter-Wave and Terahertz Integrated Circuits with Metamaterials* Hao Yu,Yang Shang,2015-10-19 This book shows that with the use of metamaterials one can have coherent THz signal generation amplification transmission and detection for phase arrayed CMOS transistors with significantly improved performance Offering detailed coverage from device to system the book describes the design and application of metamaterials in actual CMOS integrated circuits includes real circuit examples and chip demonstrations with measurement results and also evaluates system performance after CMOS based system on chip integration The book reflects the latest research progress and provides a state of the art reference on CMOS based metamaterial devices and mm wave and THz systems **Electrical and Electronic Devices, Circuits and Materials** Suman Lata Tripathi,Parvej Ahmad Alvi,Umashankar Subramaniam,2021-03-15 The increasing demand in home and industry for electronic devices has encouraged designers and researchers to investigate new devices and circuits using new materials that can perform several tasks efficiently with low IC integrated circuit area and low power consumption Furthermore the increasing demand for portable devices intensifies the search to design sensor elements an efficient storage cell and large

capacity memory elements Electrical and Electronic Devices Circuits and Materials Design and Applications will assist the development of basic concepts and fundamentals behind devices circuits materials and systems This book will allow its readers to develop their understanding of new materials to improve device performance with even smaller dimensions and lower costs Additionally this book covers major challenges in MEMS micro electromechanical system based device and thin film fabrication and characterization including their applications in different fields such as sensors actuators and biomedical engineering Key Features Assists researchers working on devices and circuits to correlate their work with other requirements of advanced electronic systems Offers guidance for application oriented electrical and electronic device and circuit design for future energy efficient systems Encourages awareness of the international standards for electrical and electronic device and circuit design Organized into 23 chapters Electrical and Electronic Devices Circuits and Materials Design and Applications will create a foundation to generate new electrical and electronic devices and their applications It will be of vital significance for students and researchers seeking to establish the key parameters for future work **Circuit Design for Reliability** Ricardo Reis,Yu Cao,Gilson Wirth,2014-11-08 This book presents physical understanding modeling and simulation on chip characterization layout solutions and design techniques that are effective to enhance the reliability of various circuit units The authors provide readers with techniques for state of the art and future technologies ranging from technology modeling fault detection and analysis circuit hardening and reliability management **The Bean** Angel Abusleme,2011 The International Linear Collider ILC a next generation particle accelerator will smash electron and positron bunches at up to 500 GeV 1000 GeV after a planned upgrade The 31 km long collider s experiments will help scientists to understand the fundamental constituents of matter Located at the ILC detector s forward region the BeamCal is a highly segmented 90 000 channels calorimeter that will serve three main purposes ensure hermeticity of the detector for low polar angles reduce the backscattering from pairs into the detector center and provide a low latency signal for beam diagnostics The BeamCal specifications in terms of radiation tolerance noise suppression signal charge pulse rate and occupancy pose unique challenges for the front end and readout electronics design Designed for the 180 nm TSMC mixed signal technology The Bean BeamCal Instrumentation IC is a 32 channel front end and readout ASIC that will address the BeamCal instrumentation requirements By employing a charge sensitive amplifier and a switched capacitor reset circuit the Bean will process the input charge signals at the ILC pulse rate Each channel will have a 10 bit successive approximation register analog to digital converter and digital memory for readout purposes The Bean will also feature a fast feedback adder capable of providing an 8 bit low latency output for beam diagnostics purposes This work presents the design and characterization of The Bean prototype a 3 channel ASIC that proves the principle of operation described *Physics of Semiconductor Devices* Simon M. Sze,Yiming Li,Kwok K. Ng,2021-03-03 The new edition of the most detailed and comprehensive single volume reference on major semiconductor devices The Fourth Edition of Physics of Semiconductor Devices remains the standard

reference work on the fundamental physics and operational characteristics of all major bipolar unipolar special microwave and optoelectronic devices This fully updated and expanded edition includes approximately 1 000 references to original research papers and review articles more than 650 high quality technical illustrations and over two dozen tables of material parameters Divided into five parts the text first provides a summary of semiconductor properties covering energy band carrier concentration and transport properties The second part surveys the basic building blocks of semiconductor devices including p n junctions metal semiconductor contacts and metal insulator semiconductor MIS capacitors Part III examines bipolar transistors MOSFETs MOS field effect transistors and other field effect transistors such as JFETs junction field effect transistors and MESFETs metal semiconductor field effect transistors Part IV focuses on negative resistance and power devices The book concludes with coverage of photonic devices and sensors including light emitting diodes LEDs solar cells and various photodetectors and semiconductor sensors This classic volume the standard textbook and reference in the field of semiconductor devices Provides the practical foundation necessary for understanding the devices currently in use and evaluating the performance and limitations of future devices Offers completely updated and revised information that reflects advances in device concepts performance and application Features discussions of topics of contemporary interest such as applications of photonic devices that convert optical energy to electric energy Includes numerous problem sets real world examples tables figures and illustrations several useful appendices and a detailed solutions manual for Instructor s only Explores new work on leading edge technologies such as MODFETs resonant tunneling diodes quantum cascade lasers single electron transistors real space transfer devices and MOS controlled thyristors Physics of Semiconductor Devices Fourth Edition is an indispensable resource for design engineers research scientists industrial and electronics engineering managers and graduate students in the field *Variation-Aware Design of Custom Integrated Circuits: A Hands-on Field Guide* Trent McConaghy,Kristopher Breen,Jeffrey Dyck,Amit Gupta,2012-09-28 This book targets custom IC designers who are encountering variation issues in their designs especially for modern process nodes at 45nm and below such as statistical process variations environmental variations and layout effects It teaches them the state of the art in Variation Aware Design tools which help the designer to analyze quickly the variation effects identify the problems and fix the problems Furthermore this book describes the algorithms and algorithm behavior performance limitations which is of use to designers considering these tools designers using these tools CAD researchers and CAD managers **Surrogate-Based Modeling and Optimization** Slawomir Koziel,Leifur Leifsson,2013-06-06 Contemporary engineering design is heavily based on computer simulations Accurate high fidelity simulations are used not only for design verification but even more importantly to adjust parameters of the system to have it meet given performance requirements Unfortunately accurate simulations are often computationally very expensive with evaluation times as long as hours or even days per design making design automation using conventional methods impractical These and other problems can be alleviated by the development and employment of

so called surrogates that reliably represent the expensive simulation based model of the system or device of interest but they are much more reasonable and analytically tractable This volume features surrogate based modeling and optimization techniques and their applications for solving difficult and computationally expensive engineering design problems It begins by presenting the basic concepts and formulations of the surrogate based modeling and optimization paradigm and then discusses relevant modeling techniques optimization algorithms and design procedures as well as state of the art developments The chapters are self contained with basic concepts and formulations along with applications and examples The book will be useful to researchers in engineering and mathematics in particular those who employ computationally heavy simulations in their design work *ESD Design Challenges and Strategies in Deeply-scaled Integrated Circuits* Shuqing Cao,2010 It is the main objective of this work to address the scaling and design challenges of ESD protection in deeply scaled technologies First the thesis introduces the on chip ESD events the scaling and design challenges and the nomenclatures necessary for later chapters The ESD design window and the I O schematics for both rail clamping and local clamping ESD schemes are illustrated Then the thesis delves into the investigation of the input and output driver devices and examines their robustness under ESD The input driver s oxide breakdown levels are evaluated in deeply scaled technologies The output driver s trigger and breakdown voltages are improved appreciably by applying circuit and device design techniques The ESD device sections first discuss rail based clamping a widely used protection scheme Two diode based devices namely the gated diode and substrate diode are investigated in detail with SOI test structures Characterization is based on DC current voltage I V Very Fast Transmission Line Pulse VF TLP capacitance and leakage measurements Improvements in performance are realized Technology computer aided design TCAD simulations help understand the physical effects and design tradeoffs Then the following section focuses on the local clamping scheme Two devices the field effect diode FED and the double well FED DWFED are developed and optimized in an SOI technology Trigger circuits are designed to improve the turn on speed The advantages of local clamping is highlighted and compared with the rail based clamping The results show that the FED is a suitable option for power clamping applications and the DWFED is most suitable for pad based local clamping The thesis presents an ESD protection design methodology which takes advantage of the results and techniques from pervious chapters and put each element into a useful format Based on the correlation of package level and in lab test results a design process based on CDM target definition and device optimization discharge path analysis parasitic minimization I O data rate estimation and finally ESD and performance characterization is used sequentially to systematically realize the overall design goals Simulation and Modeling Methodologies, Technologies and Applications Nuno Pina,Janusz Kacprzyk,Joaquim Filipe,2012-10-12 The present book includes extended and revised versions of a set of selected papers from the 1st International Conference on Simulation and Modeling Methodologies Technologies and Applications SIMULTECH 2011 which was sponsored by the Institute for Systems and Technologies of Information Control and Communication INSTICC and

held in Noordwijkerhout The Netherlands SIMULTECH 2011 was technically co sponsored by the Society for Modeling Simulation International SCS GDR I3 Lionphant Simulation and Simulation Team and held in cooperation with ACM Special Interest Group on Simulation and Modeling ACM SIGSIM and the AIS Special Interest Group of Modeling and Simulation AIS SIGMAS

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