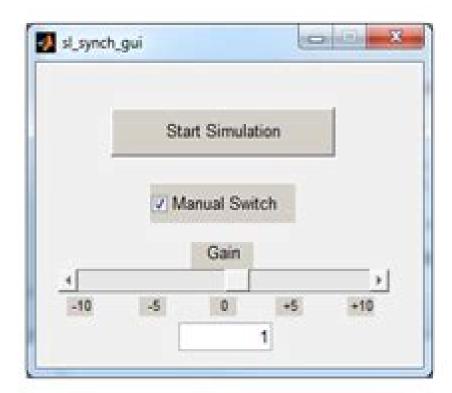
Simulink-GUI Synchronization Example by Will Campbell Copyright 2010 The MathWorks, Inc.

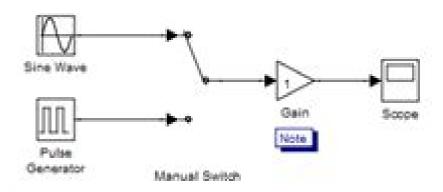
 GUI automatically opened by code in: File → Model Properties → Callbacks → PostLoadFcn

 GUI automatically closed by code in: File → Model Properties → Callbacks → CloseFcn.

3) GUI Start/Stop button synchronized by code in: File → Model Properties → Callbacks → StartFon File → Model Properties → Callbacks → StopFon

 GUI Switch checkbox synchronized by code in: Manual Switch → Block Properties → Calibacks → OpenFcn





Open sl_synch_gui.m to see how the GUI drives the model.

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Saad Motahhir, Badre Bossoufi

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Formal Verification of Simulink/Stateflow Diagrams Naijun Zhan, Shuling Wang, Hengjun Zhao, 2016-11-07 This book presents a state of the art technique for formal verification of continuous time Simulink Stateflow diagrams featuring an expressive hybrid system modelling language a powerful specification logic and deduction based verification approach and some impressive realistic case studies Readers will learn the HCSP HHL based deductive method and the use of corresponding tools for formal verification of Simulink Stateflow diagrams They will also gain some basic ideas about fundamental elements of formal methods such as formal syntax and semantics and especially the common techniques applied in formal modelling and verification of hybrid systems By investigating the successful case studies readers will realize how to apply the pure theory and techniques to real applications and hopefully will be inspired to start to use the proposed approach or even develop their own formal methods in their future work VLSI Design and Test Brajesh Kumar Kaushik, Sudeb Dasgupta, Virendra Singh, 2017-12-21 This book constitutes the refereed proceedings of the 21st International Symposium on VLSI Design and Test VDAT 2017 held in Roorkee India in June July 2017 The 48 full papers presented together with 27 short papers were carefully reviewed and selected from 246 submissions. The papers were organized in topical sections named digital design analog mixed signal VLSI testing devices and technology VLSI architectures emerging technologies and memory system design low power design and test RF circuits architecture and CAD and design verification **Correct Systems** Mike Hinchey, Jonathan P. Bowen, Ernst-Rüdiger Olderog, 2017-03-01 As computers increasingly control the systems and services we depend upon within our daily lives like transport communications and the media ensuring these systems function correctly is of utmost importance This book consists of twelve chapters and one historical account that were presented at a workshop in London in 2015 marking the 25th anniversary of the European ESPRIT Basic Research project ProCoS Provably Correct Systems The ProCoS I and II projects pioneered and accelerated the automation of verification techniques resulting in a wide range of applications within many trades and sectors such as aerospace electronics communications and retail The following topics are covered An historical account of the ProCoS project Hybrid Systems Correctness of Concurrent Algorithms Interfaces and Linking Automatic Verification Run time Assertions Checking Formal and Semi Formal Methods Provably Correct Systems provides researchers designers and engineers with a complete overview of the ProCoS initiative past and present and explores current developments and perspectives within the field Unifying Theories of Programming Jonathan P. Bowen, Huibiao Zhu, 2017-01-10 This book constitutes the refereed proceedings of the 6th International Symposium on Unifying Theories of Programming UTP 2016 held in Reykjavik Iceland in June 2016 in conjunction with the 12th International Conference on Integrated Formal Methods iFM 2016 The 8 revised full papers presented were carefully reviewed and selected from 10 submissions. They deal with the fundamental problem of combination of formal notations and theories of programming that define in various different ways many common notions such as

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First Iberian Robotics Conference Manuel A. Armada, Alberto Sanfeliu, Manuel Ferre, 2013-11-12 This book contains the proceedings of the ROBOT 2013 FIRST IBERIAN ROBOTICS CONFERENCE and it can be said that included both state of the art and more practical presentations dealing with implementation problems support technologies and future applications A growing interest in Assistive Robotics Agricultural Robotics Field Robotics Grasping and Dexterous Manipulation Humanoid Robots Intelligent Systems and Robotics Marine Robotics has been demonstrated by the very relevant number of contributions Moreover ROBOT2013 incorporates a special session on Legal and Ethical Aspects in Robotics that is becoming a topic of key relevance This Conference was held in Madrid 28 29 November 2013 organized by the Sociedad Espa ola para la Investigaci n y Desarrollo en Rob tica SEIDROB and by the Centre for Automation and Robotics CAR Universidad Polit cnica de Madrid UPM and Consejo Superior de Investigaciones Cient ficas CSIC along with the co operation of Grupo Tem tico de Rob tica CEA GTRob Sociedade Portuguesa de Robotica SPR Asociaci n Espa ola de Promoci n de la Investigaci n en Agentes F sicos RedAF and partially supported by Comunidad de Madrid under RoboCity2030 Programme Applications of FPGAs Esteban Tlelo-Cuautle, José de Jesús Rangel-Magdaleno, Luis Gerardo de la Fraga, 2016-05-28 This book offers readers a clear quide to implementing engineering applications with FPGAs from the mathematical description to the hardware synthesis including discussion of VHDL programming and co simulation issues Coverage includes FPGA realizations such as chaos generators that are described from their mathematical models artificial neural networks ANNs to predict chaotic time series for which a discussion of different ANN topologies is included with different learning techniques and activation functions random number generators RNGs that are realized using different chaos generators and discussions of their maximum Lyapunov exponent values and entropies Finally optimized chaotic oscillators are synchronized and realized to implement a secure communication system that processes black and white and grey scale images In each application readers will find VHDL programming guidelines and computer arithmetic issues along with co simulation examples with Active HDL and Simulink The whole book provides a practical guide to implementing a variety of engineering applications from VHDL programming and co simulation issues to FPGA realizations of chaos generators ANNs for chaotic time series prediction RNGs and chaotic secure communications for image transmission **Proceedings of 2013 4th** International Asia Conference on Industrial Engineering and Management Innovation (IEMI2013) Ershi Qi, Jiang Shen, Runliang Dou, 2014-01-16 The purpose of the 4th International Asia Conference on Industrial Engineering and Management Innovation IEMI 2013 is to bring together researchers engineers and practitioners interested in the application of informatics to usher in new advances in the industrial engineering and management fields **Safety and Reliability:** Methodology and Applications Tomasz Nowakowski, Marek Mlynczak, Anna Jodejko-Pietruczuk, Sylwia Werbinska-Wojciechowska, 2014-09-01 Within the last fifty years the performance requirements for technical objects and systems were supplemented with customer expectations quality abilities to prevent the loss of the object properties in

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